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Synchronous Buck

Reference Design

: ACTIVE

PMP482



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	PMP482
Name	Synchronous Buck Reference Design
Status	ACTIVE
Price (US\$)	Free

Product Information

Description

This reference design is a 5Vdc input to 1.65V @ 1A output synchronous buck converter. The controller is the TPS40003 operating at 600KHz. The controller features Predictive Gate Drive, which minimizes the bottom FET body diode conduction time. This provides the benefit of increased efficiency when compared to a standard gate drive method. Very low output ripple voltage and excellent transient load performance are achieved with this design.

Related Products

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Part#	Name	Product Family
TPS40003	Low Input Voltage Mode Synchronous Buck Controller	Switching Regulator

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