### MIC5325 Evaluation Board



# Low V<sub>IN</sub>/V<sub>OUT</sub> 400mA ULDO™ with Ultra-Low IQ

### **General Description**

The MIC5325 is a high performance,  $\mu$ Cap low dropout regulator, offering ultra-low operating current while maintaining very fast transient response. The MIC5325 can source up to 400mA of output current and can regulate down from a low input supply voltage to increase system efficiency.

Ideal for battery operated applications; the MIC5325 offers extremely low dropout voltage 110mV typically @ 400mA, and low ground current at all load conditions (typically 35 $\mu$ A). The MIC5325 can also be put into a zero-off-mode current state, drawing virtually no current when disabled.

The MIC5325 is available in fixed output voltages in the tiny 6-pin 2mm x 2mm thin MLF<sup>®</sup> leadless package.

### Requirements

The MIC5325 evaluation board requires two power supplies. The first supply must be able to provide a minimum of 400mA with a voltage range of 1.7V to 5.5V to the  $V_{\text{IN}}$ . The second supply is required to provide the  $V_{\text{BIAS}}$  current. Although typical  $V_{\text{BIAS}}$  current is 40uA in dropout conditions  $V_{\text{BIAS}}$  current can reach as high as 250mA.  $V_{\text{BIAS}}$  supply should be able to deliver a minimum of 300mA with a voltage range of 2.5V to 5.5V. The output load can be either an active or passive load.

#### **Precautions**

The evaluation board does not have reverse polarity protection. Applying a negative voltage to the  $V_{\text{IN}}$  terminal may damage the device.

#### **Getting Started**

1. Connect an external supply to  $V_{BIAS}$ . Apply desired voltage to  $V_{BIAS}$  (J1) and ground terminal (J2), paying careful attention to polarity and supply voltage (2.5V  $\leq$   $V_{BIAS} \leq$  5.5V). An ammeter may be placed between the Bias supply and  $V_{BIAS}$  terminal to the evaluation board. The evaluation board has a 1 $\mu$ F capacitor connected from  $V_{BIAS}$  (J1) pin to GND (J2).

2. Connect an external supply to  $V_{IN}$ . Apply desired input voltage to the  $V_{IN}$  (J3) and ground terminal (J2) of the evaluation board, paying careful attention to polarity and supply voltage (1.7V  $\leq$   $V_{IN}$   $\leq$   $V_{BIAS}$ ). An ammeter may be placed between the input supply and the  $V_{IN}$  terminal to the evaluation board. Ammeter and/or power lead resistance can reduce the voltage supplied to the input.

Ensure that the supply voltage is monitored at the  $V_{IN}$  terminal. There is  $1\mu F$  capacitor connected from  $V_{IN}$  (J3) to GND (J2).

- 3. **Enable/Disable the MIC5325**. The evaluation board is provided with 10k pull up resistors on enable pin (EN) to V<sub>IN</sub>. To disable an output, simply jumper the EN terminal (J6) to the GND terminal (J2). The enable pin must be either pulled high or low. Removing the pull up resistors and leaving the pins floating will cause the regulators to operate in an indeterminate state.
- 4. Connect the loads to the V<sub>OUT</sub> terminal (J4) and ground terminal (J5). The load can be either a passive (resistor) or active (electronic load). Be sure to monitor the output voltage at the V<sub>OUT</sub> (J4) terminals. The evaluation board has a 1µF capacitor connected from V<sub>OUT</sub> (J4) to GND (J5).
- 5. Bypass Pin. The evaluation board provides 10nF capacitor connected from Bypass pin to GND (J2) to reduce the output voltage noise. The capacitor bypasses the internal reference. The bypass capacitor can be increased, further reducing noise and improving PSRR. Turn-on time increases slightly with respect to bypass capacitance. A unique, quick-start circuit allows the MIC5325 to drive a large capacitor on the bypass pin without significantly slowing turn-on time.

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MLF and MicroLeadFrame are registered trademarks of Amkor Technology, Inc.

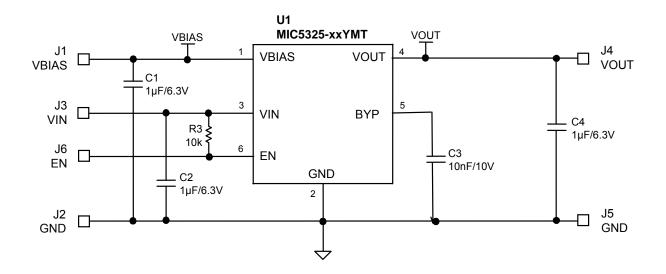
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## **Ordering Information**

| Part Number      | Description                                     |
|------------------|---|
| MIC5325-XXYMT EV | Evaluation board with the 400mA<br>ULDO™ device |

### **Evaluation Board Schematic**



### **Bill of Materials**

| Item       | Part Number        | Manufacturer          | Description                                   | Qty |
|------------|--------------------|-----------------------|---|-----|
| C1, C2, C4 | C1608X5R0J105K     | TDK <sup>(1)</sup>    | Capacitor, 1µF Ceramic, 6.3V, X5R, Size 0603  | 3   |
| C3         | VJ0603Y104KXXAT    | Vishay <sup>(2)</sup> | Capacitor, 0.1µF Ceramic, 10V, X7R, Size 0603 | 1   |
| R3         | CRCW06031002FKEYE3 | Vishay <sup>(2)</sup> | Resistor, 10kΩ, 1%, 1/16W, Size 0603          | 1   |
| U1         | MIC5325-XXYMT      | Micrel <sup>(3)</sup> | UCAP LDO, 400mA, 2mm x 2mm Thin MLF®          | 1   |

#### Notes:

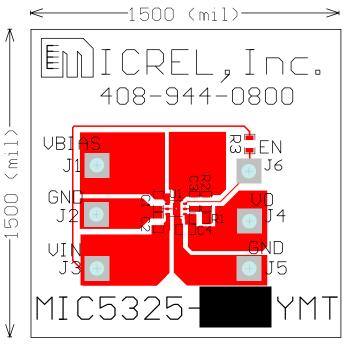
1. TDK: www.tdk.com

2. Vishay: www.vishay.com

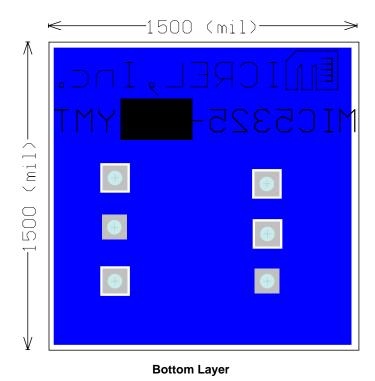
3. Micrel, Inc.: www.micrel.com

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## **PCB Layout Recommendations**



**Top Layer** 



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