

## MIC2285A Evaluation Board

## **High Efficiency 8MHz Step-Down Converter** with LOWQ<sup>®</sup> Mode

## **General Description**

The Micrel MIC2285A is a high efficiency 8MHz PWM synchronous step-down switching regulator that features a LOWQ® LDO standby mode that draws only 20µA of quiescent current. The MIC2285A features an ultra-low noise, small size, and high efficiency solution that allows sub 0.6mm profile for portable power applications.

In PWM mode, the MIC2285A operates at a constant frequency of 8MHz. Under light load conditions, such as in system sleep or standby modes, the PWM switching operation can be disabled to reduce switching losses. In this light load LOWQ® mode, the LDO maintains the output voltage and draws only 20µA of quiescent current. The LDO mode of operation saves battery life while not introducing spurious noise and high ripple as experienced with pulse skipping or bursting mode regulators.

The MIC2285A operates from a 2.7V to 5.5V input and features internal power MOSFETs that can supply up to 600mA of output current in PWM mode. It can operate with a maximum duty cycle of 100% for use in low-dropout conditions

#### Requirements

The MIC2285A evaluation board requires an input power source that is able to deliver greater than 600mA at 2.7V. The output load can either be an active or passive source.

#### **Precautions**

The evaluation board does not have reverse polarity protection. Applying a negative voltage to the V<sub>IN</sub> terminal may damage the device.

In addition, the maximum operating voltage of the MIC2285A evaluation board is 5.5V. Exceeding 6V on the input could damage the device.

#### **Getting Started**

- 1. Connect an external supply to V<sub>IN</sub> terminal. Apply desired input voltage to the V<sub>IN</sub> (J1)and ground (J2 and J5) terminals of the evaluation board, paying careful attention to polarity and supply voltage (2.7V≤V<sub>IN</sub>≤5.5V). An ammeter may be placed between the input supply and the V<sub>IN</sub> terminal to the evaluation board. Be sure to monitor the supply voltage at the  $V_{\mbox{\scriptsize IN}}$ terminal. The ammeter and/or power lead resistance can reduce the voltage supplied to the input.
- 2. Connect the load to the V<sub>OUT</sub> (J4) and ground terminals. The load can be either passive (resistor) or active (electronic load). An ammeter can be placed between the load and the  $V_{\text{OUT}}$  terminal. Be sure to monitor the output voltage at the V<sub>OUT</sub> terminal. The default output voltage is set to 1.8V. This can be adjusted by changing the feedback resistors. (see Output Voltage).
- Disable the MIC2285A. Apply ground, or 0V, to the enable (EN) terminal.

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### **Output Voltage**

The output voltage on the MIC2285A evaluation board is adjustable. The output voltage is controlled by the feedback resistors (R1 and R2) and can be calculated as follows:

$$V_{OUT} = 1.0V \times (\frac{R1}{R2} + 1)$$

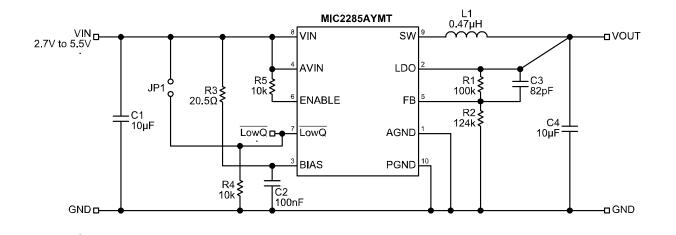
The evaluation board is initially adjusted to 1.8V, but can easily be modified by removing R2 and replacing it with the value that yields the desired output voltage. (Removing R2 sets the output to 1.0V).

$$R2 = \frac{100k\Omega}{\left(\frac{V_{OUT}}{1.0V} - 1\right)}$$

## LOWQ<sup>®</sup>

The MIC2285A is a 600mA PWM power device that utilizes a LOWQ® light load mode to maximize battery efficiency in light load conditions. This is achieved with a  $LOWQ^{\circledR}$  control pin that when pulled low, (Remove JP1), shuts down all the biasing and drive current for the PWM regulator. drawing only 20µA of operating current. This allows the output to be regulated through the LDO output that is capable of providing 60mA of output current. This method has the advantage of producing a clean, low current, ultra low noise output in LOWQ® mode. During LOWQ® mode, the SW node becomes high impedance, blocking current flow. Other methods of reducing quiescent current, such as pulse frequency modulation (PFM) or bursting techniques, create large amplitude, low frequency ripple voltages that can be detrimental to system operation.

When more than 60mA is required, the  $LOWQ^{\otimes}$  pin can be forced high, (Connect JP1), causing the MIC2285A to enter PWM mode. In this case, the LDO output makes a "hand-off" to the PWM regulator virtually without any variation in output voltage. The LDO output then turns off allowing up to 600mA of current to be efficiently supplied through the PWM output to the load.



# **Bill of Materials**

Ref Des	Part Number	Manufacturer	Description	Qty	
C1, C4	C1608X5R0J106K	TDK <sup>(1)</sup>	10µF 6.3V X5R 0603 Ceramic Capacitor	2	
	06036D106MAT2A	AVX <sup>(2)</sup>	TOUR 6.5V ASK 0003 Ceramic Capacitor		
	GRM188R60J106M	MuRata <sup>(3)</sup>	10µF 6.3V X7R 0603 Ceramic Capacitor		
C2	C1005X5R0J104M	TDK <sup>(1)</sup>	0.1µF 6.3V X5R 0402 Ceramic Capacitor	1	
	04026D104MAT2A	AVX <sup>(2)</sup>	0. THE 0.3V ASK 0402 Ceramic Capacitor		
	GRM155R60J104K	MuRata <sup>(3)</sup>	0.1µF 6.3V X7R 0402 Ceramic Capacitor		
СЗ	VJ0402A820KXQCW1BC	Vishay <sup>(4)</sup>	82pF X7R 0402 Ceramic Capacitor	1	
	C1005COG1H820J	TDK <sup>(1)</sup>	82pF COG 0402 Ceramic Capacitor	, '	
L1	DO2010-501ML	Coilcraft <sup>(5)</sup>	0.5µH Inductor	1	
	LQM21PNR47M00	MuRata <sup>(3)</sup>	0.47µH Inductor	ı	
R1	CRCW04021003F	Vishay <sup>(4)</sup>	100kΩ 1% 0402 Resistor	1	
R2	CRCW04021243F	Vishay <sup>(4)</sup>	124kΩ 1% 0402 Resistor	1	
R3	CRCW040220R5F	Vishay <sup>(4)</sup>	20.5Ω 1% 0402 Resistor	1	
R4	CRCW04021002F	Vishay <sup>(4)</sup>	10kΩ 1% 0402 Resistor	1	
R5	CRCW04021002F	Vishay <sup>(4)</sup>	10kΩ 1% 0402 Resistor	1	
U1	MIC2285AYMT	Micrel <sup>(6)</sup>	8MHz PWM Step-Down Converter/LDO	1	

#### Notes:

1. TDK: www.tdk.com

2. AVX: www.avx.com

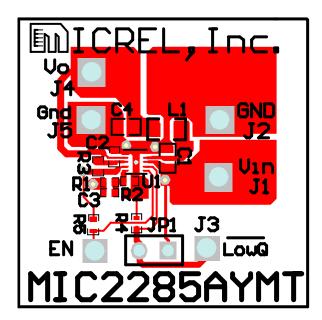
3. Murata: <u>www.murata.com</u>

4. Vishay: www.vishay.com

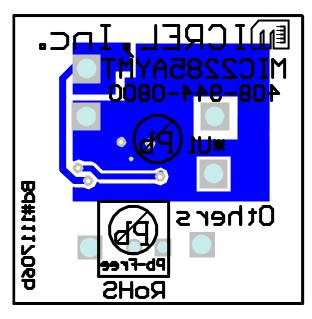
5. Coilcraft: www.coilcraft.com

6. Micrel, Inc.: www.micrel.com

# **Printed Circuit Board Layouts**



Тор



**Bottom** 

Micrel, Inc.	MIC2285A Evaluation Board

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