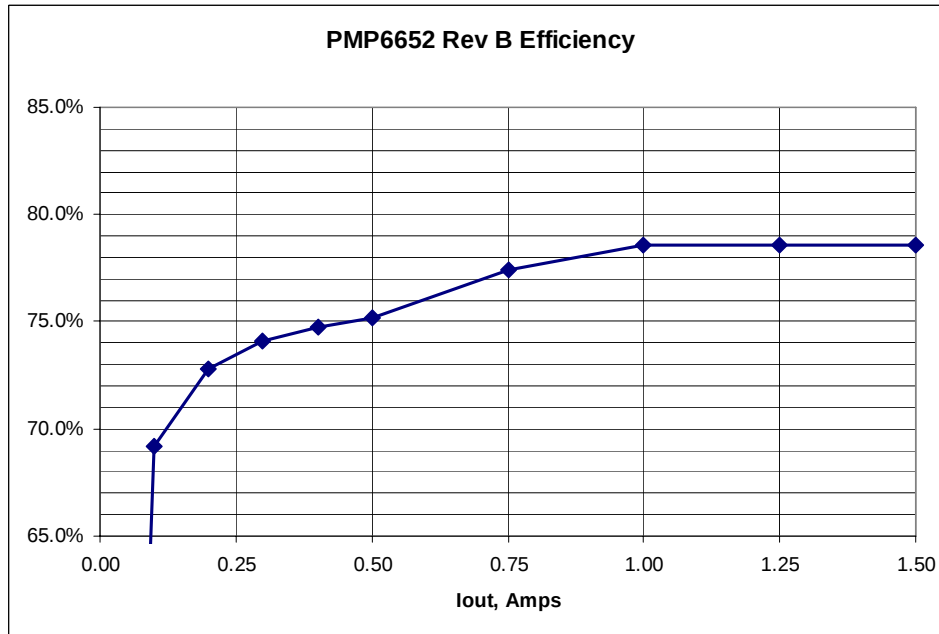


Efficiency

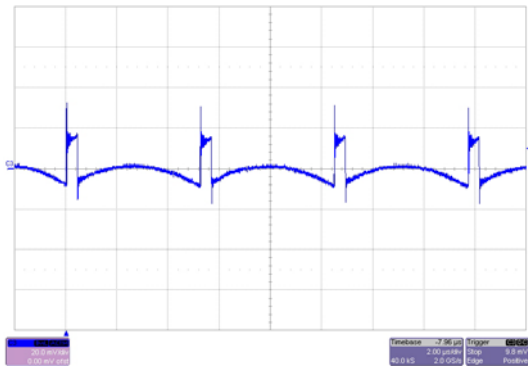
The efficiency is shown below:



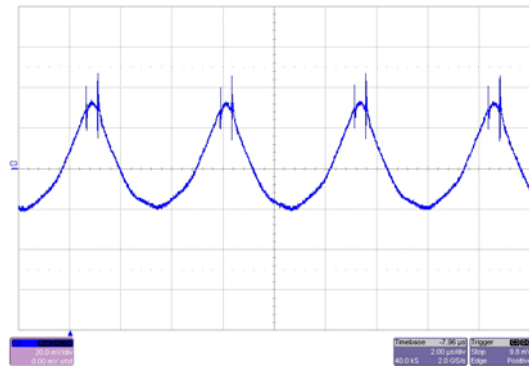
Ripple and Noise

Ripple measurements taken with a 48V input, 1.5A load, and 20MHz BWL.

Output Ripple (J2), 20mV/div:

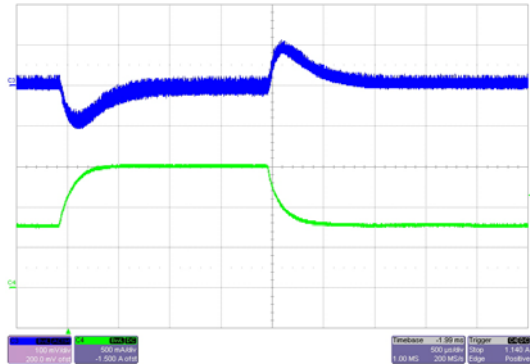


Input Ripple (D41), 20mV/div:



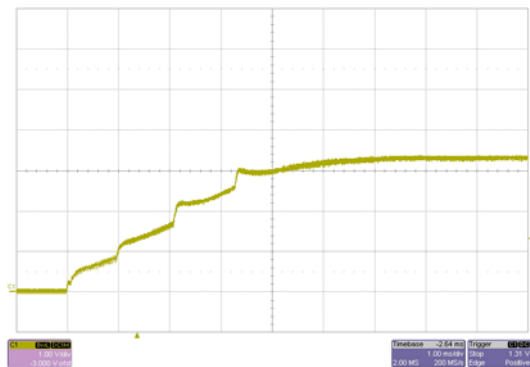
Dynamic Loading

Load Step, 48V input
100mV/div, 500usec/div:

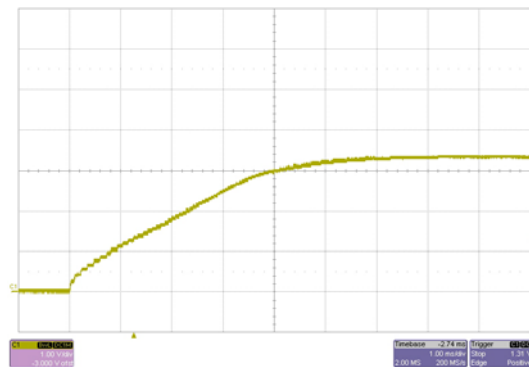


Turn On Response

48VIN, 1.5A Load, 1msec/div:

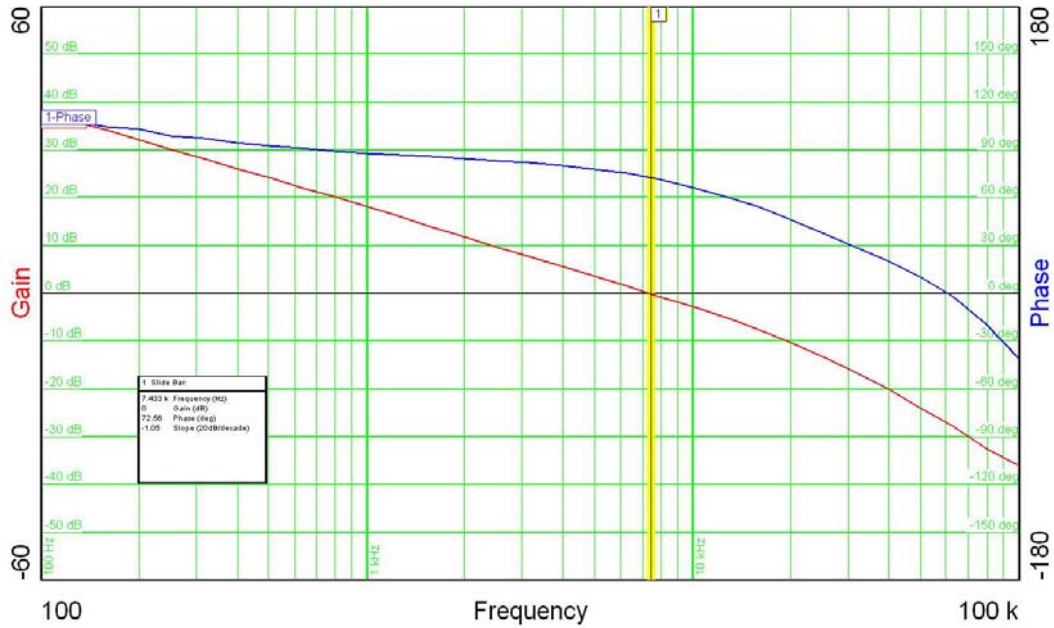


48VIN, 0A Load, 1msec.div:



Stability

The measured Bode plot of the converter with a 48V input and 1.5A load is shown below. Bandwidth is 7.4KHz; Phase Margin is 72 degrees; Gain Margin is 20 dB.



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