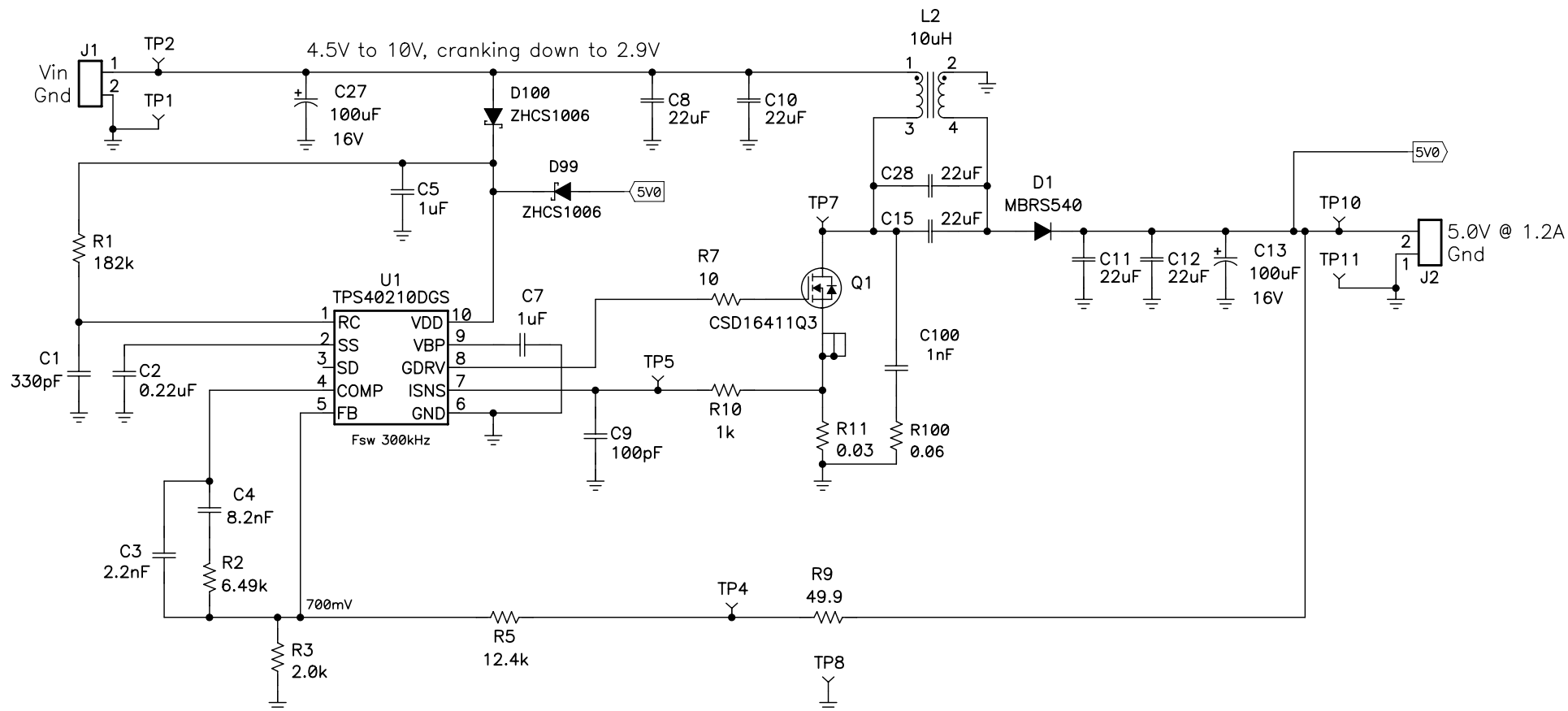




Assembly Instructions:

- 1) L1 OPEN, L3 OPEN, L4 SHORT
- 2) Q2 OPEN
- 3) C5, C13, C15, C27, C28, R11 on different pattern
- 4) R4 OPEN, R12 OPEN, R13 OPEN, R14 SHORT, R15 SHORT
- 5) C6 OPEN, C14 OPEN
- 6) D2 OPEN
- 7) D99, R100, C100 placed externally
- 8) Replace R6 by D100 for Vdd ORing

NOTES:

- 1) built on universal board PMP2773RevB
- 2) R9 for test purposes only
- 3) quiescent current in shutdown SD max. 20uA
- 4) D99, D100 only for cranking Vin < 4.5V, here 2.9Vmin
- 5) max. height 6mm: C13, C27, L2

Title			TPS40210, Sepic, 5V @ 1.2A		
Size	Number		Rev		
B	PMP5540		B		
Date	07/13/2010		Drawn by	B.Geck	
Filename	PMP5540RevB.SCH		Sheet	1 of 1	

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