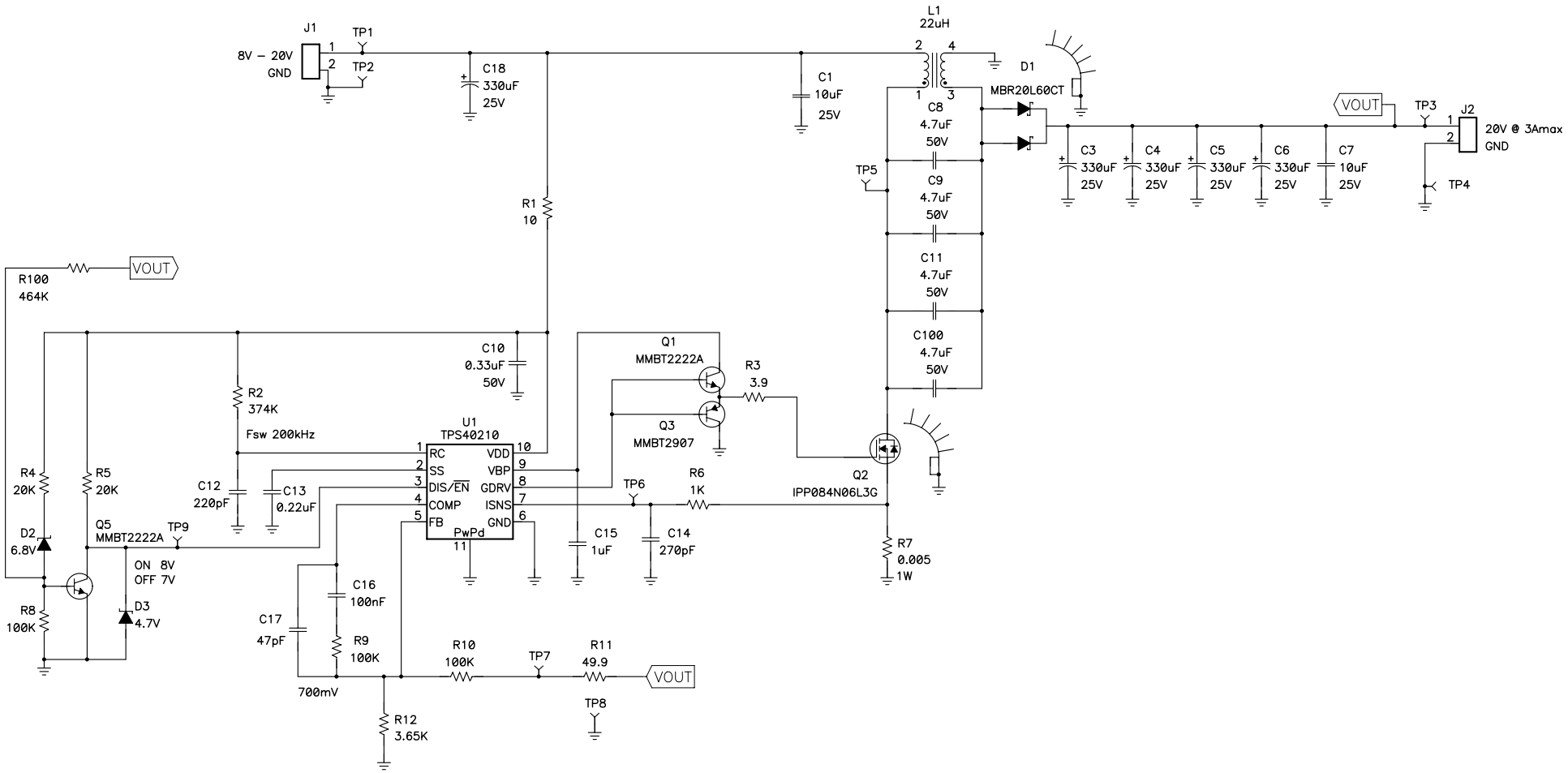

Rev B:
1) added R100 externally for hysteresis at UVLO
2) adjusted E/A
3) increased sepic caps

NOTES:
1) use PCB PMP5197 REVA
2) R11 for test purposes only

Texas Instruments

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Size C	Number PMP4700		Rev B
Date	12/10/2009	Drawn by	B.Geck
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