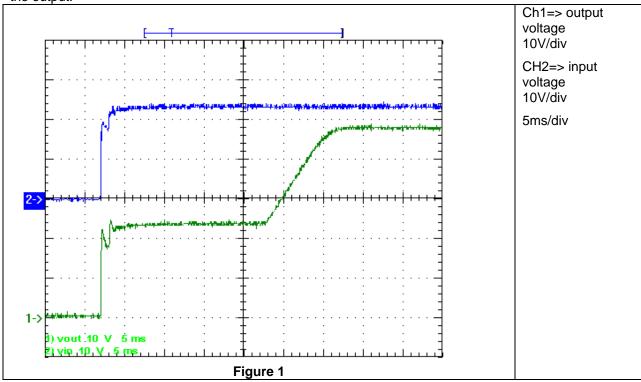
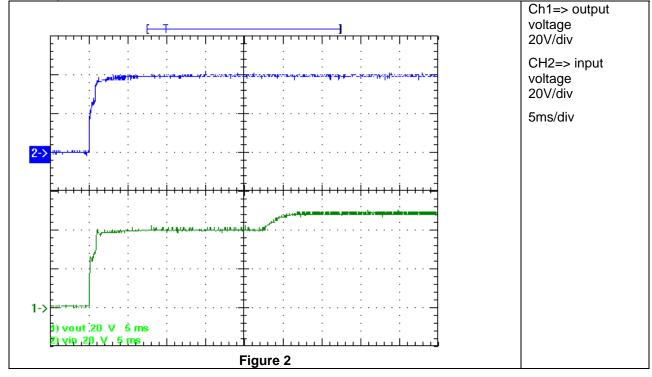


### 1 Startup

The startup waveform is shown in the Figure 1. The input voltage was set at 24V, with 1A load on the output.



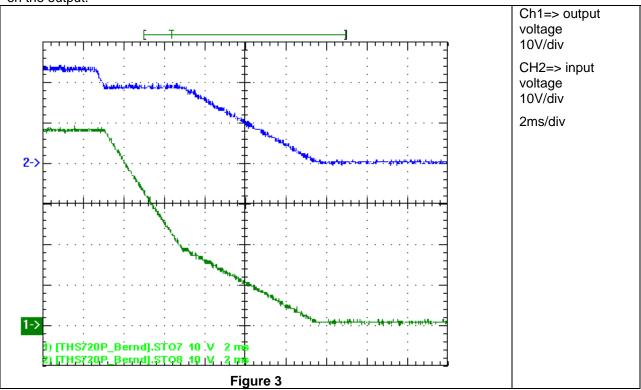
The startup waveform is shown in the Figure 2. The input voltage was set at 40V, with 1A load on the output.



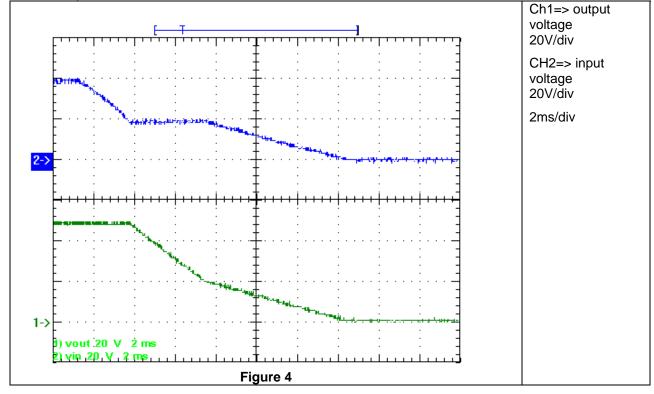


#### 2 Shutdown

The shutdown waveform is shown in the Figure 3. The input voltage was set at 24V, with 1A load on the output.



The shutdown waveform is shown in the Figure 4. The input voltage was set at 40V, with 1A load on the output.





# 3 Efficiency

The efficiency is shown in the Figure 5 below.

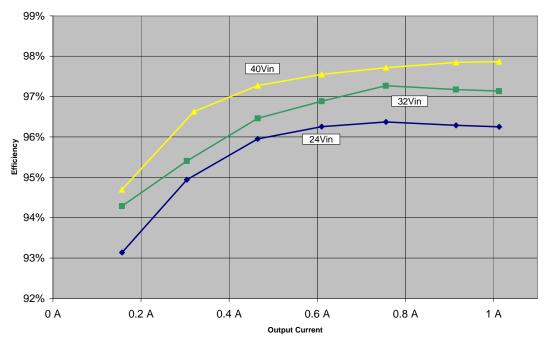


Figure 5

# 3 Load Regulation

The load regulation of the output is shown in the Figure 6 below.

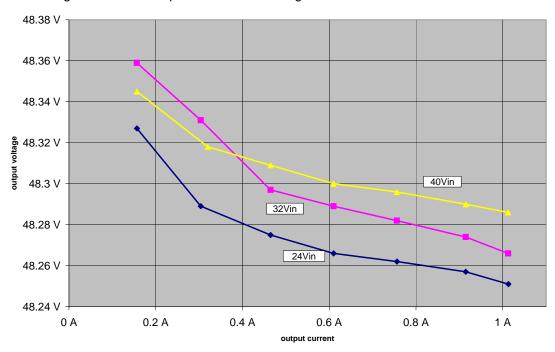


Figure 6



## 4 Input regulation

Figure 7shows the output voltage dependency to the input voltage. Output current was set to 1A.

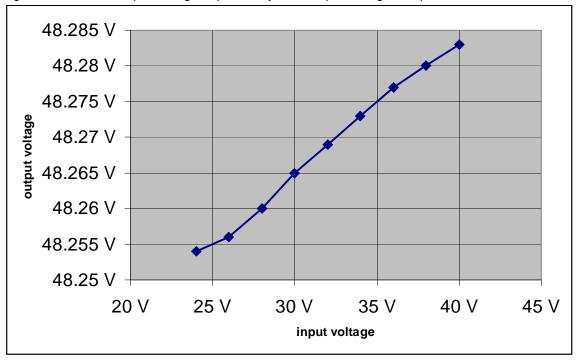
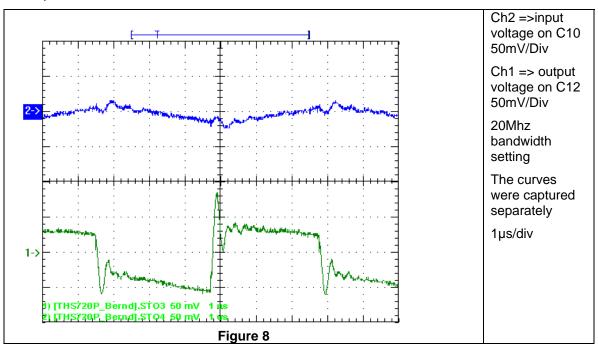


Figure 7

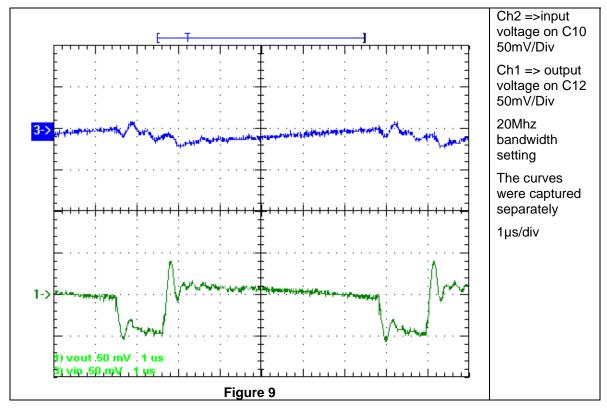


### 5 Ripple Voltage

The output ripple voltage is shown in Figure 8. The image was taken with a 1 A load and 24V at the input.



The output ripple voltage is shown in Figure 9. The image was taken with a 1 A load and 40V at the input.





# **6 Control Loop Frequency Response**

Figure 10 shows the loop response with different input voltages.

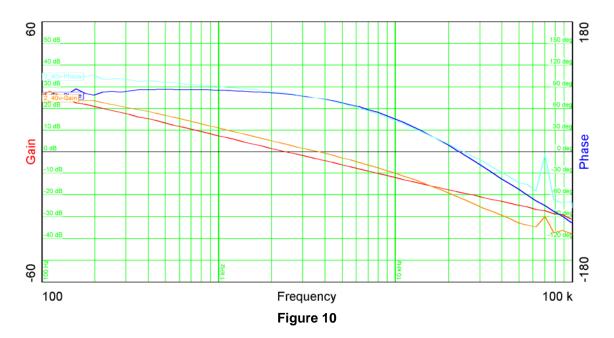


Table 1 summarizes the results from Figure 10

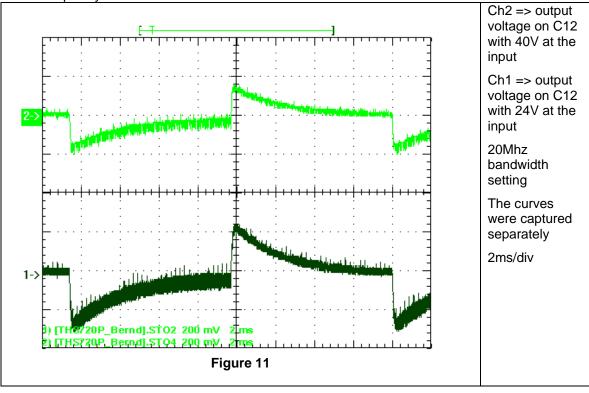
	24Vin	40Vin
Bandwidth (kHz)	2.39	3.63
Phasemargin	80.2°	74.1°
slope (20dB/decade)	-0.978	-0.989
gain margin (dB) slope	-18.75	-21.77
(20dB/decade)	-0.954	-1.73
freq (kHz)	23	23.9
Table 1		





#### 7 Load Transients

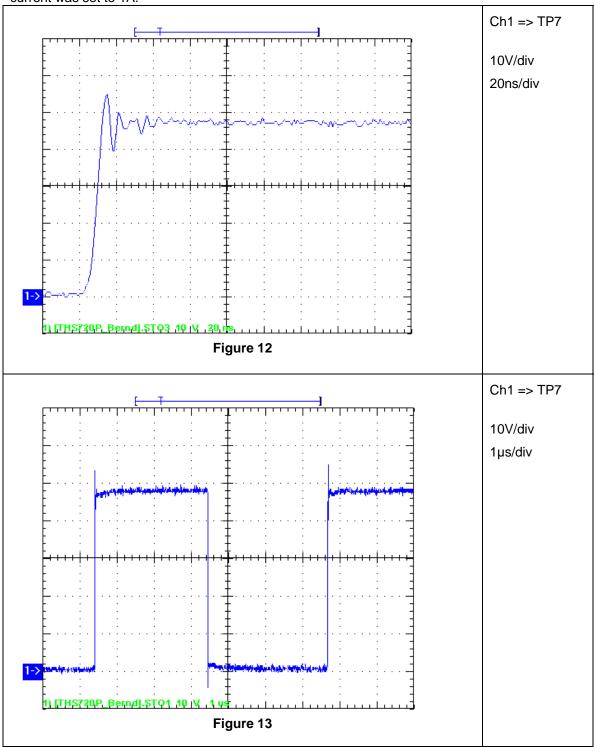
The Figure 11shows the response to load transients. The load is switching from 0.5A to 1A.with 60Hz frequency





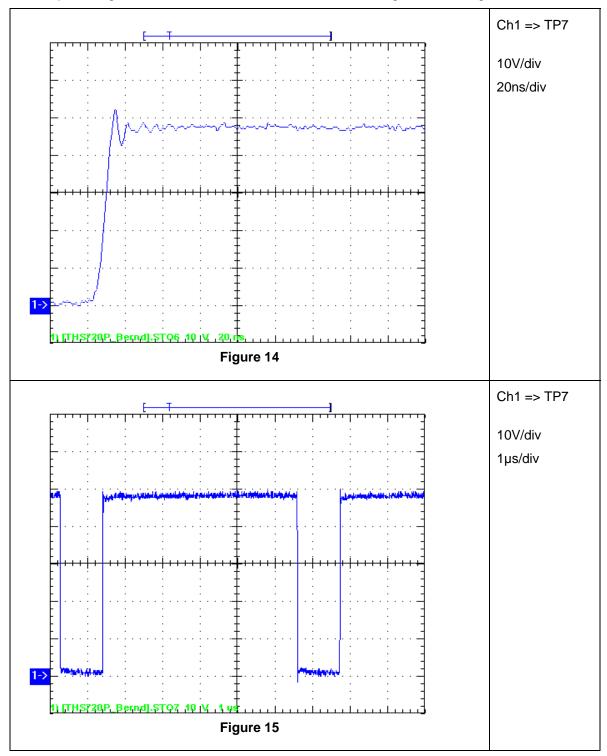
### 8 Switch Node Waveform

With input voltage set to 24V result in the waveform shown in Figure 12 and Figure 13. Output current was set to 1A.





With input voltage set to 40V result in the waveform shown in Figure 14 and Figure 15



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