

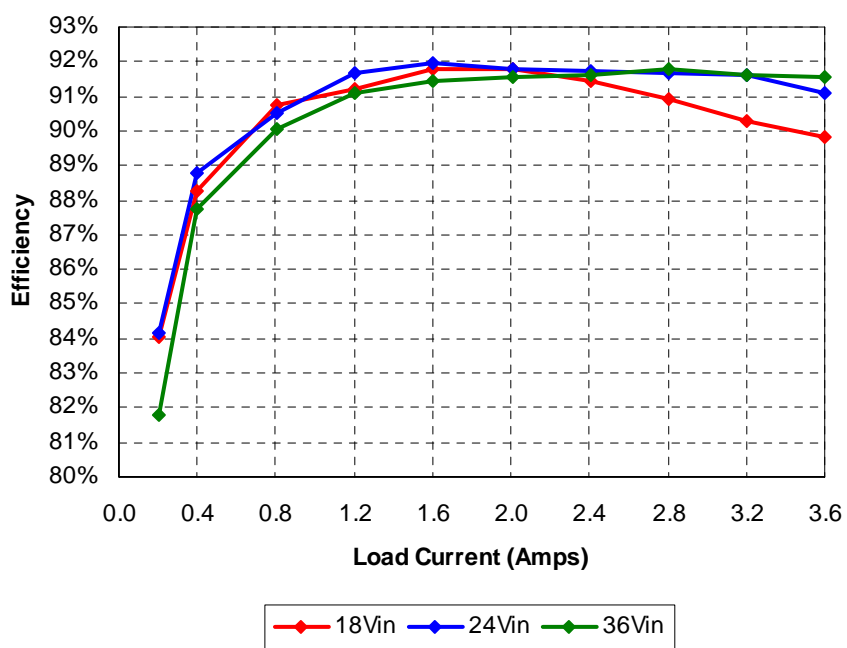
## 1 Photo

The photograph below shows the top view of the PMP6943 Rev A demo board. The circuit is built on a PMP4963 Rev C PWB.



## 2 Efficiency

The efficiency data is shown in the tables and graphs below.



Iout	Vout	Vin	Iin	Pout	Losses	Efficiency
0.000	27.19	18.0	0.018	0.00	0.324	0.0%
0.203	27.23	18.0	0.366	5.53	1.049	84.0%
0.402	27.23	18.0	0.689	10.95	1.456	88.3%
0.803	27.23	18.0	1.339	21.87	2.236	90.7%
1.202	27.23	18.0	1.993	32.73	3.144	91.2%
1.601	27.23	18.0	2.638	43.60	3.889	91.8%
2.004	27.23	18.0	3.302	54.57	4.867	91.8%
2.403	27.23	18.0	3.975	65.43	6.116	91.5%
2.803	27.24	18.0	4.665	76.35	7.616	90.9%
3.203	27.24	18.0	5.369	87.25	9.392	90.3%
3.603	27.25	18.0	6.074	98.18	11.150	89.8%
Iout	Vout	Vin	Iin	Pout	Losses	Efficiency
0.000	27.21	24.0	0.017	0.00	0.408	0.0%
0.203	27.26	24.0	0.274	5.53	1.042	84.2%
0.402	27.25	24.0	0.514	10.95	1.382	88.8%
0.803	27.25	24.0	1.007	21.88	2.286	90.5%
1.202	27.25	24.0	1.489	32.75	2.982	91.7%
1.601	27.25	24.0	1.977	43.63	3.821	91.9%
2.004	27.25	24.0	2.479	54.61	4.887	91.8%
2.403	27.25	24.0	2.975	65.48	5.918	91.7%
2.803	27.25	24.0	3.471	76.38	6.922	91.7%
3.203	27.25	24.0	3.970	87.28	7.998	91.6%
3.603	27.26	24.0	4.492	98.22	9.590	91.1%
Iout	Vout	Vin	Iin	Pout	Losses	Efficiency
0.000	27.21	36.0	0.014	0.00	0.504	0.0%
0.203	27.26	36.0	0.188	5.53	1.234	81.8%
0.402	27.26	36.0	0.347	10.96	1.533	87.7%
0.803	27.26	36.0	0.675	21.89	2.410	90.1%
1.202	27.25	36.0	0.999	32.75	3.210	91.1%
1.601	27.25	36.0	1.325	43.63	4.073	91.5%
2.004	27.25	36.0	1.657	54.61	5.043	91.5%
2.403	27.25	36.0	1.985	65.48	5.978	91.6%
2.803	27.25	36.0	2.311	76.38	6.814	91.8%
3.203	27.25	36.0	2.646	87.28	7.974	91.6%
3.603	27.26	36.0	2.979	98.22	9.026	91.6%

### 3 Thermal Images

The thermal images below show a top view of the board, with an ambient temperature of 25°C, with no forced air flow. The output was loaded with 2.5A. Sustained loads over 2.5A require forced air cooling.

#### 3.1 18V Input

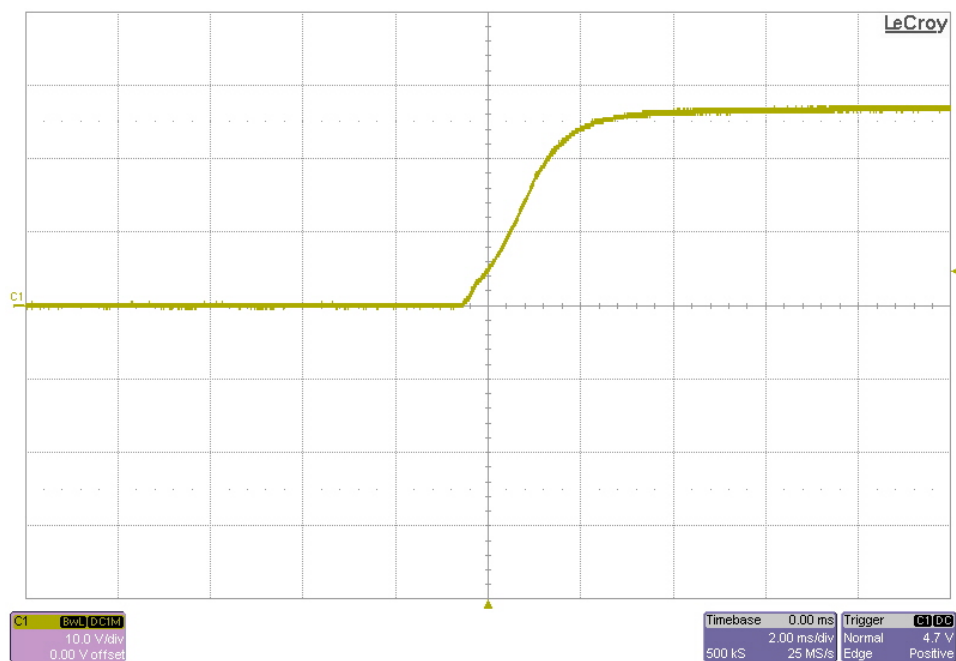


### 3.2 36V Input

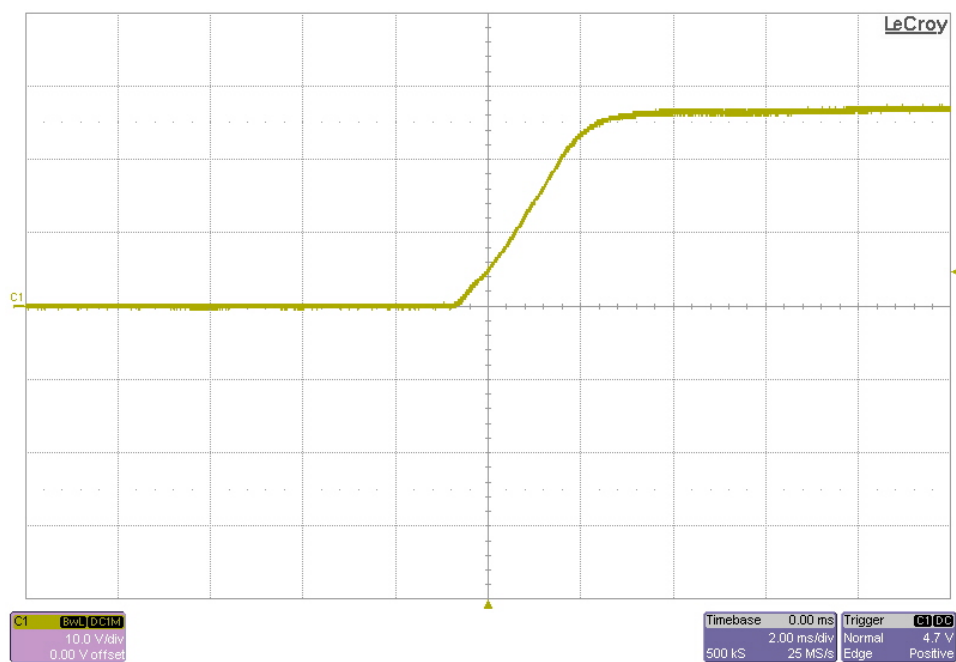


## 4 Startup

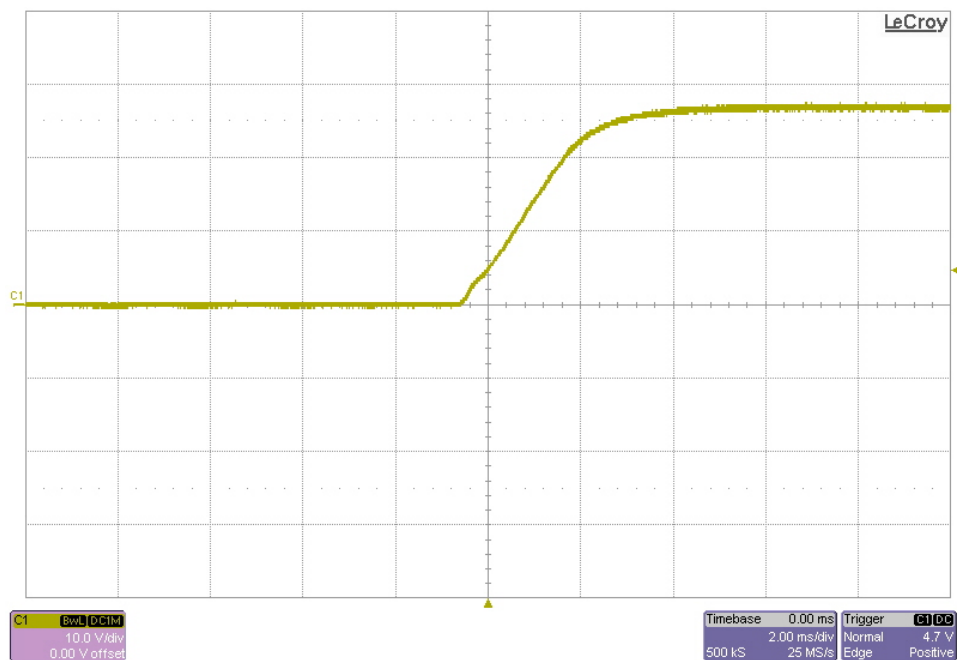
### 4.1 18V Input – No Load



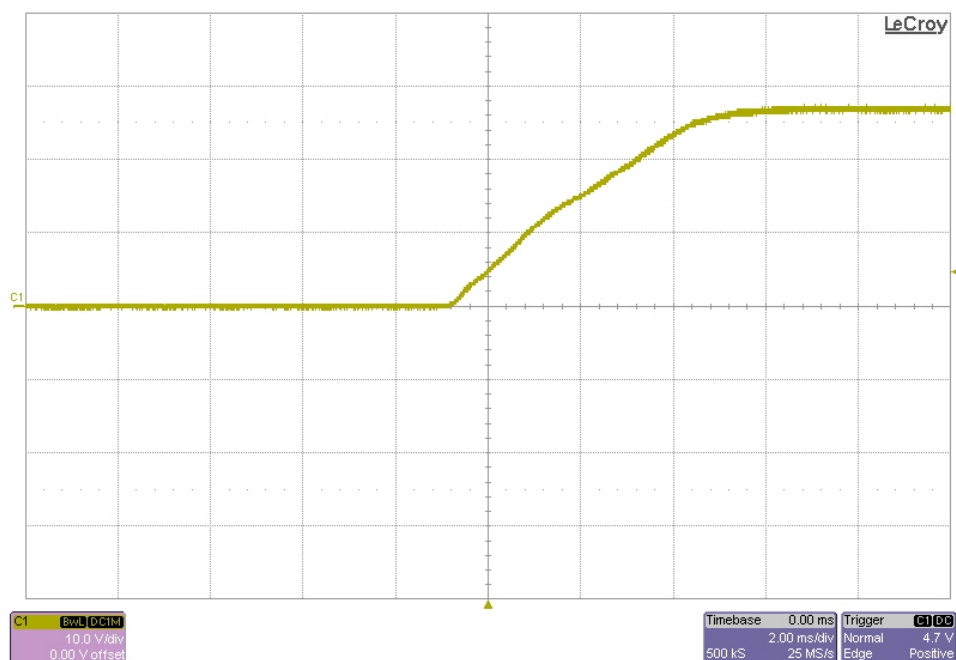
## 4.2 36V Input – No Load



## 4.3 18V Input – 3.6A Load



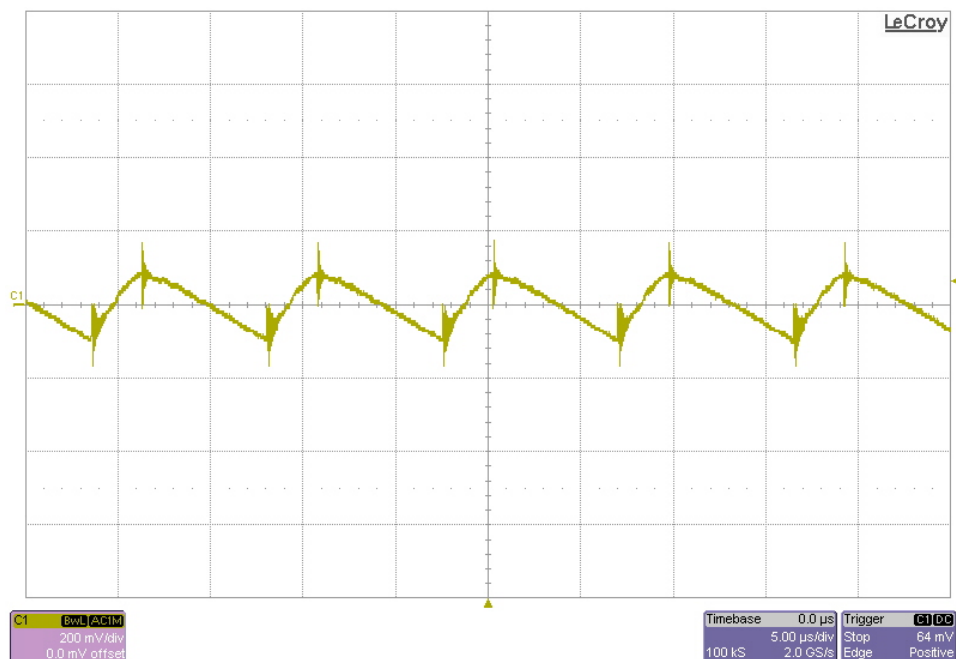
#### 4.4 36V Input – 3.6A Load



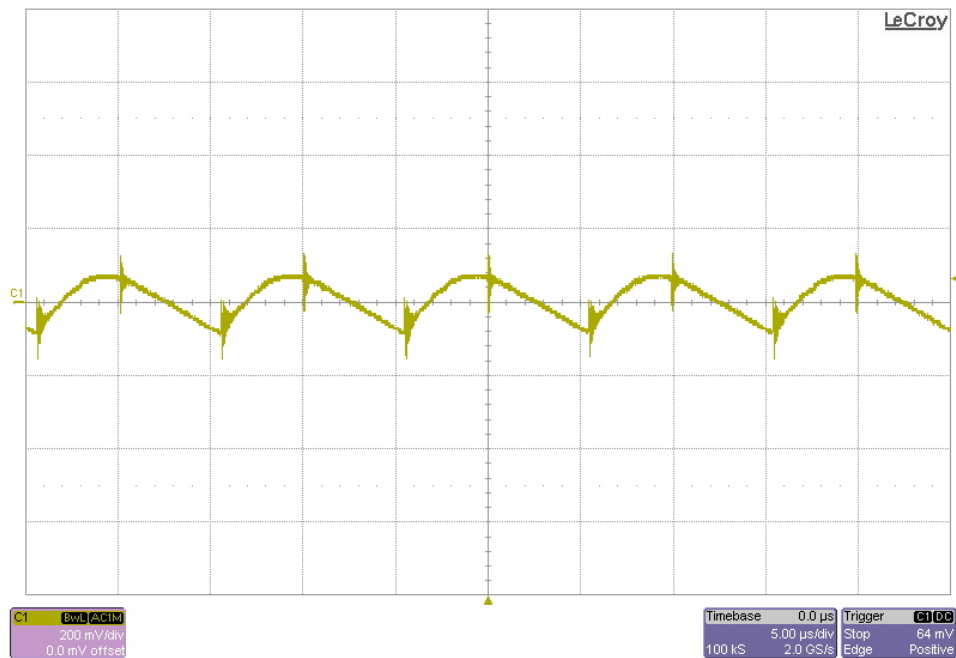
### 5 Output Ripple Voltage

The output ripple voltage is shown in the plots below. The output was loaded with 3.6A.

#### 5.1 18V Input

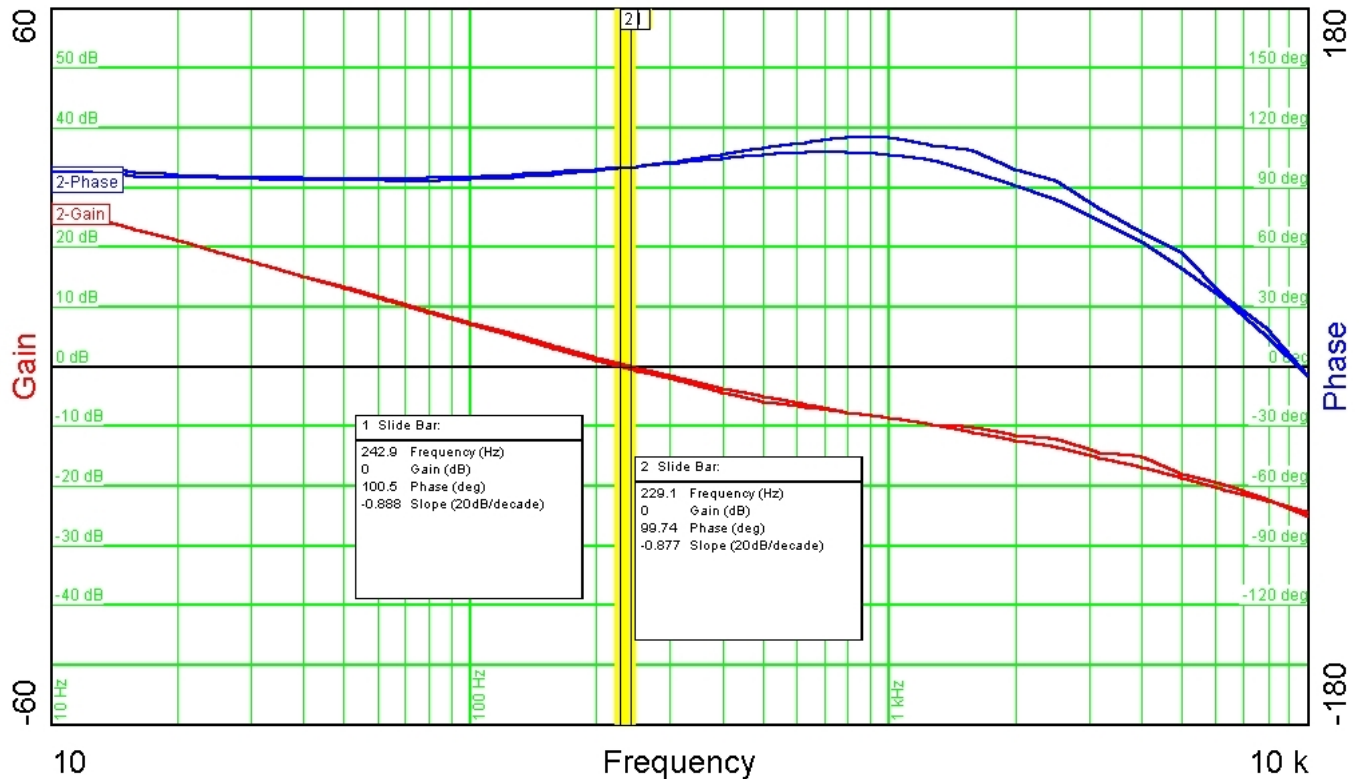


## 5.2 36V Input



## 6 Frequency Response

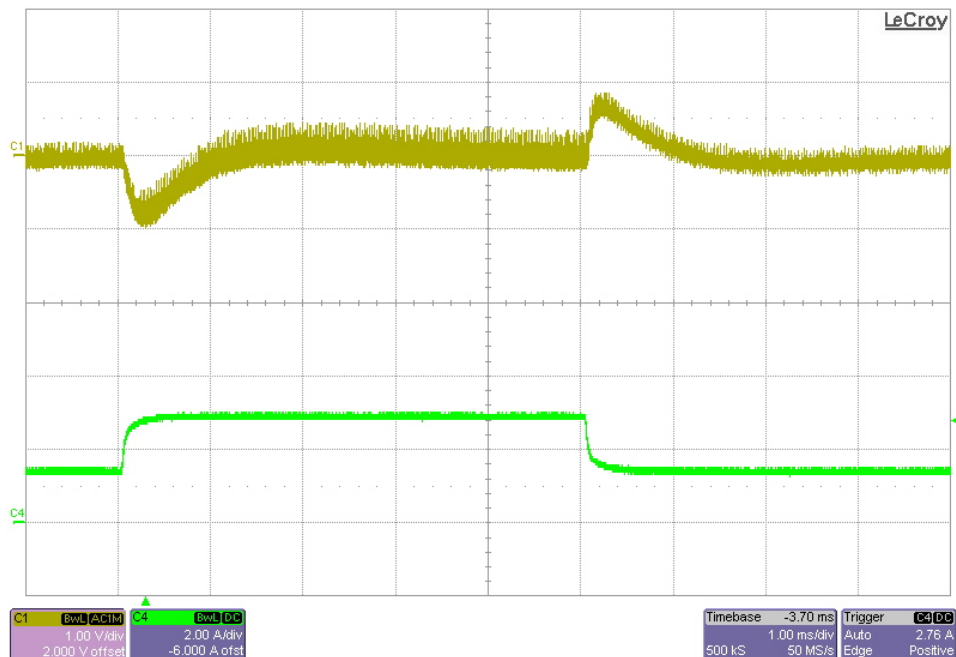
The frequency response of the feedback loop is shown below. For the gain/phase plot #1, the input was set to 18V. For the gain/phase plot #2, the input was set to 36V. The output was loaded with 3.6A.



## 7 Load Transients

The response to a load step from 1.5A to 3A is shown in the images below. Channel 1: Vout (ac coupled); Channel 4: Iout.

### 7.1 18V Input



### 7.2 24V Input

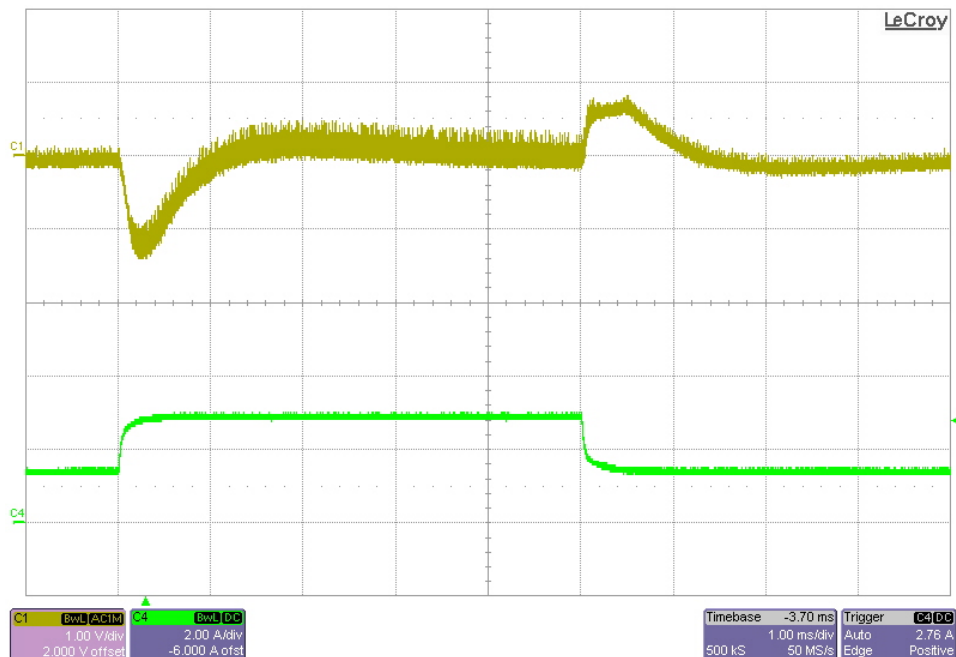
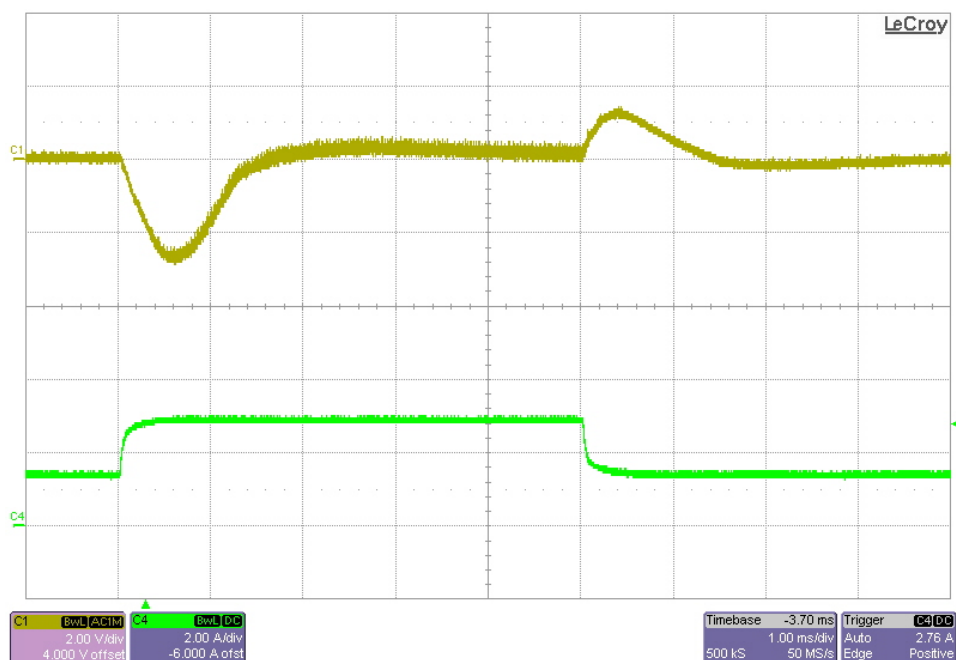


Figure 1



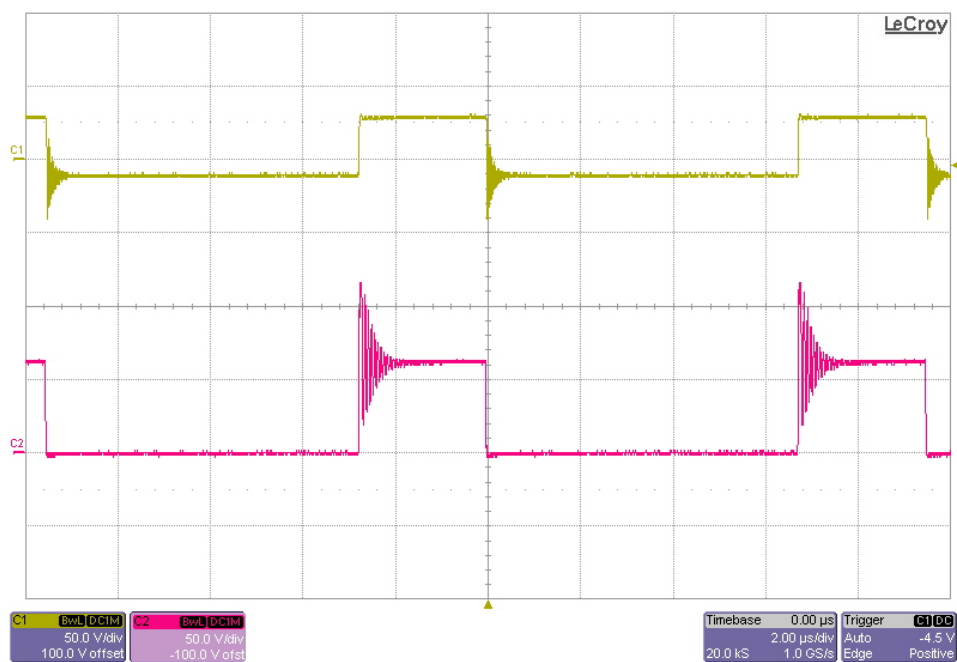
### 7.3 36V Input



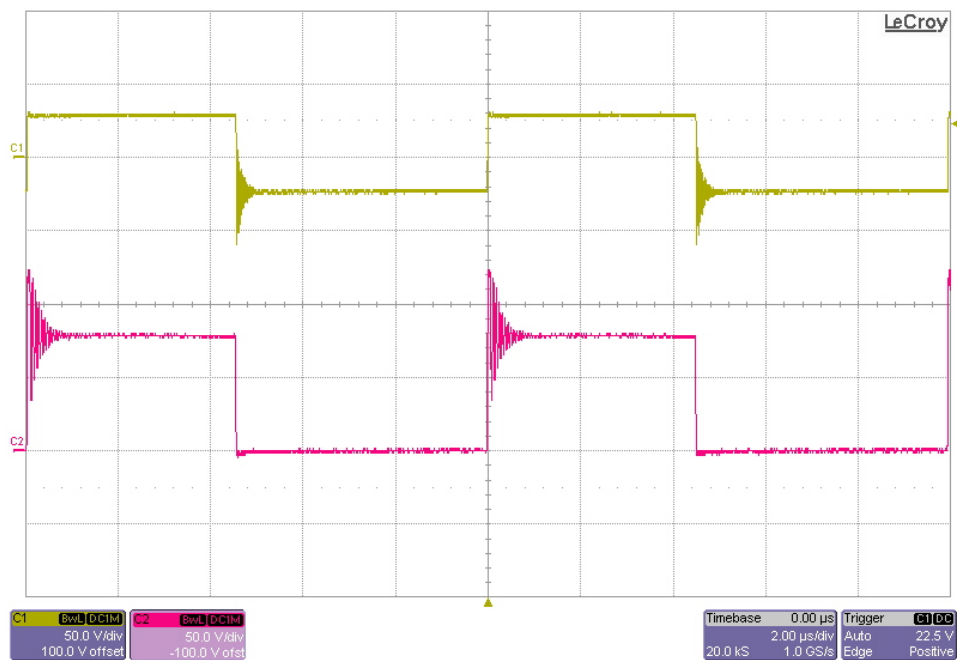
## 8 Switching Waveforms

The images below show the drain-to-source voltage waveforms on the switching MOSFETs. The output was loaded with 3.6A. Channel 1: D3 anode; Channel 2: Q1 & Q2 Vds.

### 8.1 18V Input





**8.2 36V Input**

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