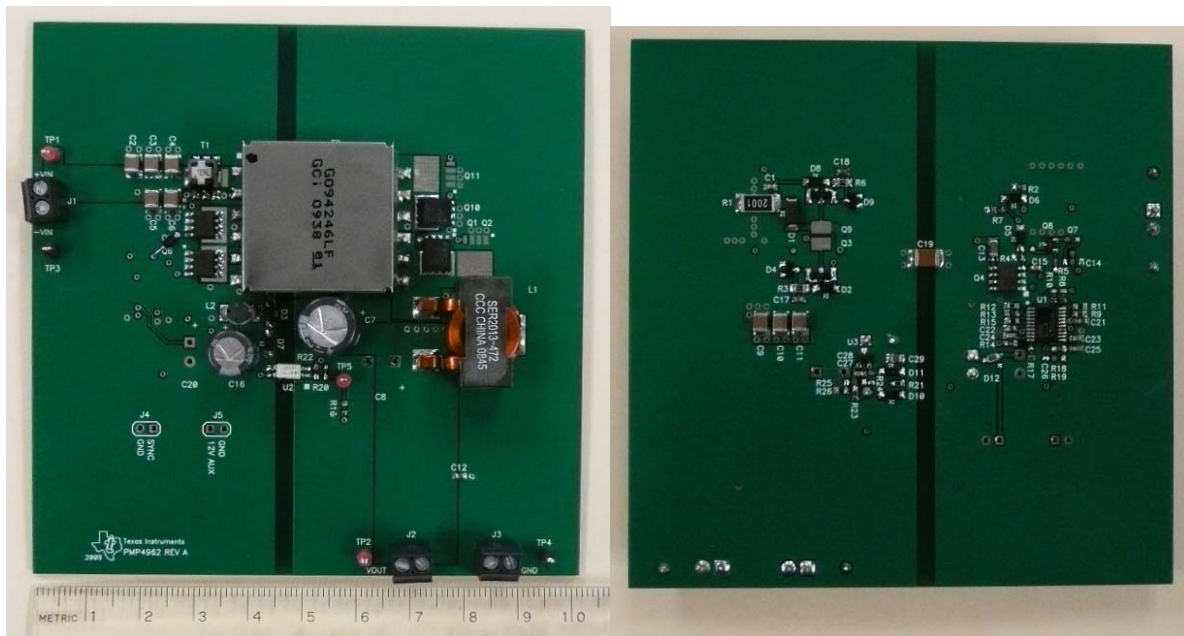


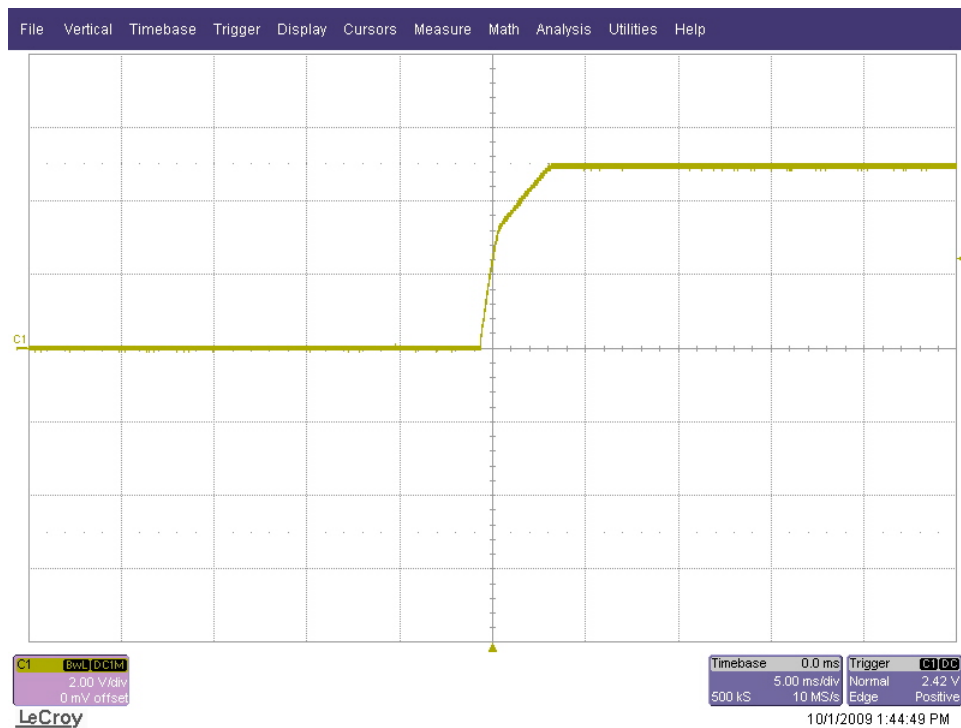
## 1 Photo

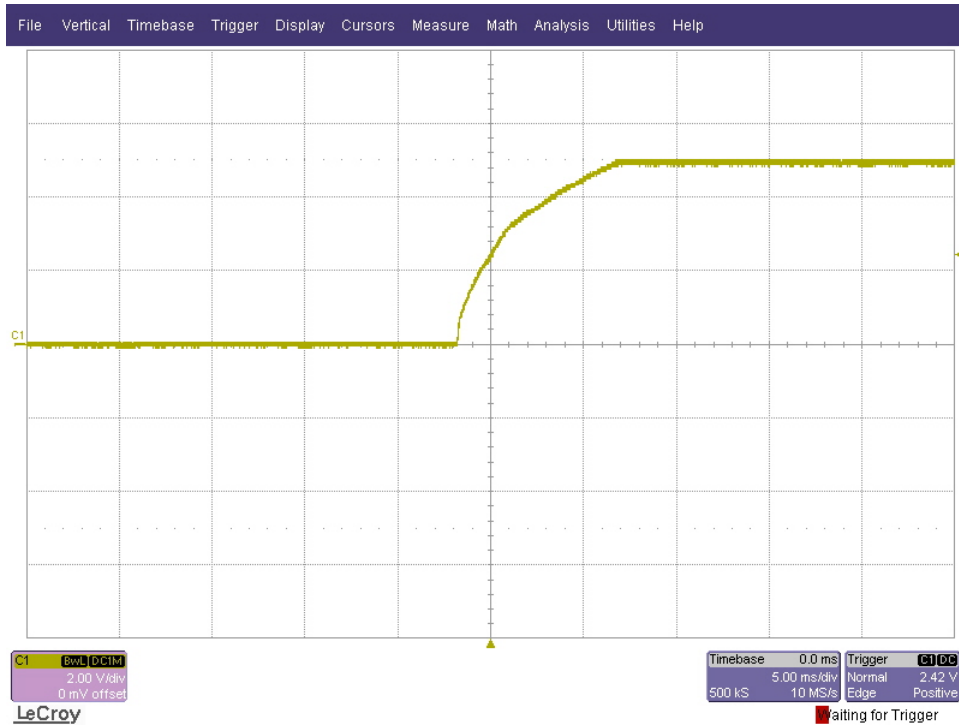
The photographs below show a top and bottom view of the PMP5117 Rev B demo board. The circuit is built on a PMP4962 Rev A PWB.



## 2 Startup

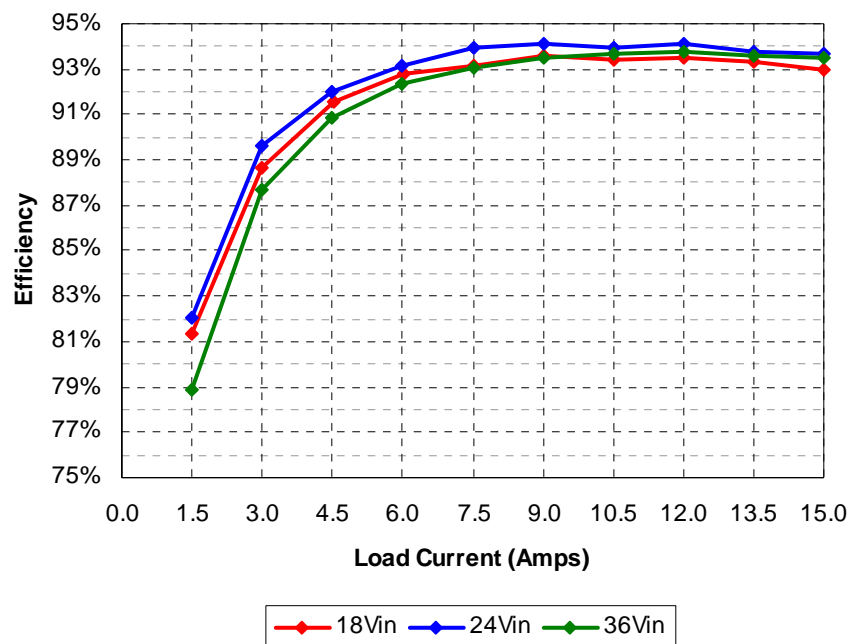
The output voltage at startup is shown in the images below. The input was 24VDC. For the top image the output was unloaded. For the bottom image, the output was loaded with 15A.





### 3 Efficiency

The efficiency data is shown in the tables and graph below.



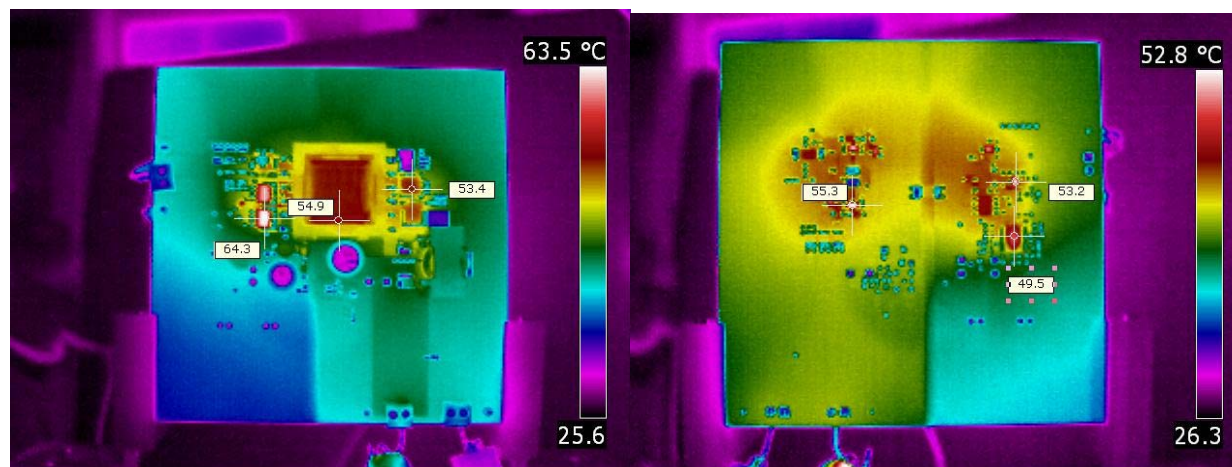
Vin	Iin	Iout	Vout	Pout	Losses	Efficiency
18.0	0.100	0.000	5.00	0.00	1.800	0.0%
18.0	0.515	1.508	5.00	7.54	1.730	81.3%
18.0	0.941	3.004	5.00	15.02	1.918	88.7%
18.0	1.371	4.518	5.00	22.59	2.088	91.5%
18.0	1.801	6.014	5.00	30.07	2.348	92.8%
18.0	2.237	7.50	5.00	37.50	2.766	93.1%
18.0	2.671	9.00	5.00	45.00	3.078	93.6%
18.0	3.123	10.50	5.00	52.50	3.714	93.4%
18.0	3.563	11.99	5.00	59.95	4.184	93.5%
18.0	4.016	13.49	5.00	67.45	4.838	93.3%
18.0	4.48	15.00	5.00	75.00	5.640	93.0%

Vin	Iin	Iout	Vout	Pout	Losses	Efficiency
24.0	0.069	0.000	5.00	0.00	1.656	0.0%
24.0	0.380	1.496	5.00	7.48	1.640	82.0%
24.0	0.698	3.003	5.00	15.02	1.737	89.6%
24.0	1.019	4.499	5.00	22.50	1.961	92.0%
24.0	1.343	6.004	5.00	30.02	2.212	93.1%
24.0	1.665	7.51	5.00	37.55	2.410	94.0%
24.0	1.993	9.00	5.00	45.00	2.832	94.1%
24.0	2.328	10.50	5.00	52.50	3.372	94.0%
24.0	2.656	12.00	5.00	60.00	3.744	94.1%
24.0	2.996	13.49	5.00	67.45	4.454	93.8%
24.0	3.333	14.99	5.00	74.95	5.042	93.7%

Vin	Iin	Iout	Vout	Pout	Losses	Efficiency
36.0	0.056	0.000	5.01	0.00	2.016	0.0%
36.0	0.264	1.499	5.00	7.50	2.009	78.9%
36.0	0.475	2.999	5.00	15.00	2.105	87.7%
36.0	0.688	4.502	5.00	22.51	2.258	90.9%
36.0	0.903	6.003	5.00	30.02	2.493	92.3%
36.0	1.119	7.50	5.00	37.50	2.784	93.1%
36.0	1.337	9.00	5.00	45.00	3.132	93.5%
36.0	1.557	10.50	5.00	52.50	3.552	93.7%
36.0	1.778	12.00	5.00	60.00	4.008	93.7%
36.0	2.002	13.49	5.00	67.45	4.622	93.6%
36.0	2.228	15.00	5.00	75.00	5.208	93.5%

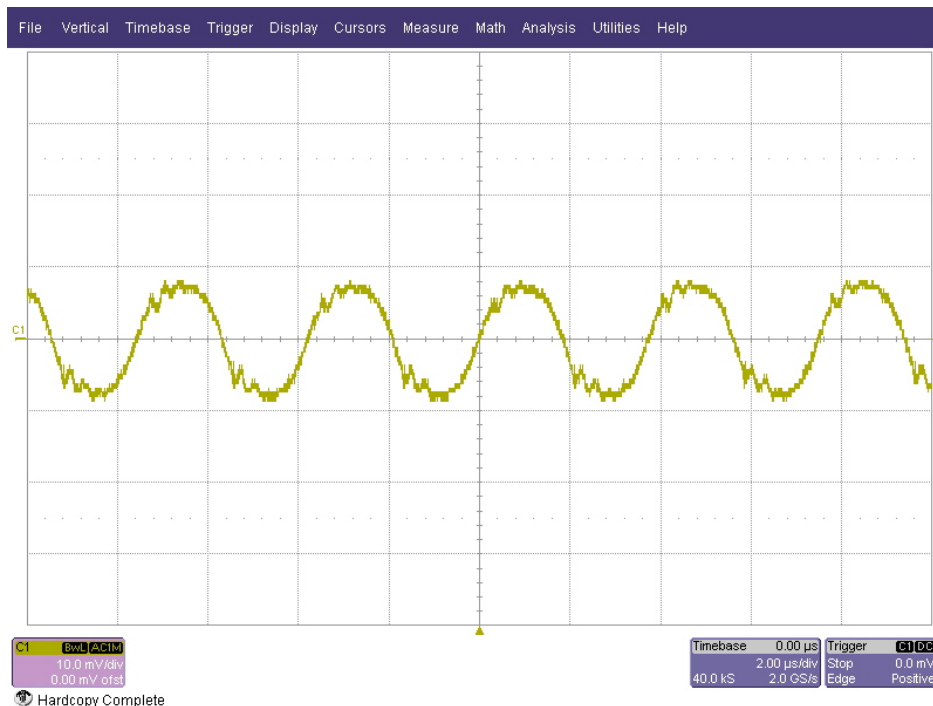
## 4 Thermal Images

The thermal images below show a top view (left) and bottom view (right) of the board. The ambient temperature was 26°C with no forced air flow. The output was load with 15A. The input was 24V.



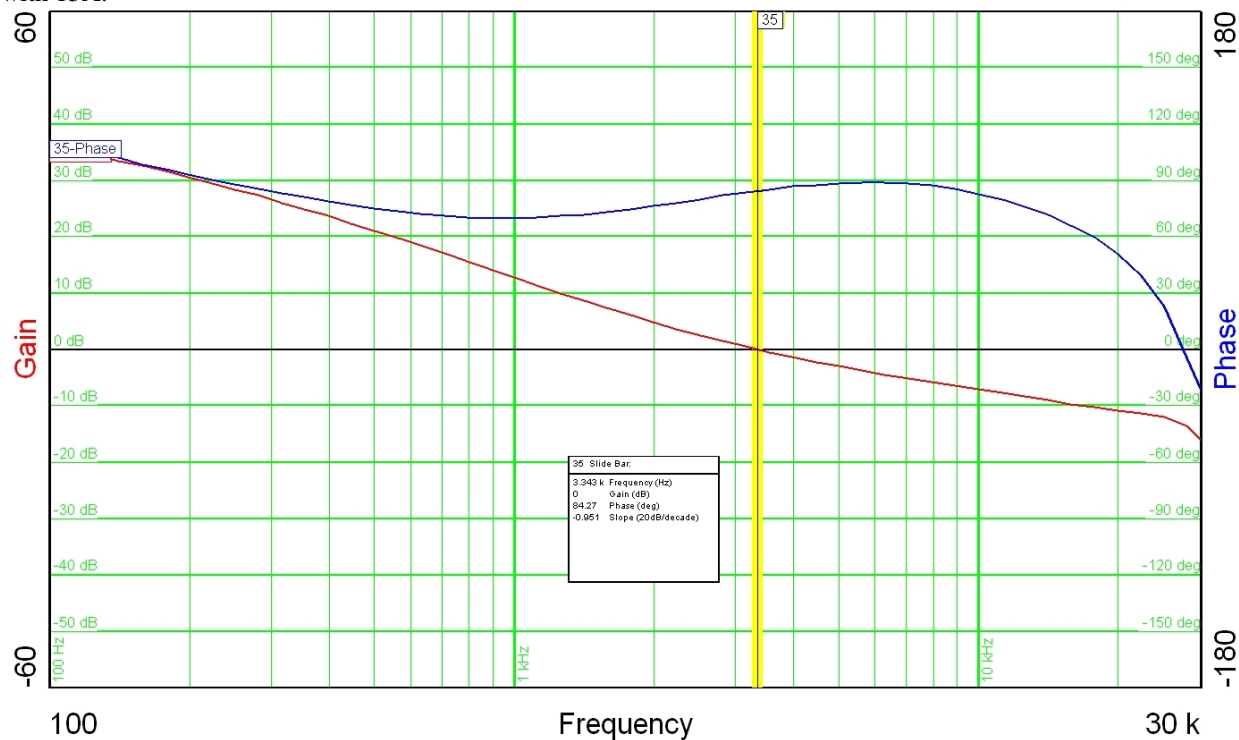
## 5 Output Ripple Voltage

The output ripple voltage is shown in the plot below. The input was set to 24VDC and the output was loaded with 15A.



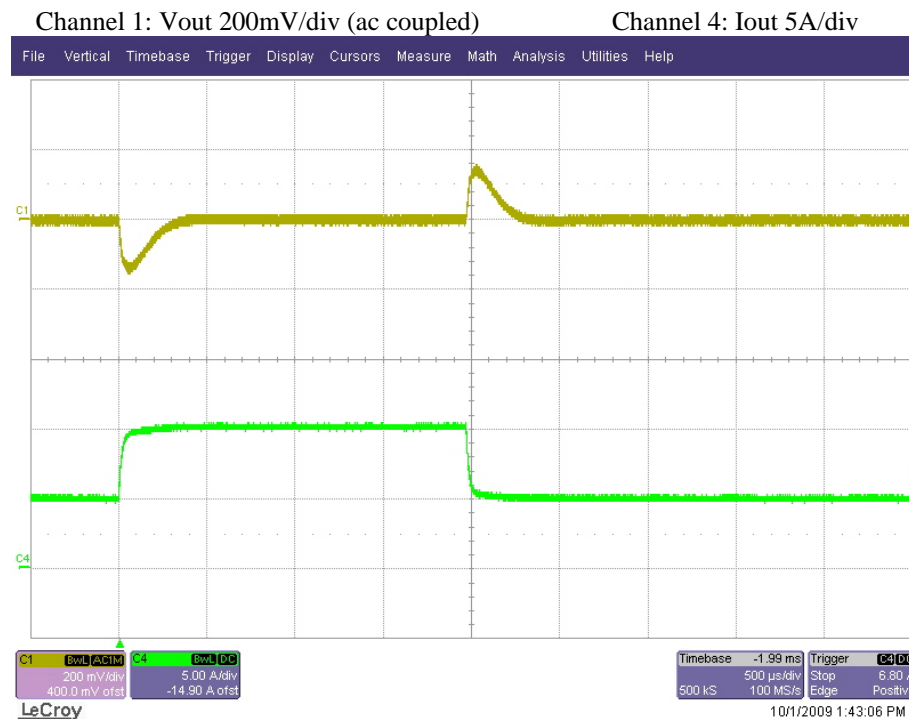
## 6 Frequency Response

The frequency response of the feedback loop is shown below. The input was set to 24V and the output was loaded with 15A.



## 7 Load Transients

The response to a load step from 5A to 10A is shown in the image below. The input was set to 24V.



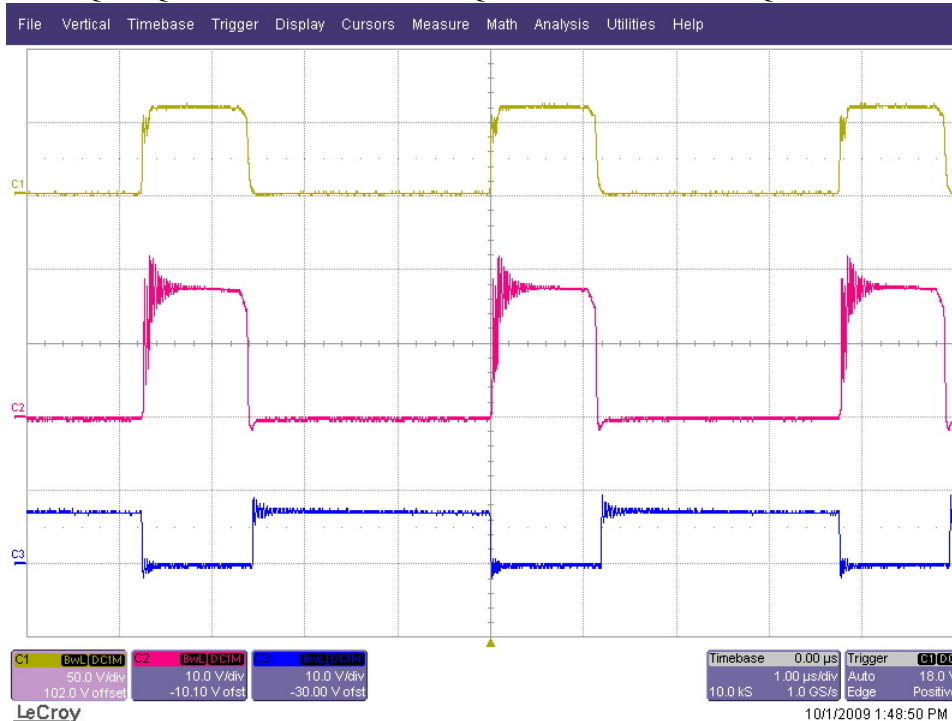
## 8 Switching Waveforms

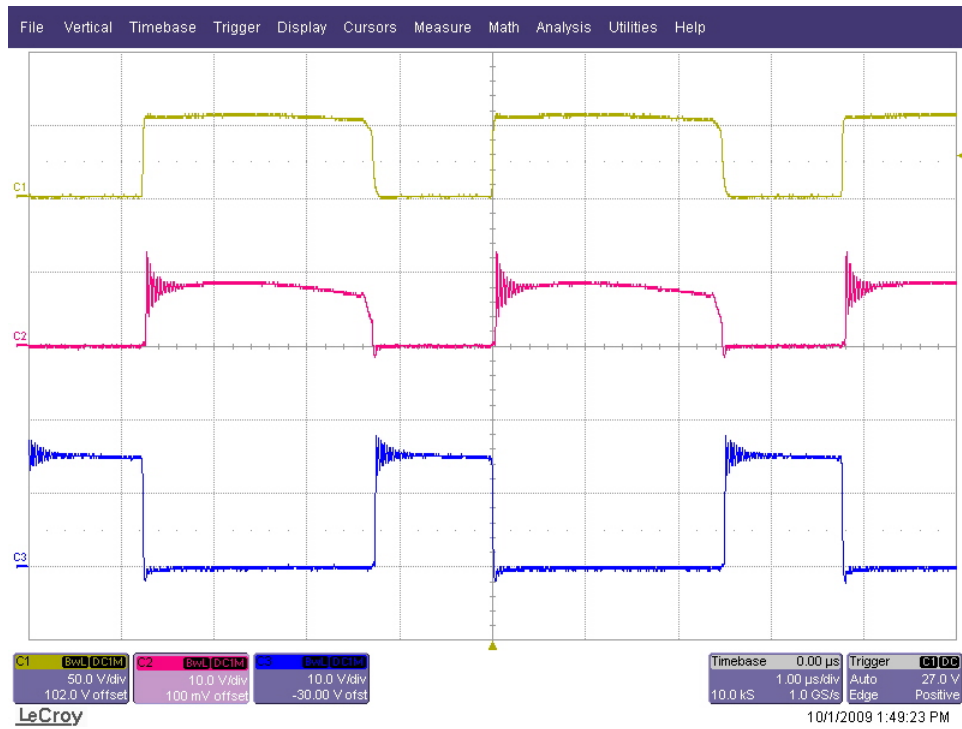
The images below show the drain-to-source voltage waveforms on the switching MOSFETs. The output was loaded with 15A. For the top image, the input was 18V. For the bottom image, the input was 36V.

Channel 1: Q5 & Q6 Vds

Channel 2: Q1 Vds

Channel 3: Q10 Vds







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