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1 Startup

The startup waveform is shown in Figure 1. The input voltage is set at 9V with LED load (golden dragon LCW W5SG) connected to the output.

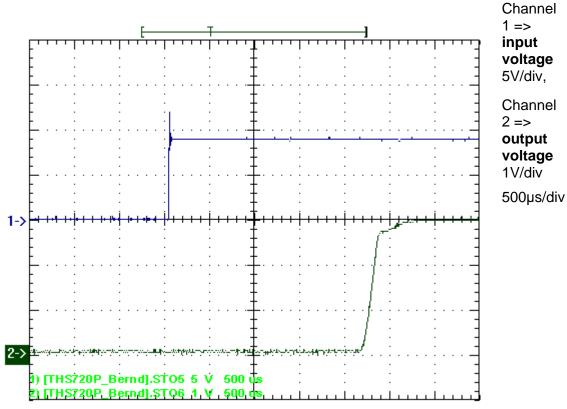
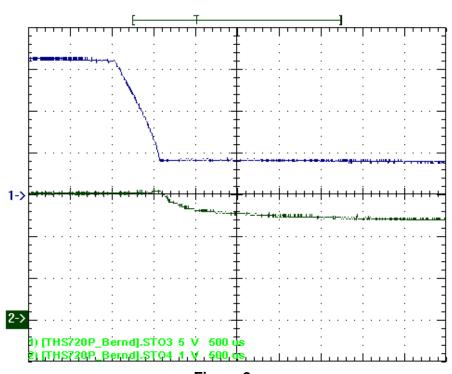


Figure 1



2 Shutdown

The shutdown waveform is shown in Figure 2. The input voltage is set at 16.5V with LED load (golden dragon LCW W5SG) connected to the output.



Channel 1 => input voltage 5V/div,

Channel 2 => output voltage 1V/div

500µs/div

Figure 2



3 Efficiency

The efficiency at different input voltages is shown in Figure 3.

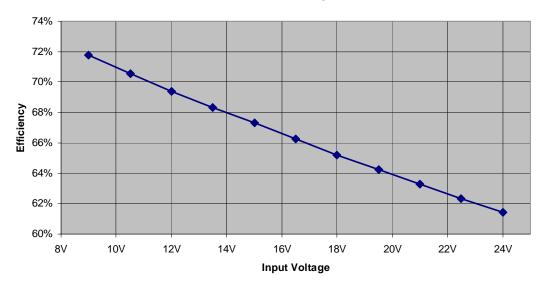


Figure 3



4 Line Regulation

The line regulation is shown in Figure 4.

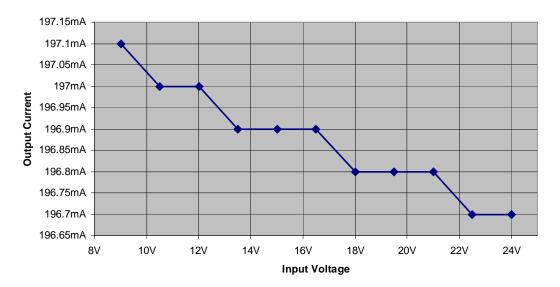


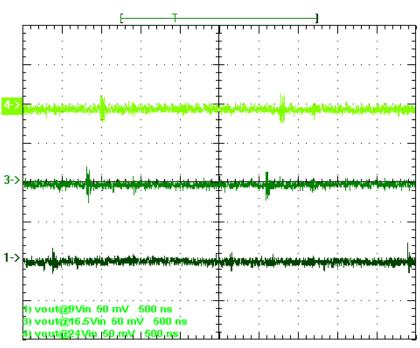
Figure 4



Ripple voltage 5

The **output** ripple voltage at 9V, 16.5V and 24V input voltage is shown in Figure 5.

Curves were captured separately with the triggered to switchnode



Channel 4 => output voltage@24Vin 50mV/div,

Channel 3 => output voltage@16.5Vin 50mV/div

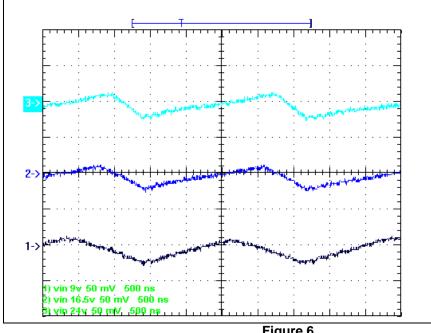
Channel 3: output voltage@9Vin 50mV/div

500ns/div full bandwidth

Figure 5

The input ripple voltage at 9V, 16.5V and 24V input voltage is shown in Figure 6.

Curves were captured separately with the triggered to switchnode



Channel 3: input voltage@24Vin 50mV/div,

Channel 2: input voltage@16.5Vin 50mV/div

Channel 1: input voltage@9Vin 50mV/div

500ns/div 20MHz bandwidth

Figure 6



6 Frequency response

Figure 7 shows the loop response of output with 24V input and a LED as load.

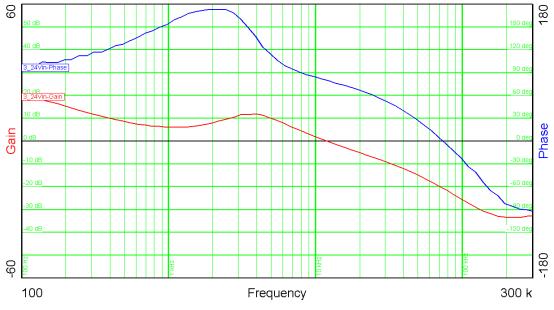


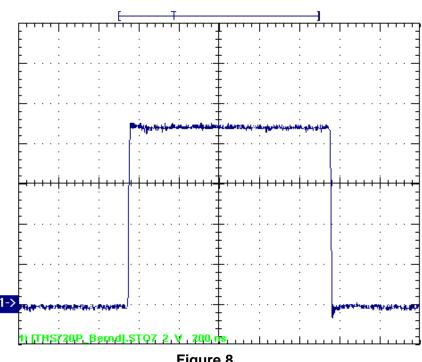
Figure 7

Bandwidth (kHz) Phase margin slope	12 79.6°
(20dB/decade)	-1.17
gain margin (dB) slope	-20.6
(20dB/decade) freq (kHz)	-1.68 74.12



Miscellaneous waveforms 7

The drain-source voltage on the switch node with an input voltage of 9V and a LED as is shown in Figure 8.

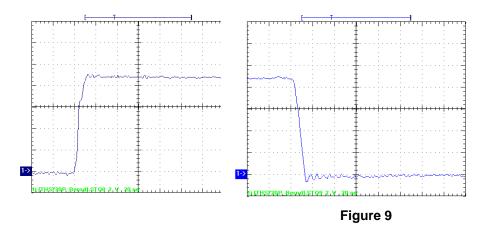


Channel 1: switch node voltage 2V/div full bandwidth

200ns/div

Figure 8

The same with other time base is shown in Figure 9.

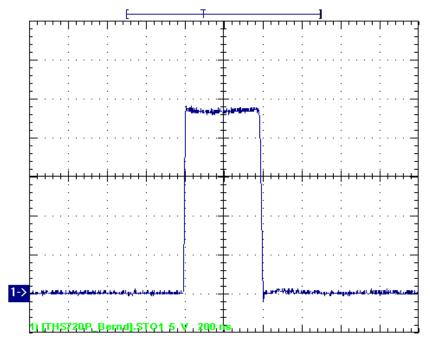


Channel 1: switch node voltage 2V/div full bandwidth

20ns/div



The drain-source voltage on the switch node with an input voltage of 24V and a L:ED as load is shown in Figure 10.



Channel 1 => switch node voltage 5V/div full bandwidth 200ns/div

Figure 10

The same with other time base is shown in Figure 11.

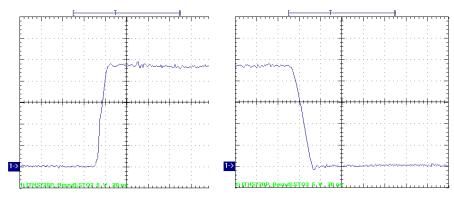


Figure 11

Channel 1: switch node voltage 5V/div full bandwidth 20ns/div



8 Photo

The photo (Figure 12) shows the circuit in operation.

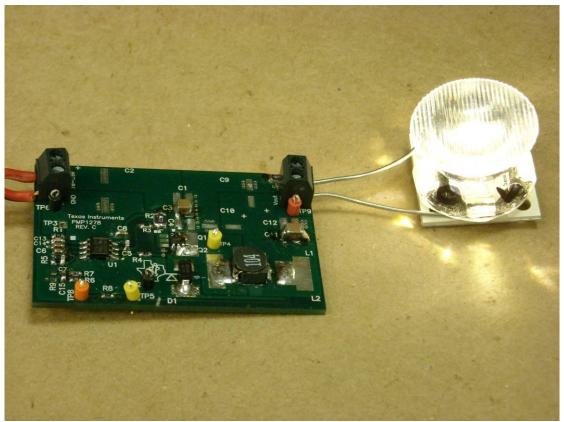


Figure 12

PMP4560 Rev. A - Test Results



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