

# **China Power Reference Design**

PMP4323 Test Procedure

**REV A** 

11/18/2011

### <u>General</u>

### 1.1 PURPOSE

To provide detailed data for evaluating and verifying the PMP4323.

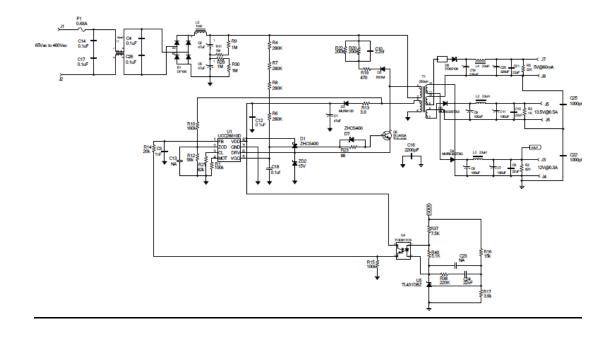
**1.2 REFERENCE DOCUMENTATION**Schematic PMP4323\_SCH\_RevA.PDF Assembly PMP4323\_PCB\_RevA.PDF BOM list

# **1.3** <u>TEST EQUIPMENTS</u> Multi-meter: Fluke 289

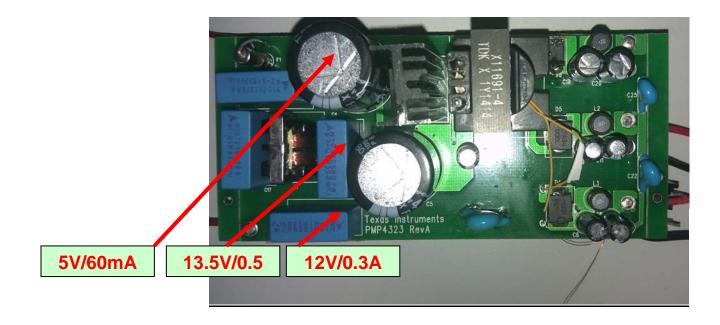
DC Source: Chroma 62012P-600-8

E-load: Chroma 6312 Oscilloscope: TDS3034C

### 2 Schematics

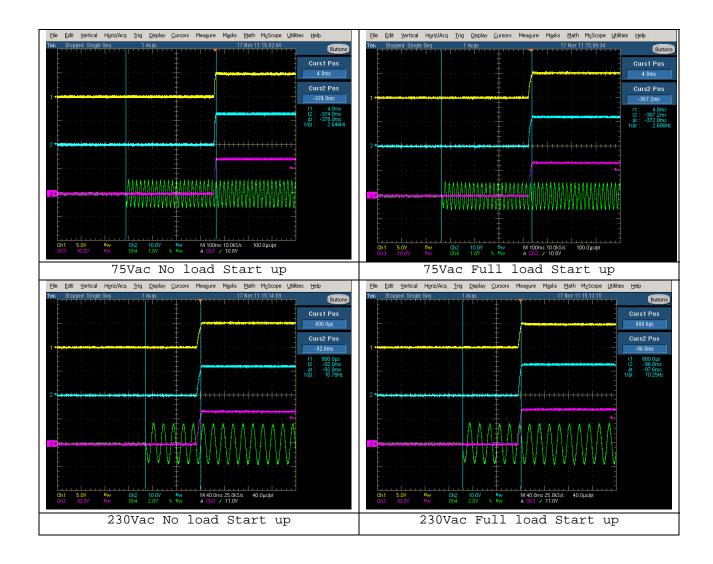


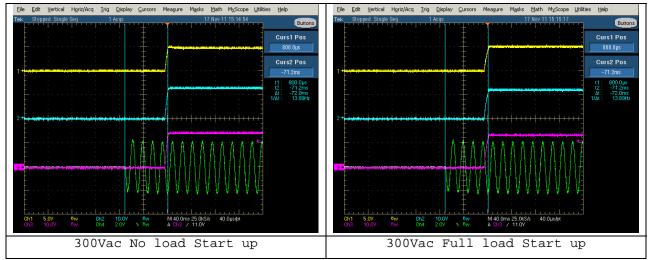
# 3 Demo Board



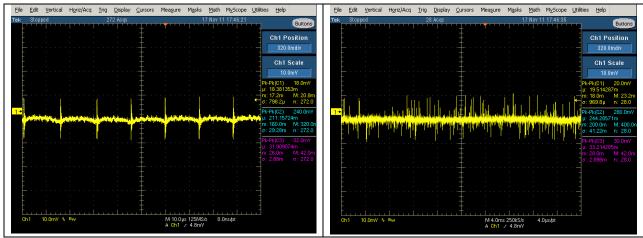
# 4 Electric Performance

4.1 Start Up

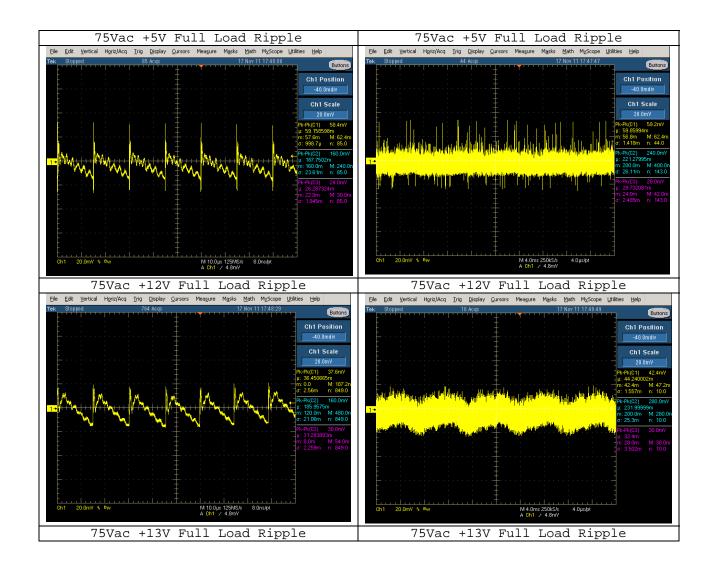


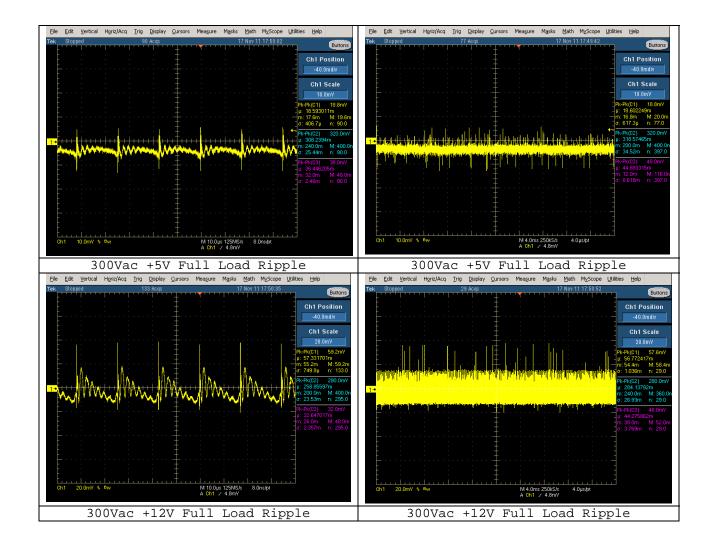


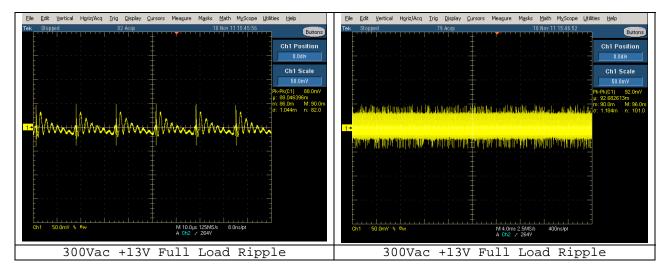
4.2 Output Ripple
Requirement: 12V ripple<120mV 5V Ripple<50mV











5 Efficiency
Requirement: 70%(TYP) for Vin=300Vdc,rated load.

Vin(Vac)	Pin(W)	Vol(V)	Io1(A)	Vo2(V)	Io2(A)	Vo3(V)	Io3(A)	Po(W)	Eff(%)
75	15.18	5.01	0.06	12.06	0.306	13.6	0.493	10.70	70.5
230	14.27	5.03	0.06	12.06	0.306	13.6	0.493	10.70	75.0
300	14.44	5.00	0.06	12.06	0.306	13.6	0.493	10.70	74.1

## 6 Static Regulation

Requirement:5V@0.06A 2% 12V@0.3A 0.5% 13V@0.5A 2%

Vin=75Vac

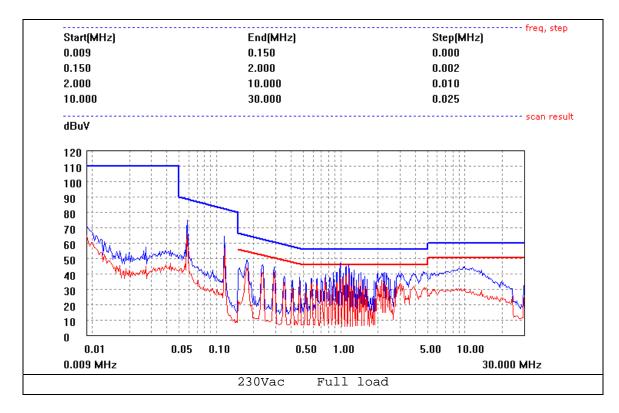
5V	12V	13V	5V	12V	13V
(0.06A)	(0.3A)	(0.5A)	(0.06A)	(0.3A)	(0.5A)
0	0	0	4.85	12.06	13.8
0	0	1	5.23	12.06	15.3
0	1	0	4.9	12.07	11.8

0	1	1	5.52	12.06	13.47
1	0	0	4.44	12.06	13.8
1	0	1	4.92	12.06	15.35
1	1	0	4.42	12.06	15.35
1	1	1	5.03	12.06	13.49

### Vin=300Vac

	-				
5V	12V	13V	5V	12V	13V
(0.06A)	(0.3A)	(0.5A)	(0.06A)	(0.3A)	(0.5A)
0	0	0	4.86	12.06	13.86
0	0	1	5.25	12.06	15.4
0	1	0	4.84	12.06	11.86
0	1	1	5.45	12.06	13.5
1	0	0	4.45	12.07	13.87
1	0	1	4.94	12.06	15.37
1	1	0	4.42	12.07	11.99
1	1	1	5.03	12.05	13.5

# 7 EMI Test



# 8 Thermal Test

Thermal test is tested under half load,75C ambient temperature as customer required. The test data as below:

	70Vac	230Vac	300Vac
Q2	81.8	80.5	81.4
Transformer Core	79.4	79.6	80.1
Transformer	79.1	79.3	80.0
Winding			
D4	83.5	83.7	83.8
D5	85.1	84.1	85.0
B1	80.1	76.6	76.1

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