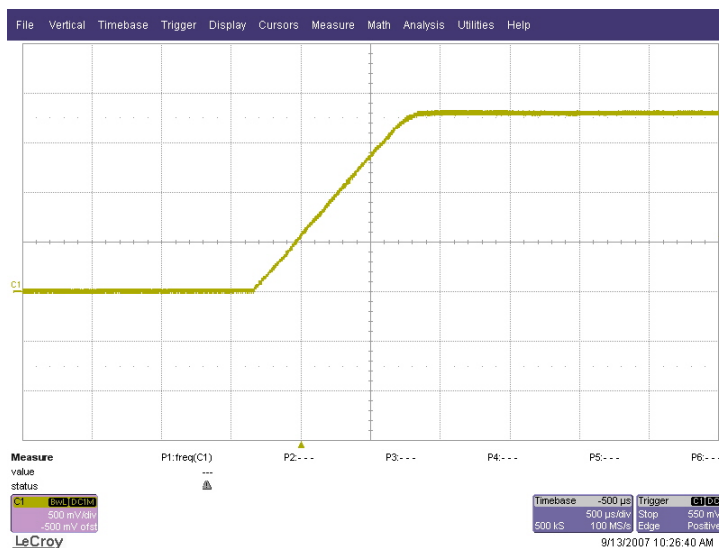


1 Startup

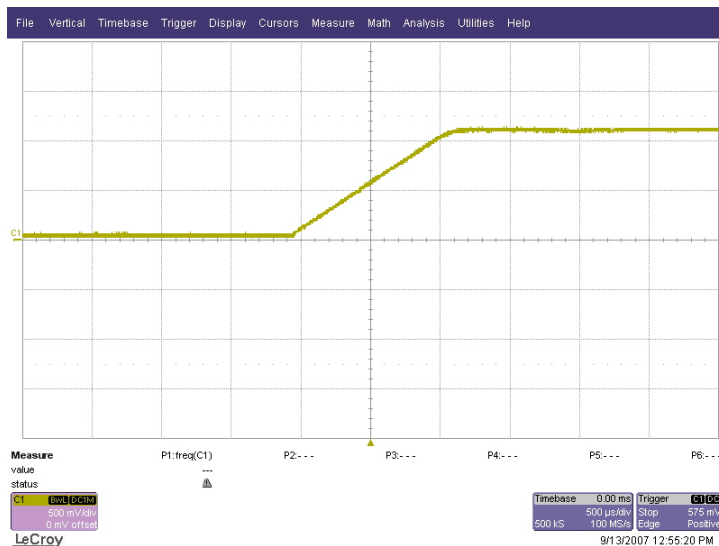
The photos below show the startup waveforms. The input voltage is 12V, the outputs are not loaded. The timebase is set to 500us/Division.

Channel 1 : 1.8V Output – Yellow (500mV/Division)



5V is applied

Channel 1 : 1.1V Output – Yellow (500mV/Division)

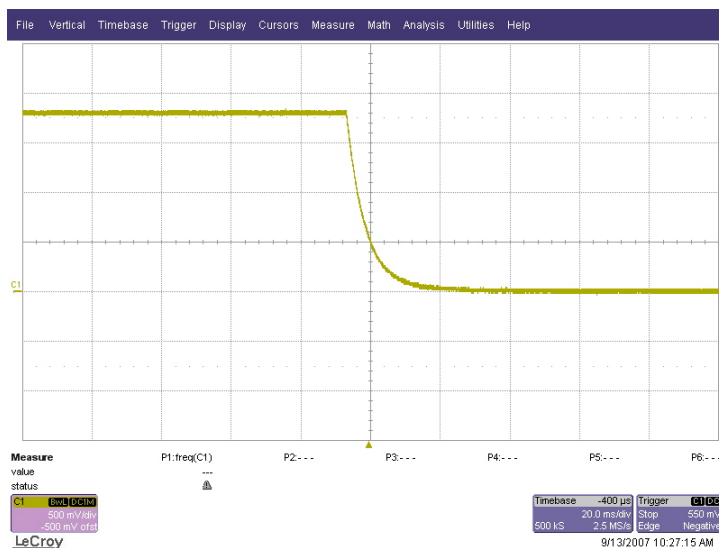


5V is applied

2 Shutdown

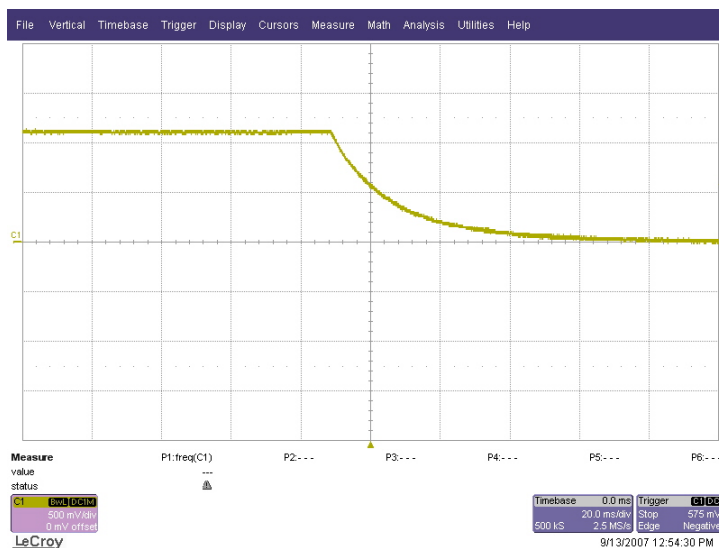
The photos below show the shutdown waveforms. The input voltage is 12V. The timebase is set to 20ms/Division. The outputs are unloaded. The supplies are turned off by removing the 5V bias.

Channel 1 : 1.8V Output – Yellow (500mV/Division)



5V is removed

Channel 1 : 1.1V Output – Yellow (500mV/Division)

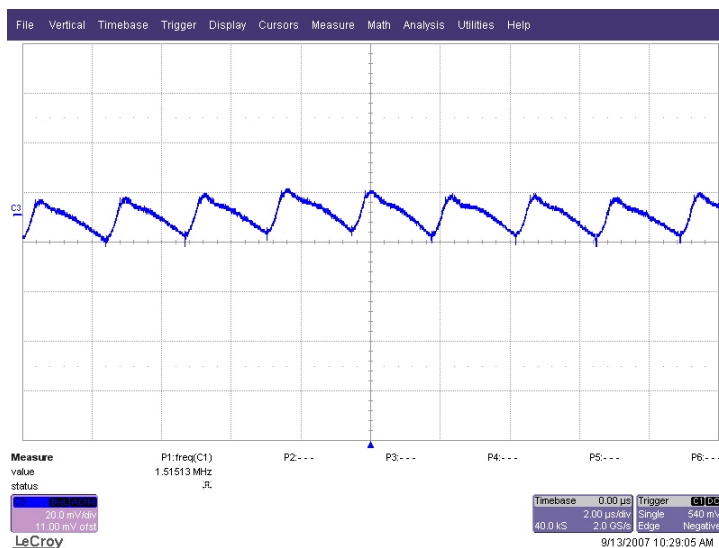


5V is removed

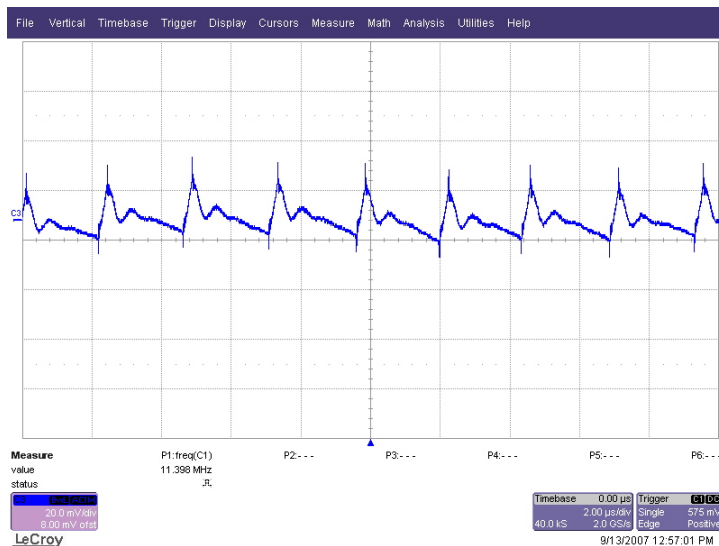
3 Output Ripple Voltage

The output voltage ripple for each converter is shown in the figures below. The input is 12V. The outputs are fully loaded. The timebase is 2us/Division.

Channel 3 : 1.8 Output – Blue (AC Coupled; 20mV/Division)



Channel 3 : 1.1 Output – Blue (AC Coupled; 20mV/Division)

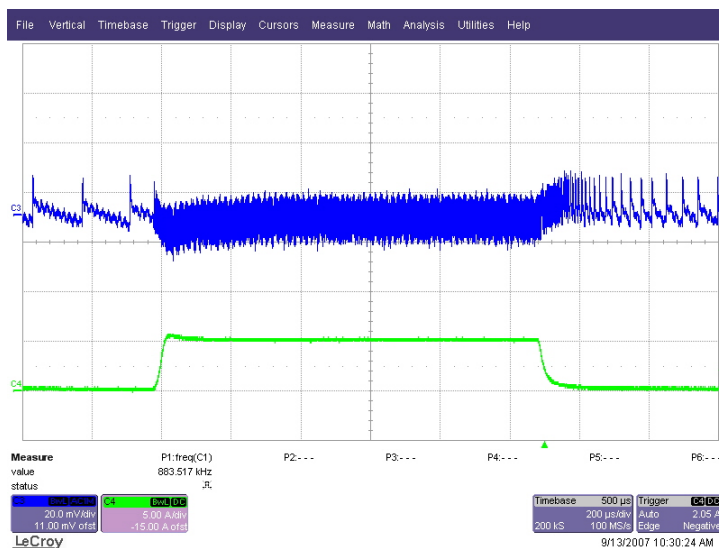


4 Load Transients

The photos below show the transient response. The current is pulsed from 0% to 100% load. The timebase is set to 200us/Division.

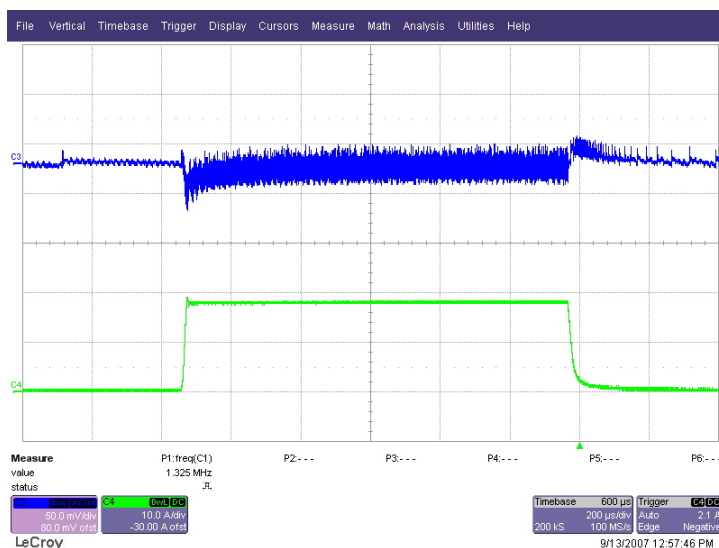
Channel 3 : 1.8V Output – Blue (AC Coupled; 20mV/Division)

Channel 4 : Output Current – Green (5A/Division)



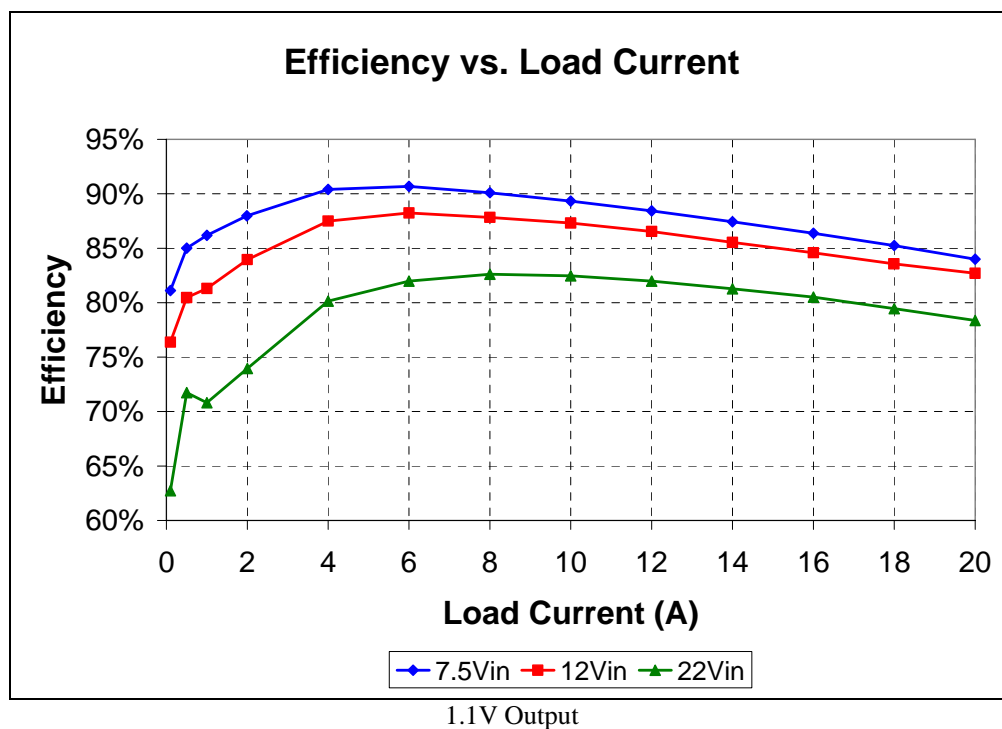
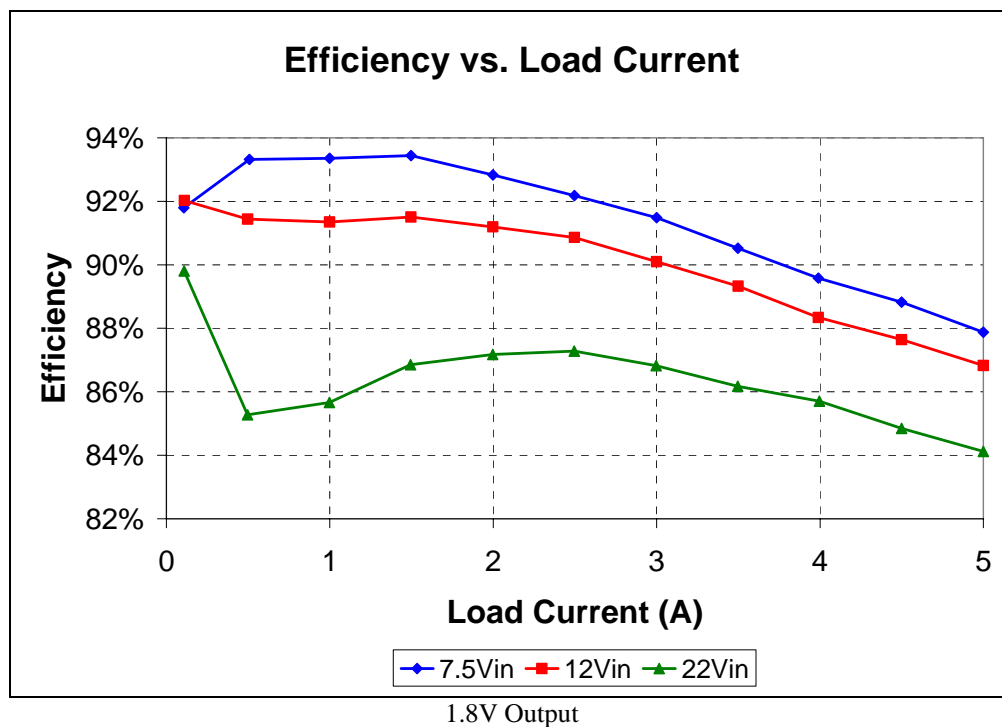
Channel 3 : 1.1V Output – Blue (AC Coupled; 50mV/Division)

Channel 4 : Output Current – Green (10A/Division)



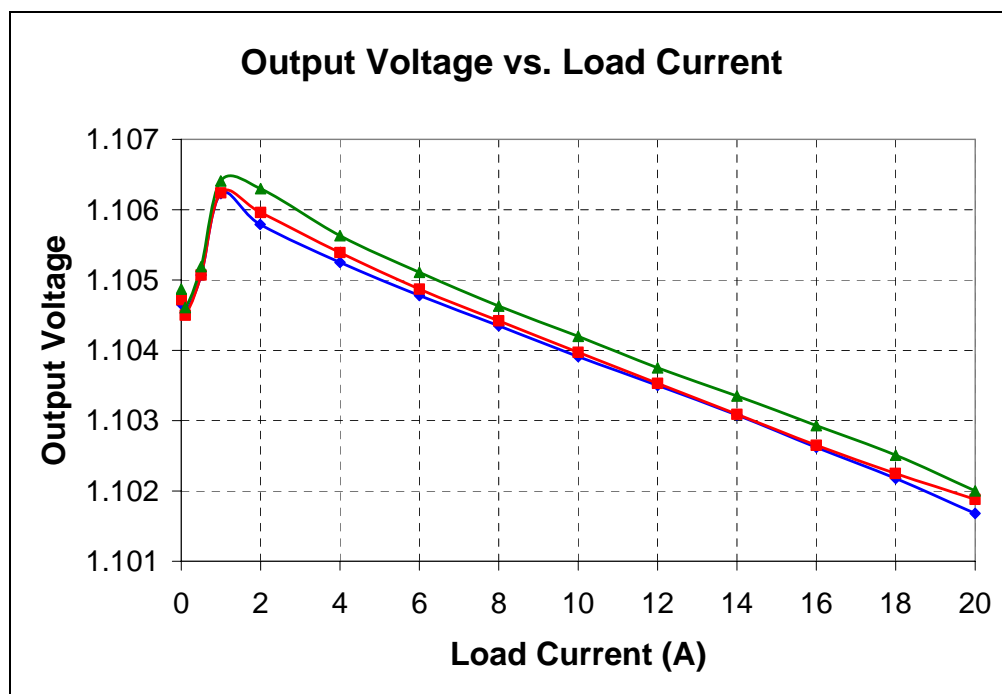
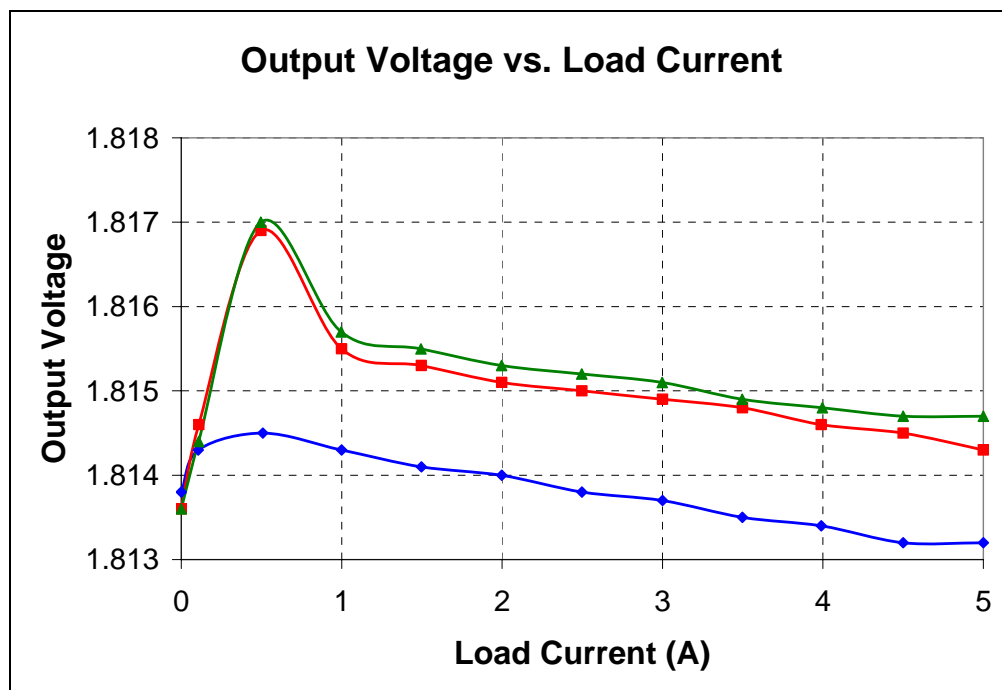
5 Efficiency

The efficiency for each converter is shown in the figures below.



6 Load Regulation

The load regulation is shown in the figures below.



7 Switching Waveforms

The plots below show the switching waveforms. The input is 12V. The outputs are fully loaded. The time base is set to 1us/Division.



1.8V Switch Node



1.1V Switch Node

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