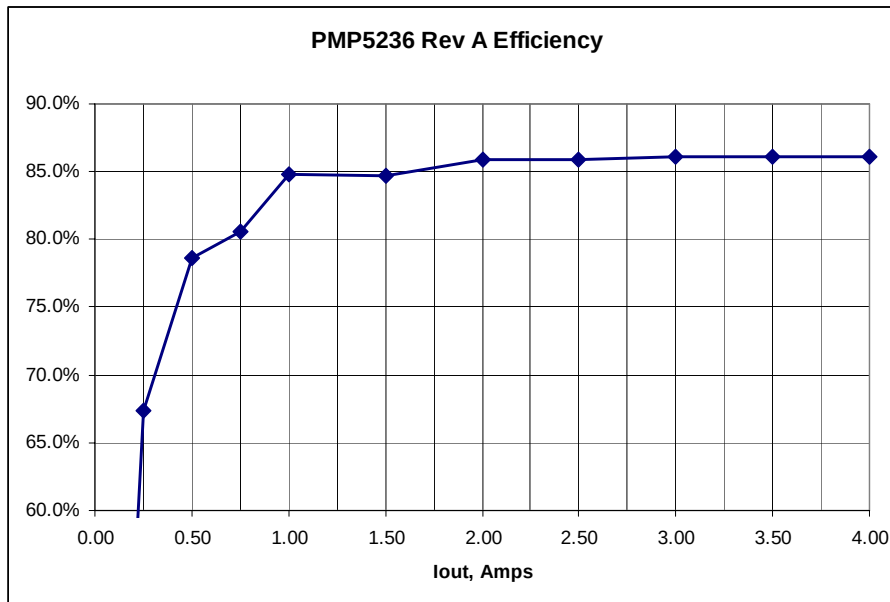


12V Output Data

Efficiency

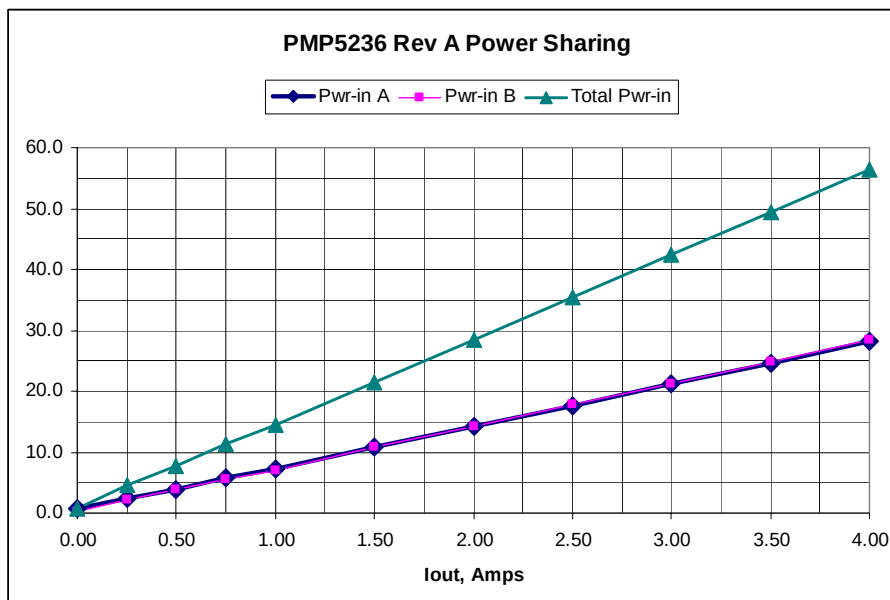
All measurements taken with a Phihong POE80U-560(G)-R PSE source.

The efficiency is shown below:



Load Sharing

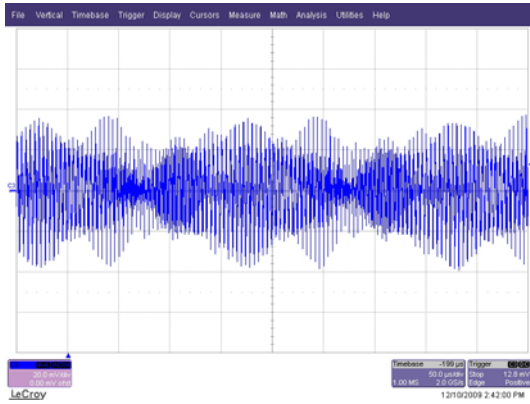
The power sharing between the two converters is shown below:



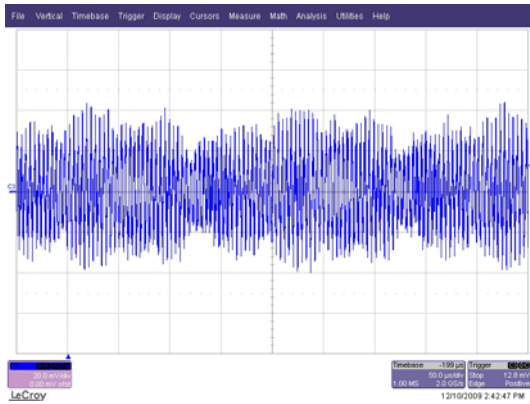
Ripple and Noise

All measurements taken with a 20MHz BWL unless otherwise noted.

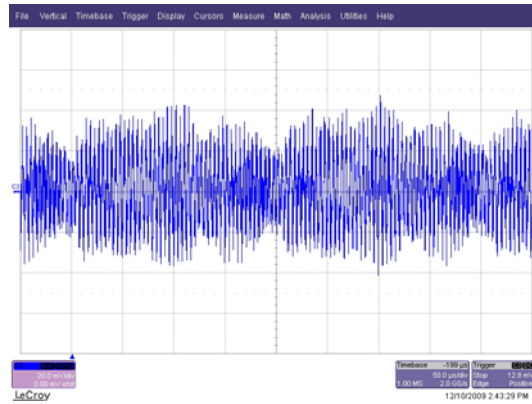
Output ripple at J7; 20mV/div:



Input ripple at J3; 20mV/div:

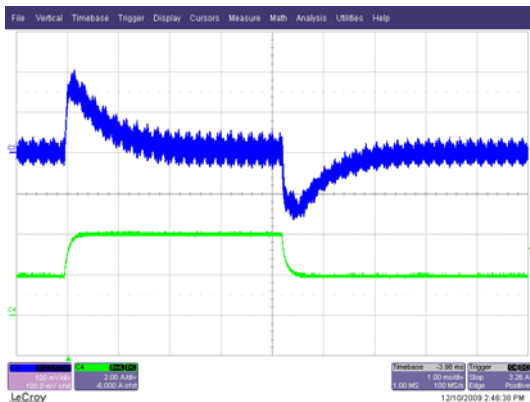


Input ripple at J4; 20mV/div:



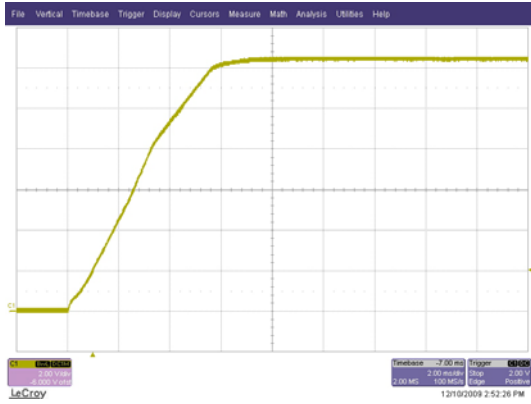
Dynamic Loading

12V response; 48VIN, 200mV/div:

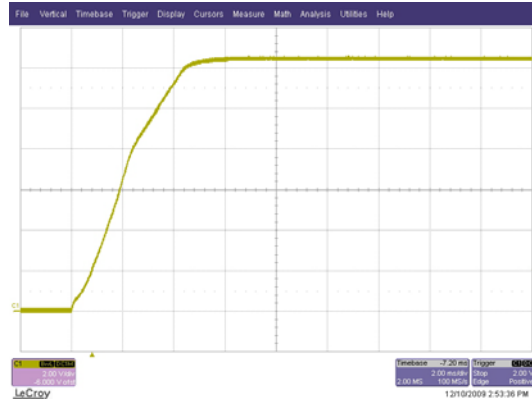


Turn On Response

Max load:



0A load:



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