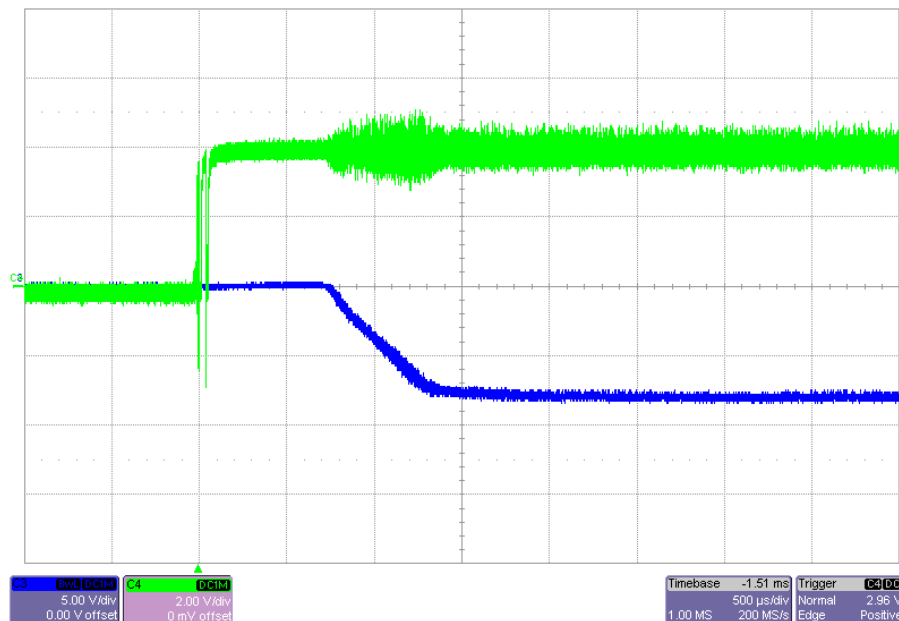


# PMP5026 Test Results

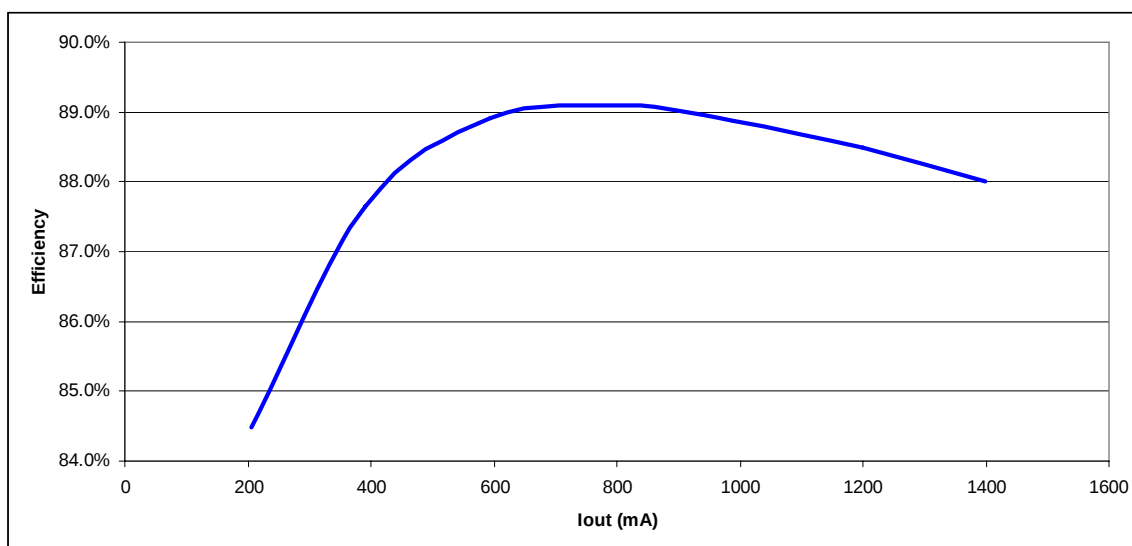
## 1 Startup

The startup waveform is shown in the figure below. The input voltage was set at 12V, with 1 A load on the output. Top trace is enable, bottom trace is output voltage.



## 2 Efficiency

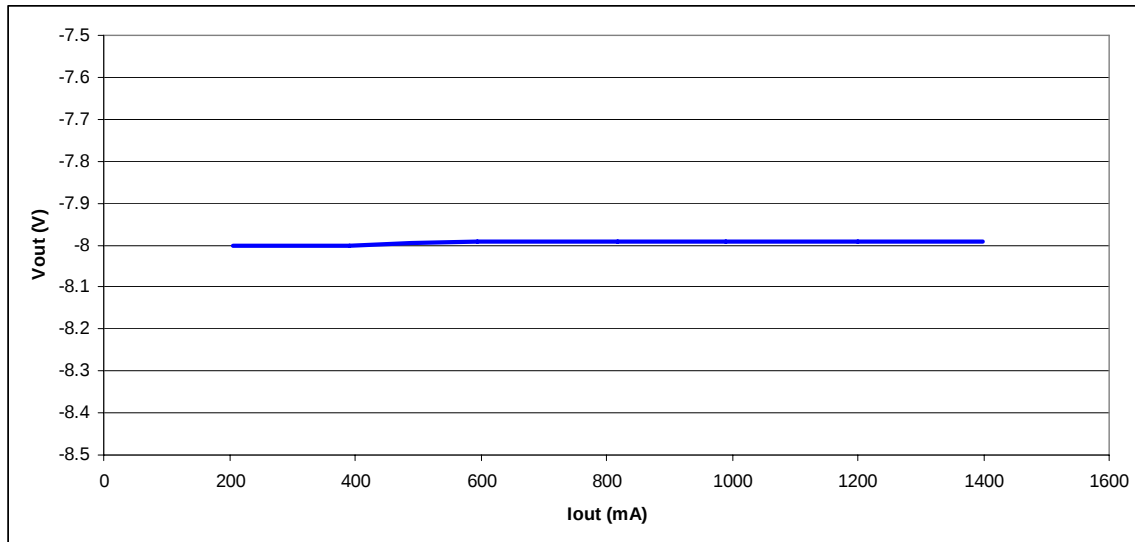
The efficiency is shown in the figure below. Input was 12 volts.



## PMP5026 Test Results

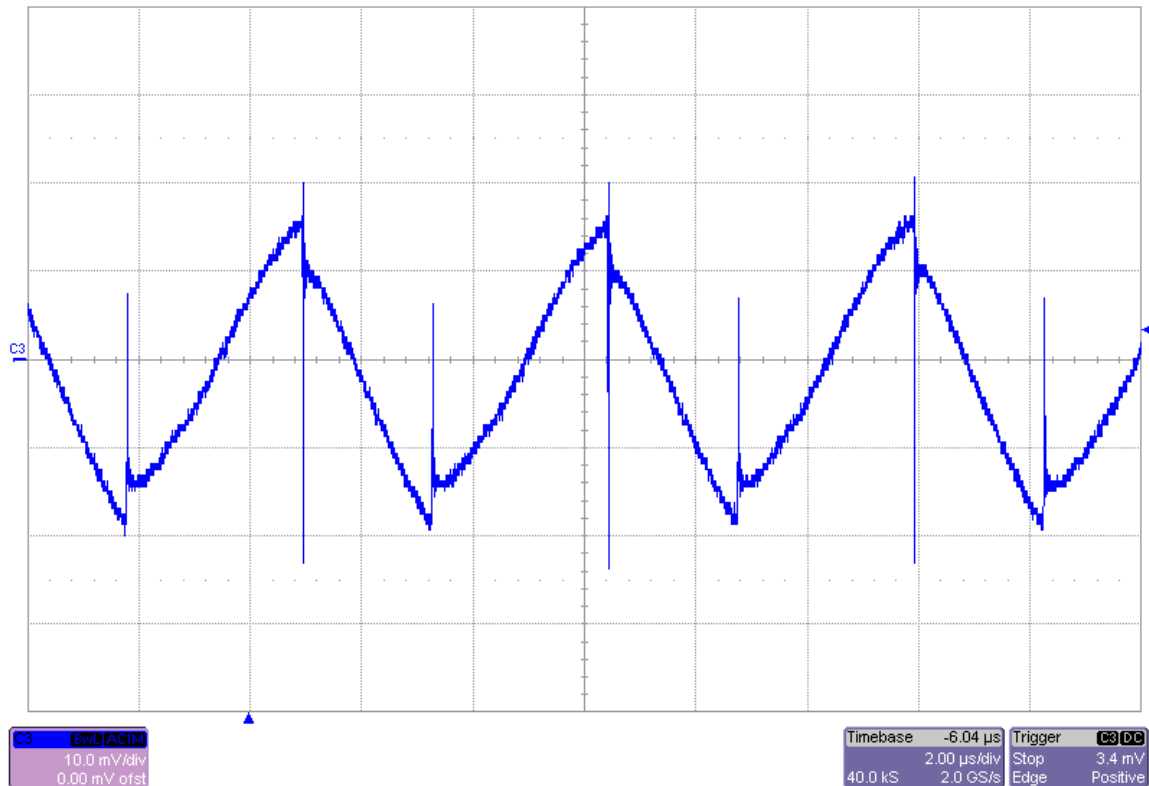
### 3 Load Regulation

The load regulation of the output is shown in the graph below.



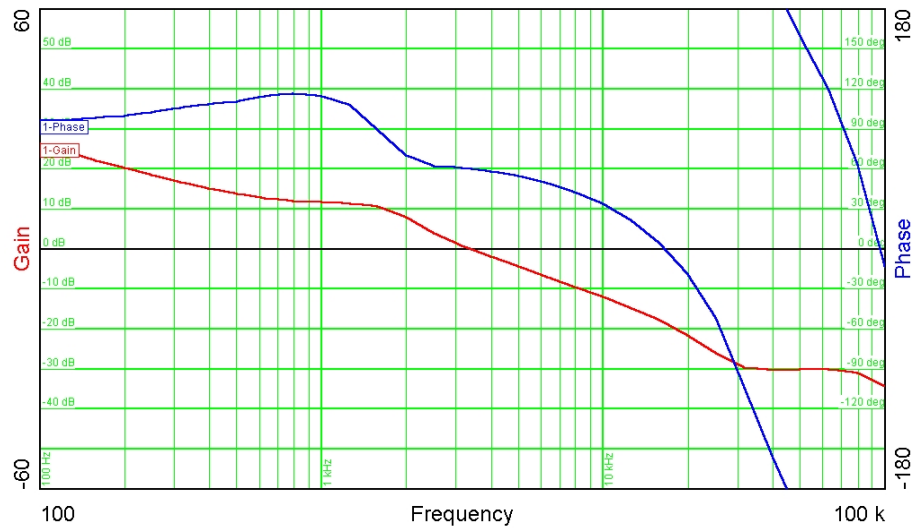
### 4 Output Ripple Voltage

The output ripple voltage is shown in the figure below. The image was taken with a 1.3 A load and 12 volt input. 10 mV/div Vertical and 1  $\mu$ S/div Horizontal.



## 5 Control Loop Frequency Response

The figures below show the loop responses with a 12V input, and a 1.3 A load.

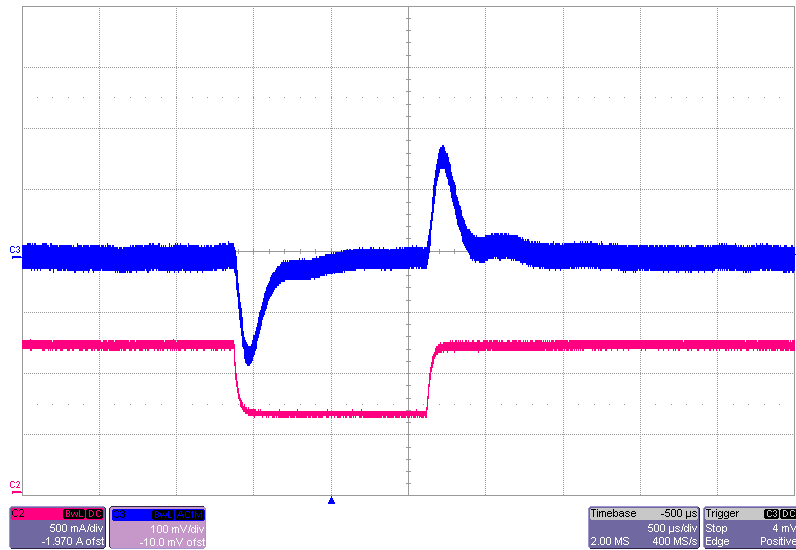


## 6 Load Transients

The figures below show the output response to load transients. The input voltage was set to 12V.

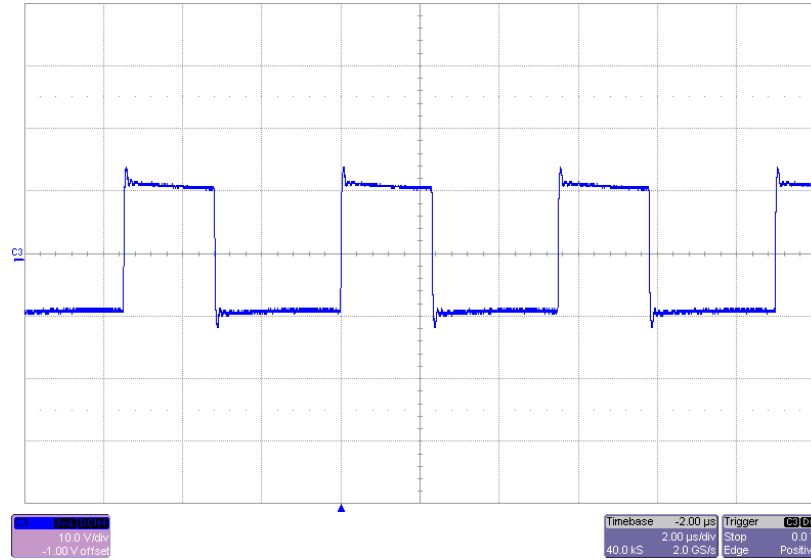
Channel 3: Vout (AC coupled)

Channel 2: Load current



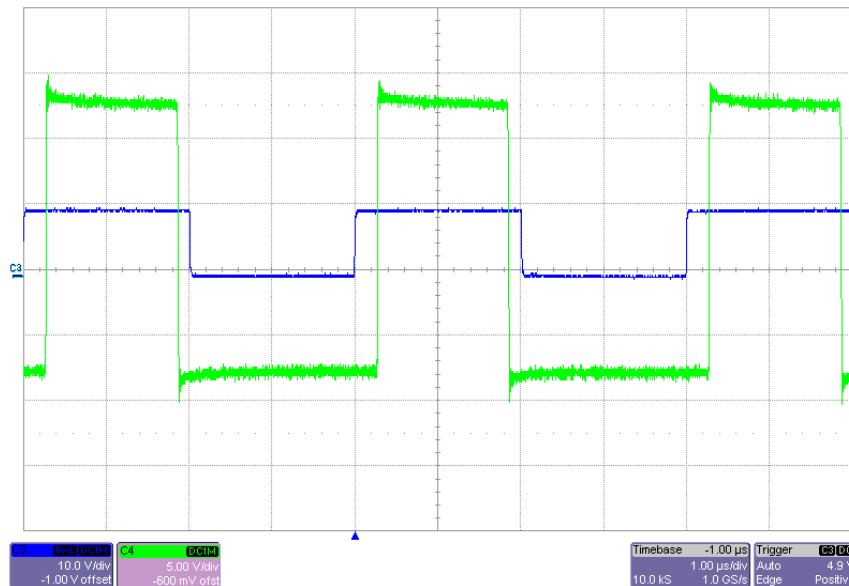
## 7 Switch Node Waveforms

The following figure shows the switching node waveform at 1.3 amps output and 12 volts in.



## 8 Synchronization Waveforms

The following figure shows the switching node and sync signal waveform at 1.3A output and 12V in.



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