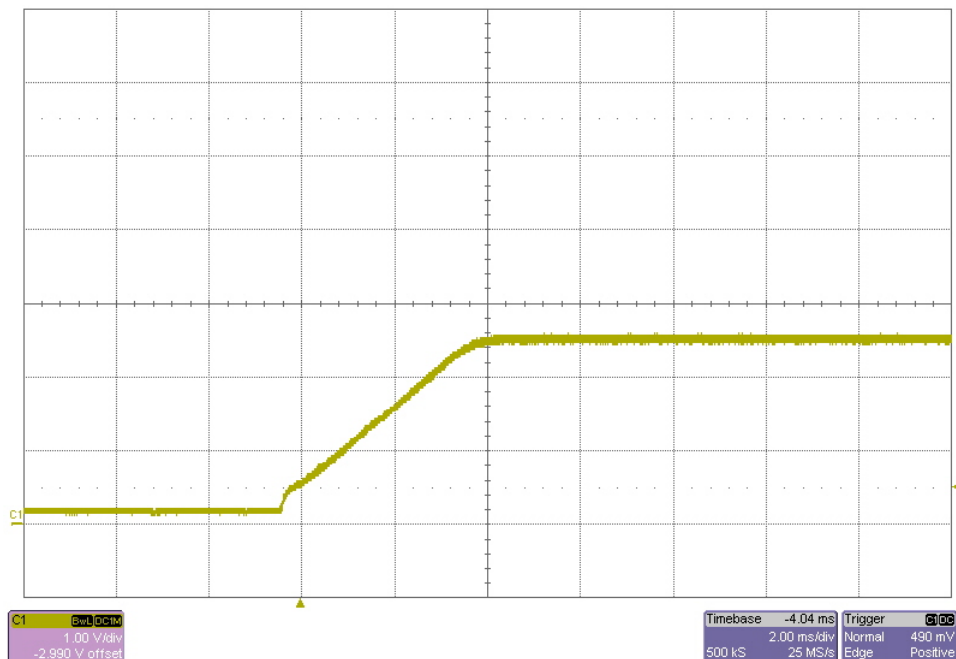
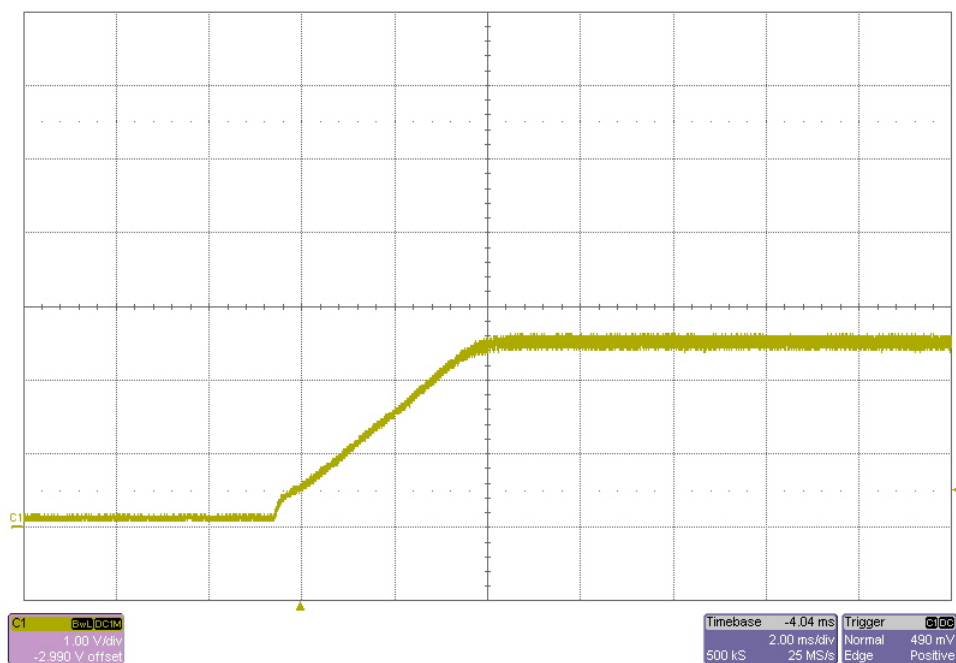


1 Startup

The photo below shows the 2.5V output voltage startup waveforms after the application of 12Vdc in. The output was loaded to 0A. (1V/DIV)

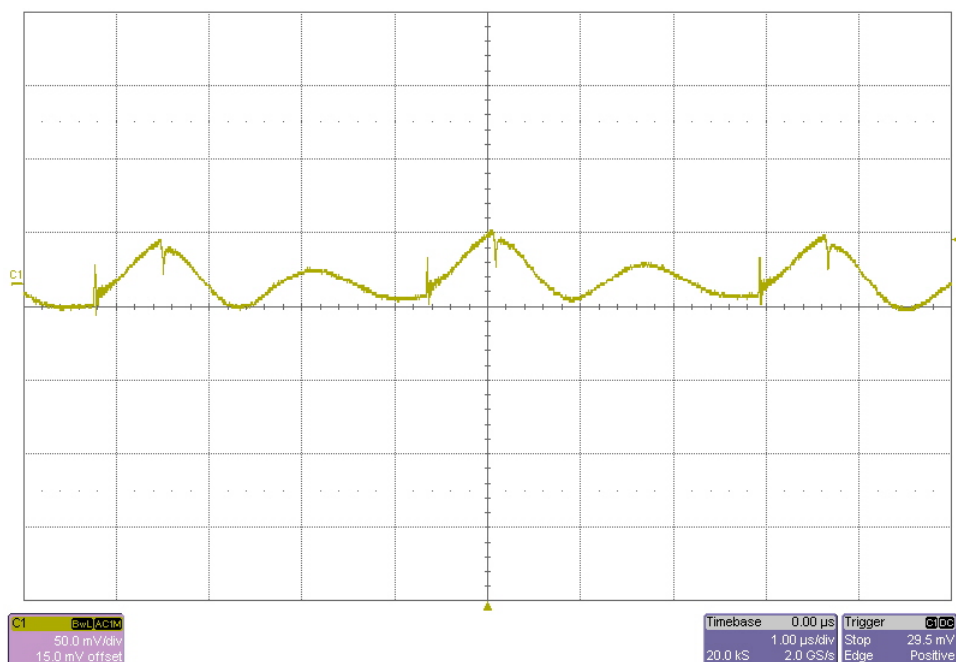


The photo below shows the 2.5V output voltage startup waveforms after the application of 12Vdc in. The output was loaded to 20A. (1V/DIV)



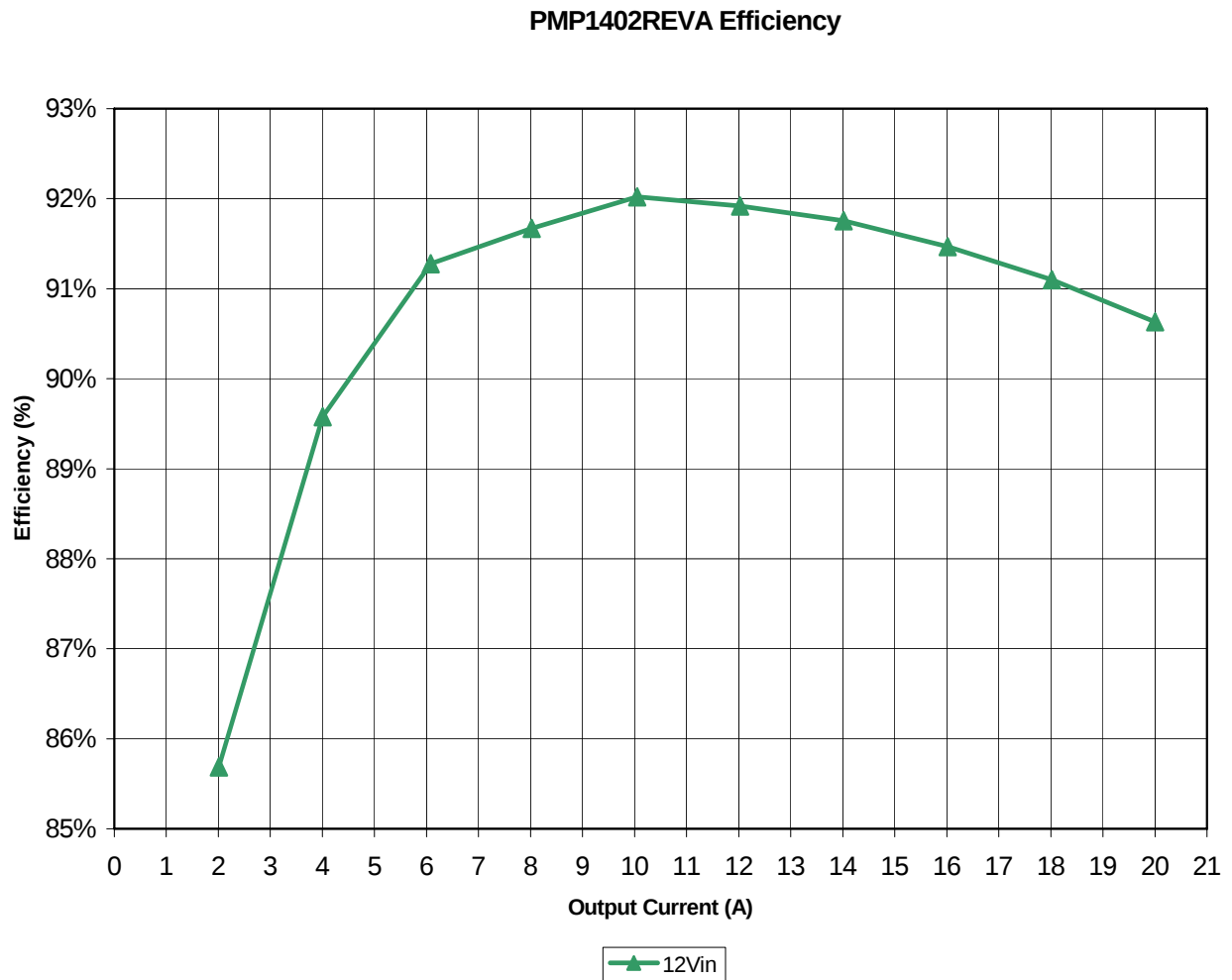
2 Output Ripple Voltage

The 2.5V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 20A and the input voltage set to 13.2Vdc. (50mV/DIV)



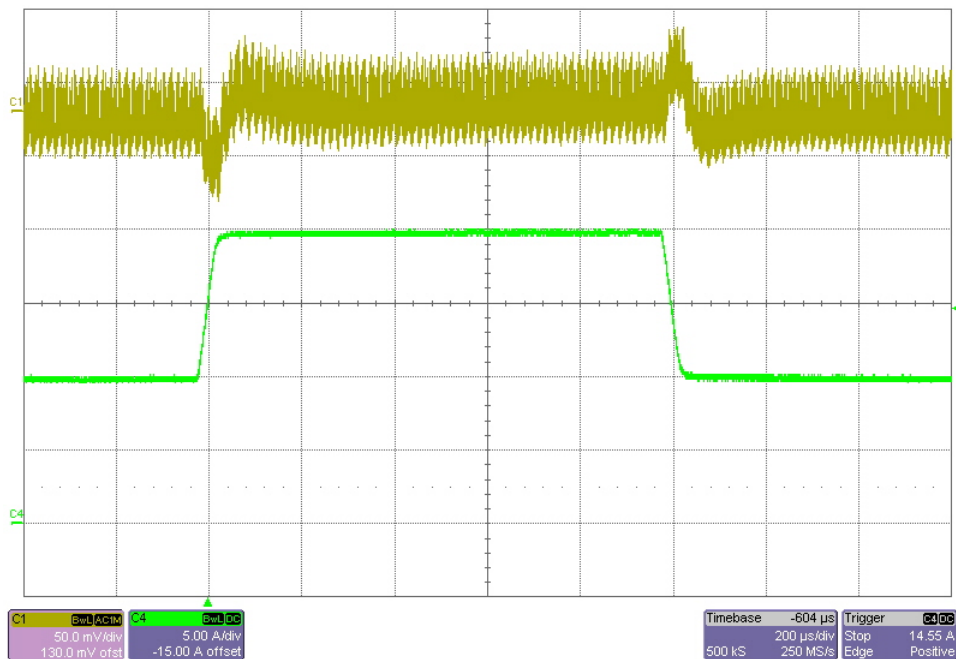
3 Efficiency

The converter efficiency is shown in the figure below.



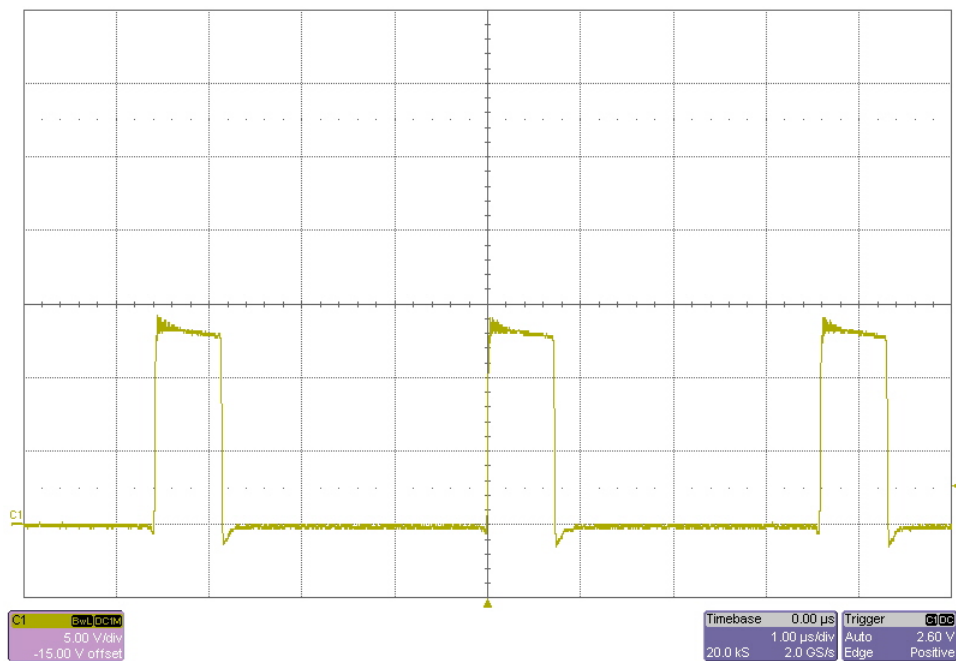
4 Load Transients

The photo below shows the 2.5V output voltage when the load current is pulsed between 10A and 20A. $V_{in} = 13.2\text{Vdc}$. (5A/DIV, 50mV/DIV)



5 Switch Node Waveforms

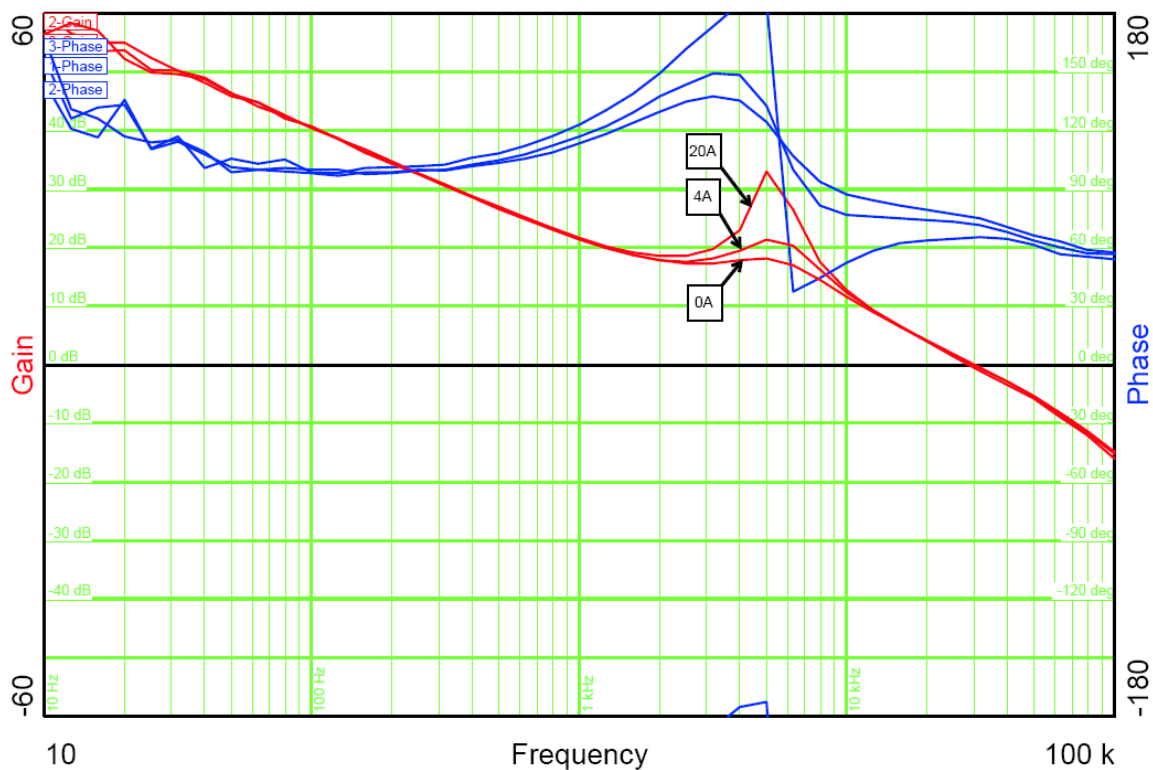
The photo below is of the switching node waveform. The input voltage is 13.2V and the output is loaded to 20A.



6 Control Loop Gain / Stability

The plot below shows the loop gain and phase margin with the input voltage set to 13.2V. The output was loaded to 0A, 4A and 20A.

I _{out} = 0A;	Band Width = 30KHz,	Phase Margin = 75 degrees
I _{out} = 4A;	Band Width = 30KHz,	Phase Margin = 72 degrees
I _{out} = 20A;	Band Width = 30KHz,	Phase Margin = 65 degrees



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