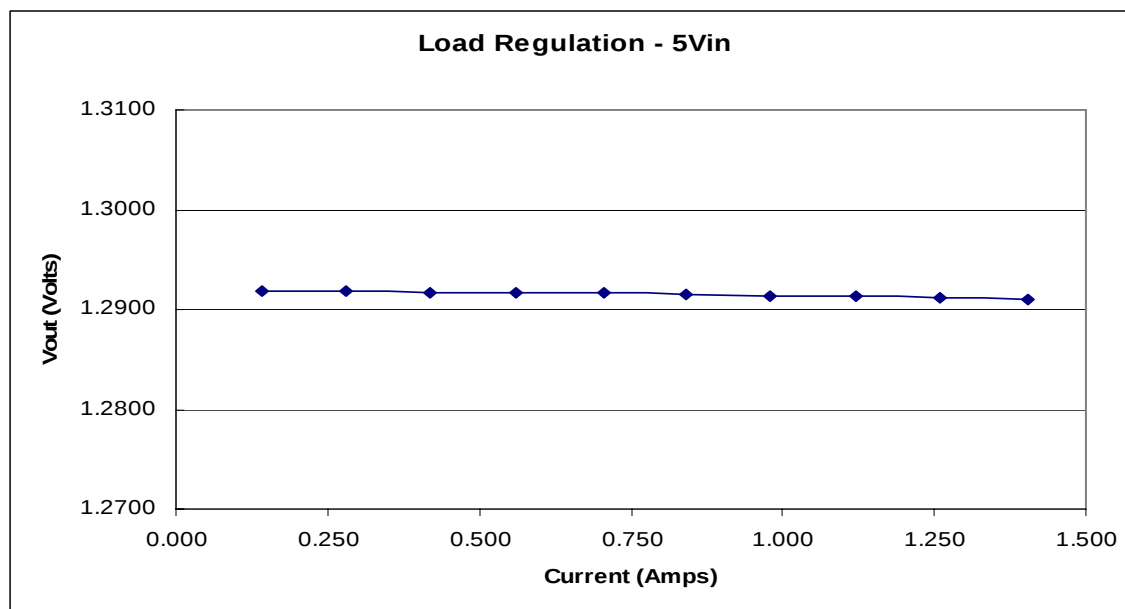
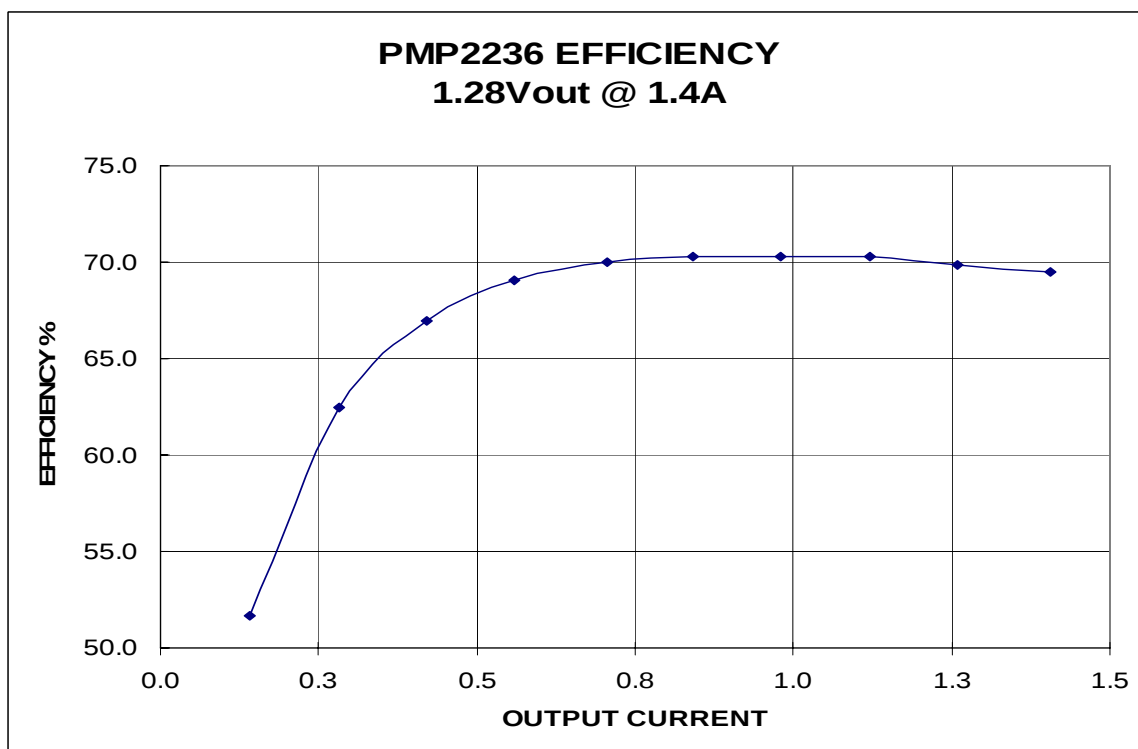


1 Efficiency/Line Regulation (1.28V output)

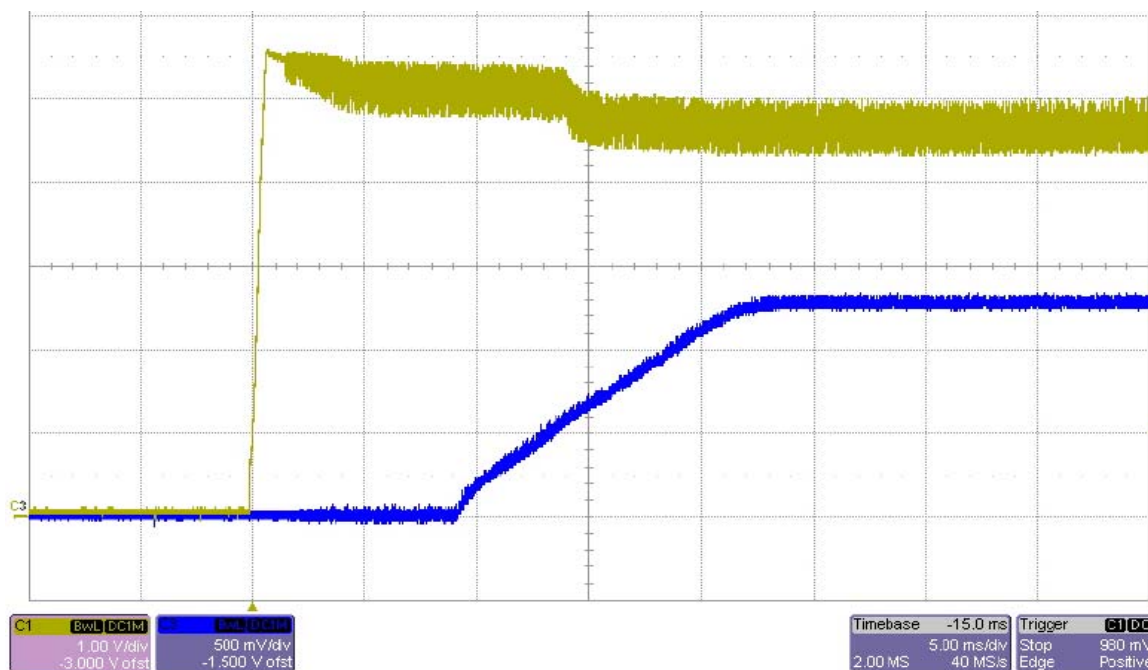
The converter efficiency and regulation over load are shown in the figures below.



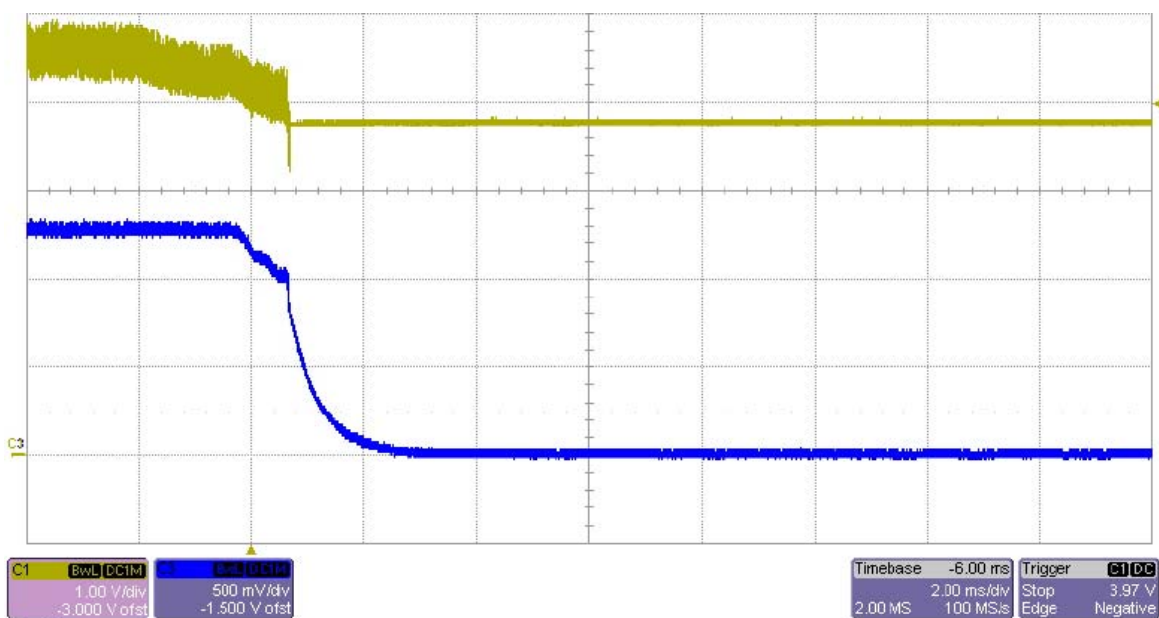
1.28V@1.4A

2 Startup/Shutdown (1.28V)

The photo below shows the 1.28V output voltage (Ch3) startup waveform after the application of 5Vdc in (Ch1). The output was loaded to 0A.



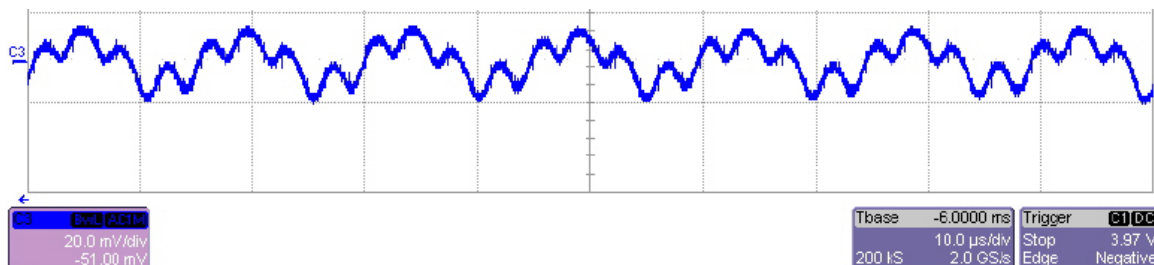
The photo below shows the 1.28V output voltage (Ch3) shutdown waveform after the application of 5Vdc in (Ch1). The output was loaded to 1.2A.



1.28V@1.4A

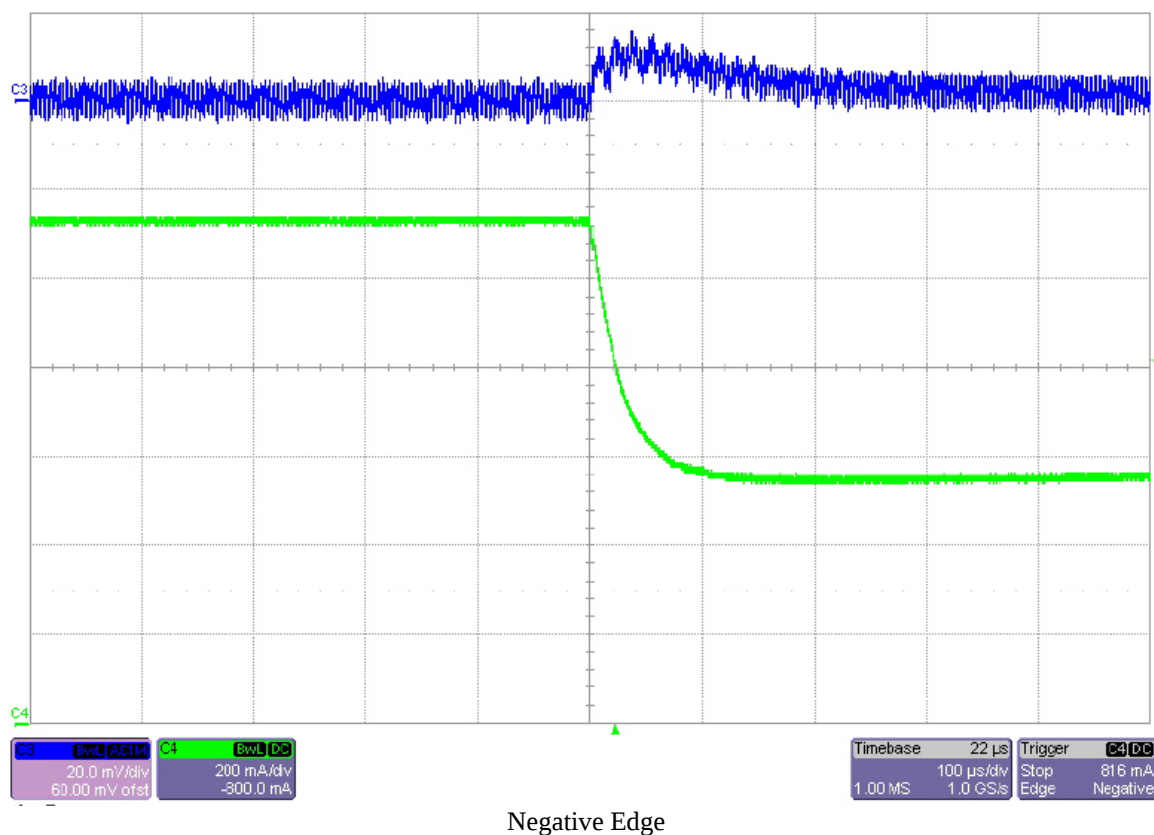
3 Output Ripple Voltage (1.28V)

The 1.28V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 1.2A and the input voltage set to 5Vdc. The V_{pp} was approx. 60mV

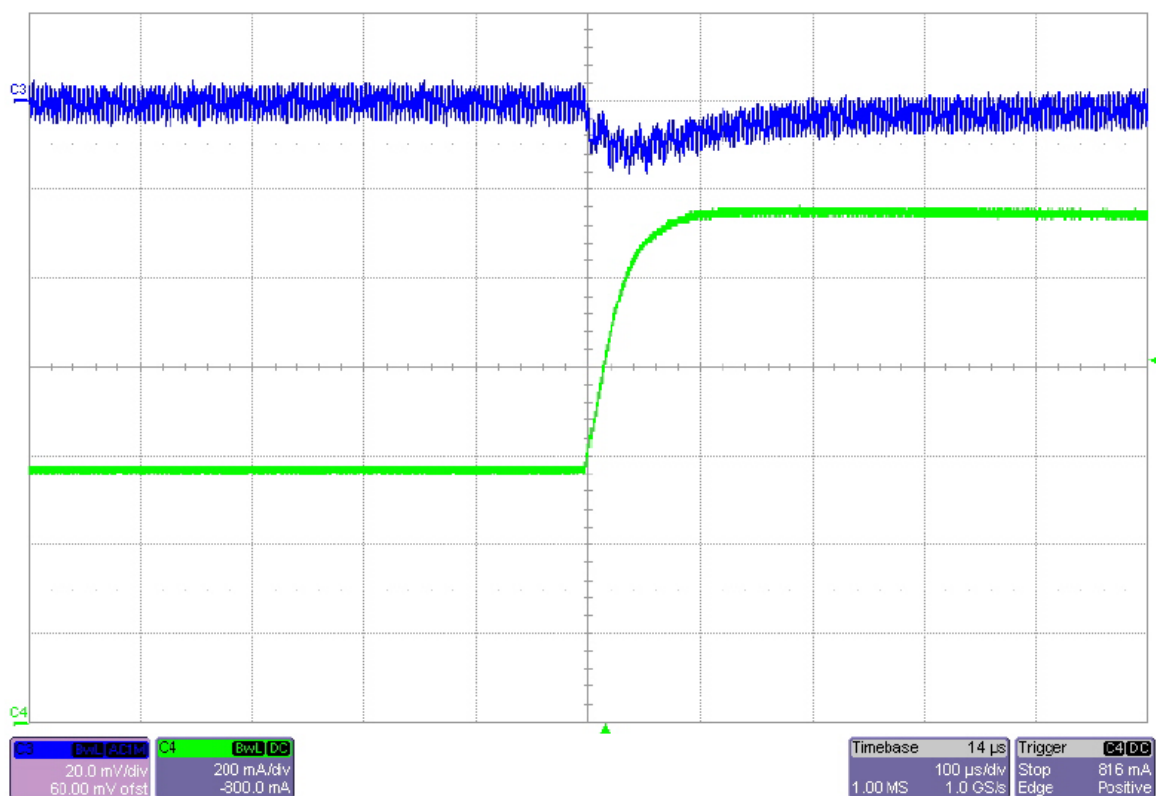


4 Load Transient (1.28V)

The photo below shows the 1.28V output voltage when the load current is pulsed between 1.2A and 600mA. $V_{in} = 5V_{dc}$.



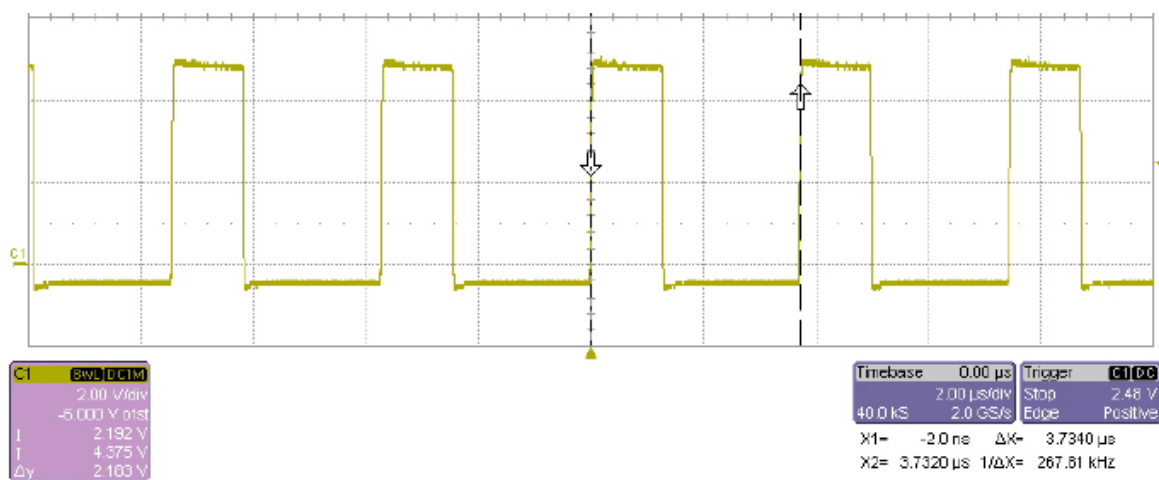
5 Load Transient (1.28V) Cont.



Positive Edge

6 Switch Node Waveform (1.28V)

The photo below is of the switching node waveform. The input voltage is 5V and the output is loaded to 1.2A.



12/19/2006

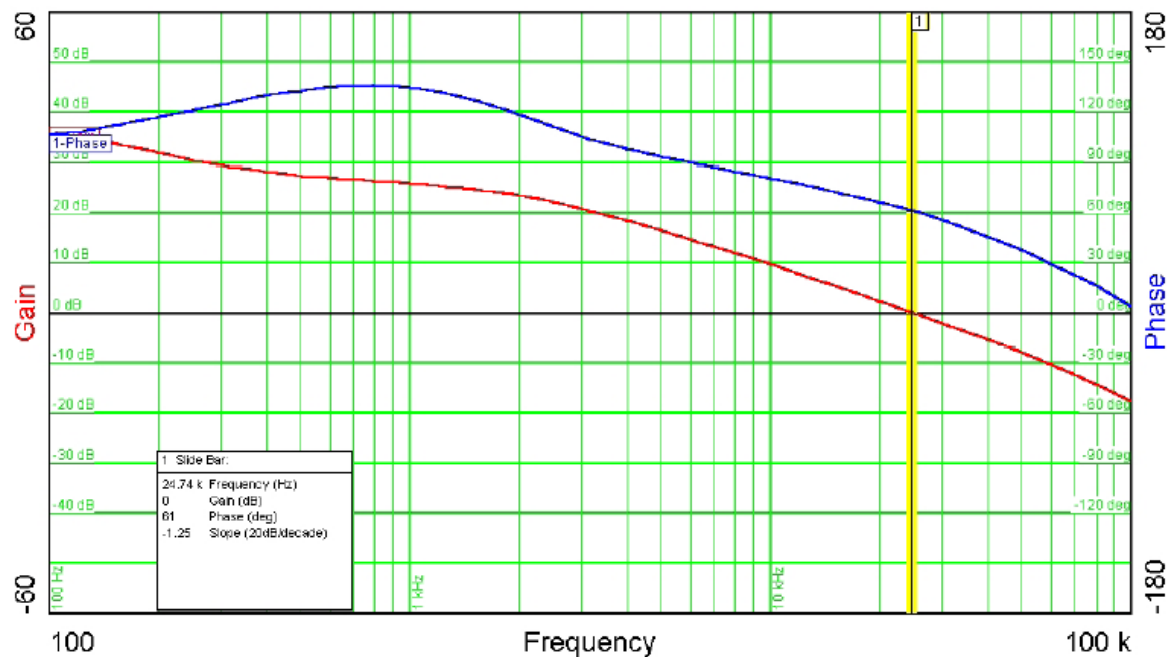
PMP2236 Rev A Test Results



1.28V@1.4A

7 Control Loop Gain / Stability (1.28V)

The plot below shows the loop gain and phase margin with output voltage set to 1.28V. The output was loaded to 1.2A. $V_{in} = 5V$ Bandwidth = 24.74kHz, Phase Margin = 61.00 degrees



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