Build on PCB PMP1278 rev C

J1
12 – 16V
GND
TP1
TP2
R1
86.6k
C5
100pF
R2
0.03
C4
22uF
R3
1.0K
U1
TPS40200D
RC
SS
COMP
FB
VDD
ISNS
DRV
GND
F = 330KHz
TP3
R4
0
RSQ035P03
Q2
Rohm
1500pF
C6
10pF
C7
R5
16.5k
330pF
C13
0.47uF
C14
C8
0.1uF
TP4
L1
15uHy
DO3316P-153
5V
TP5
J2
5V @ 2A
GND
1210
C12
22uF
D1
SS22
On Semi
TP6
TP7
C15
220pF
R6
4.99k
R7
100K
TP8
R8
49.9
TP9
R9
16.2K

Updated: May 8: Q2 to RSQ035P03 from incorrect On Semi part
C12 from size 0805 to 1210 to get lower esr

May 9: L1 from 10uHy (and 22uHy temporarily) to 15uHy DO3316
C6 and R5 updated to get Voltage loop crossover at 33kHz

5V off 14V

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| | | |
|---|---|---|
| Title | 14Vinput; Out: 5V TPS40200 | |
| Size B | Number PMP2391 | Rev A |
| Date May 9, 2007 | Drawn by J Mandelcorn | |
| Filename PMP2391_REVA.SCH | Sheet 1 of 1 | |

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