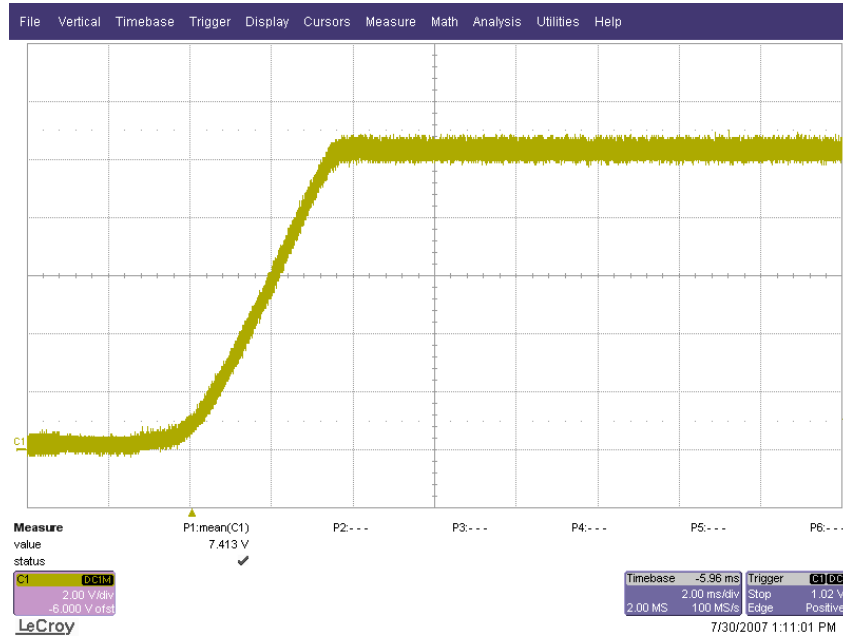


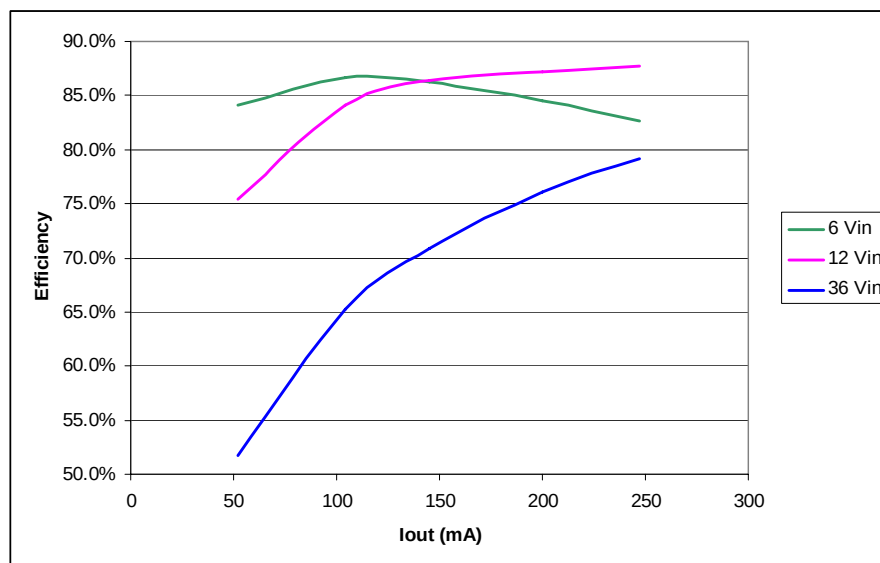
1 Startup

The startup waveform is shown in the figure below. The input voltage was set at 12V, with 0.2 A load on the output.



2 Efficiency

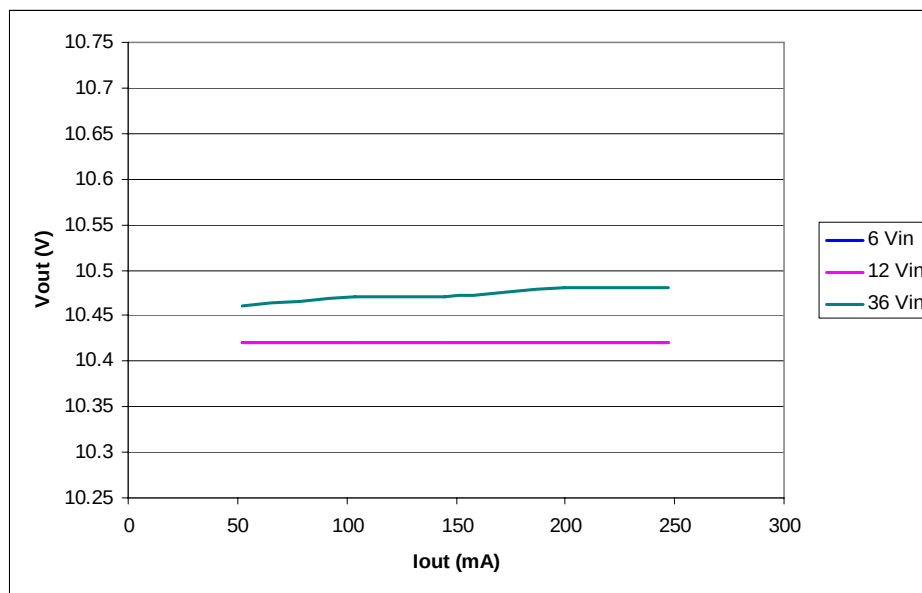
The efficiency is shown in the figure below.



PMP2778 Test Results

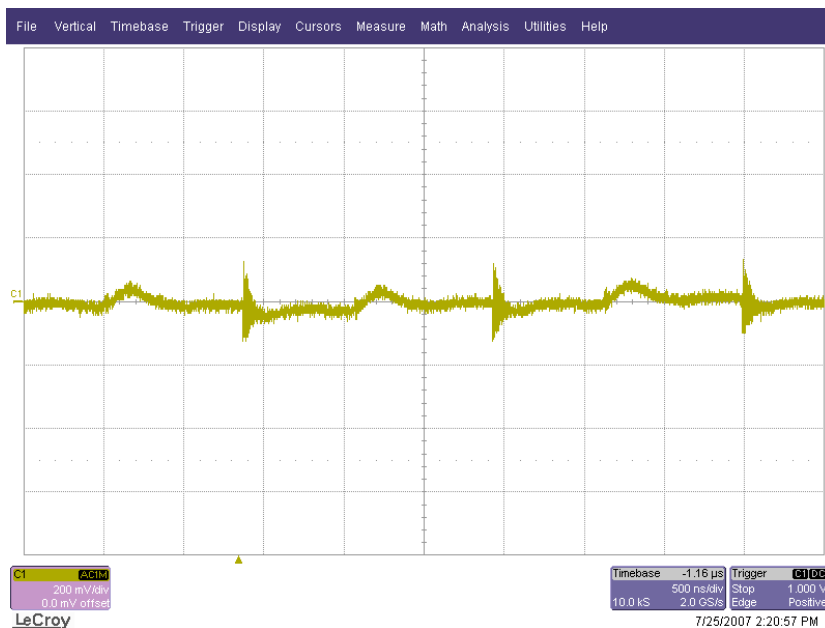
3 Load Regulation

The load regulation of the output is shown in the graph below.



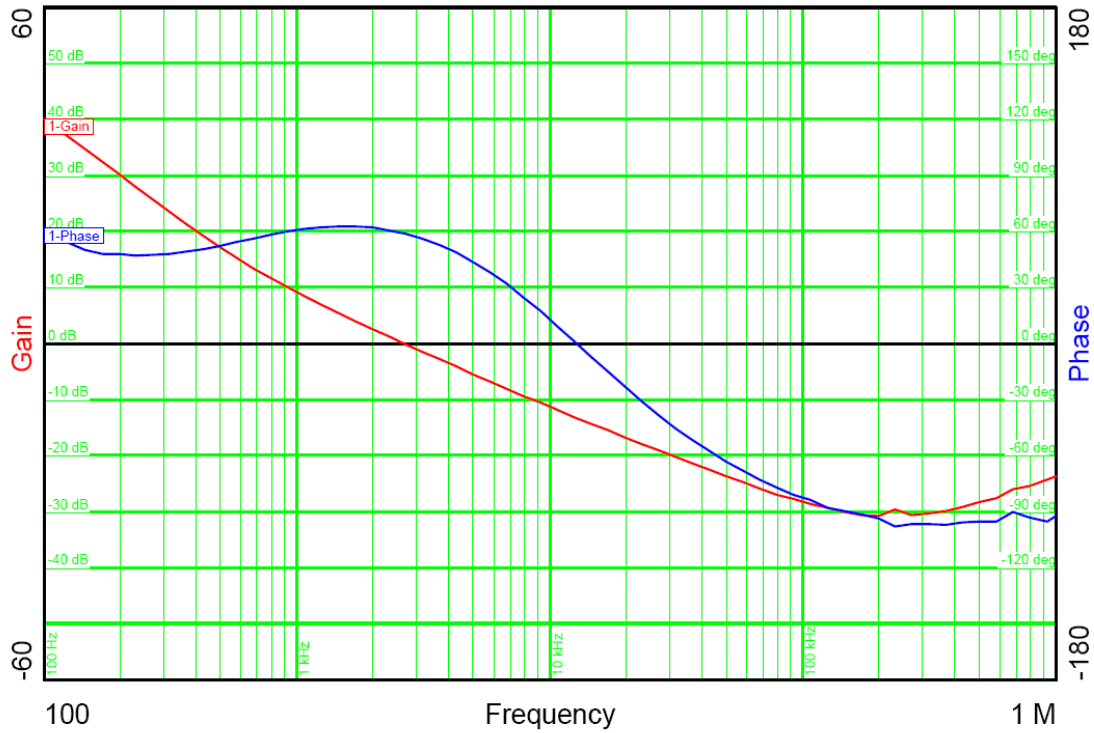
4 Output Ripple Voltage

The output ripple voltage is shown in the figure below. The image was taken with a 0.25 A load. 200 mV/div Vertical and 0.5 μ S/div Horizontal.



5 Control Loop Frequency Response

The figures below shows the loop response with a 12V input, and a 250mA load.



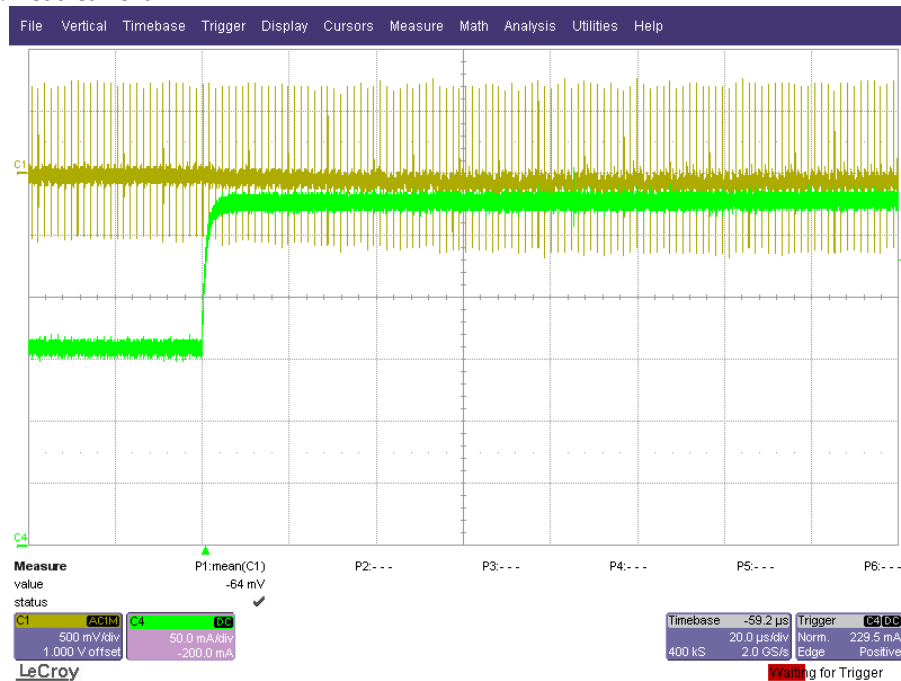
PMP2778 Test Results

6 Load Transients

The figures below show the response to load transients. The input voltage was set to 12V.

Channel 1 : Vout (AC coupled)

Channel 4 : Load current



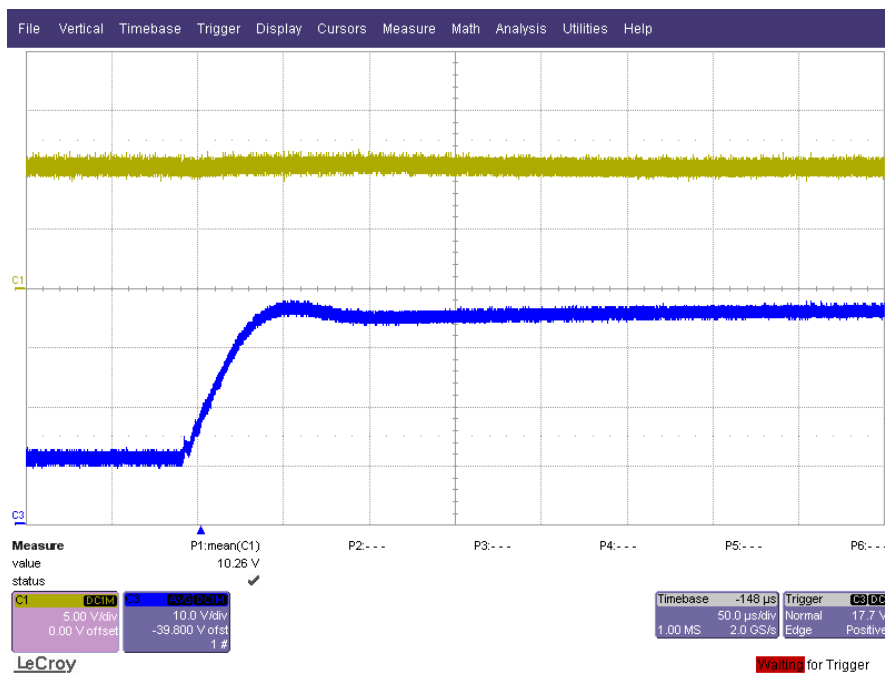
7 Switching Waveform

The following figure shows FET drain at 12 VDC input/0.25A output.



8 Load Dump

The following figure shows no overshoot of the output voltage as the input is stepped from 12 to 36 volts.



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