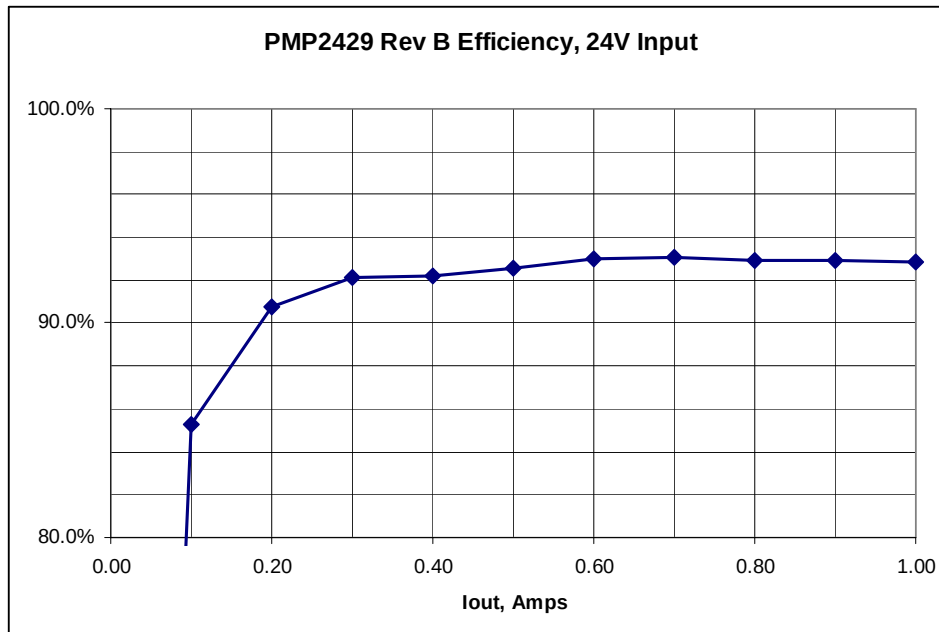


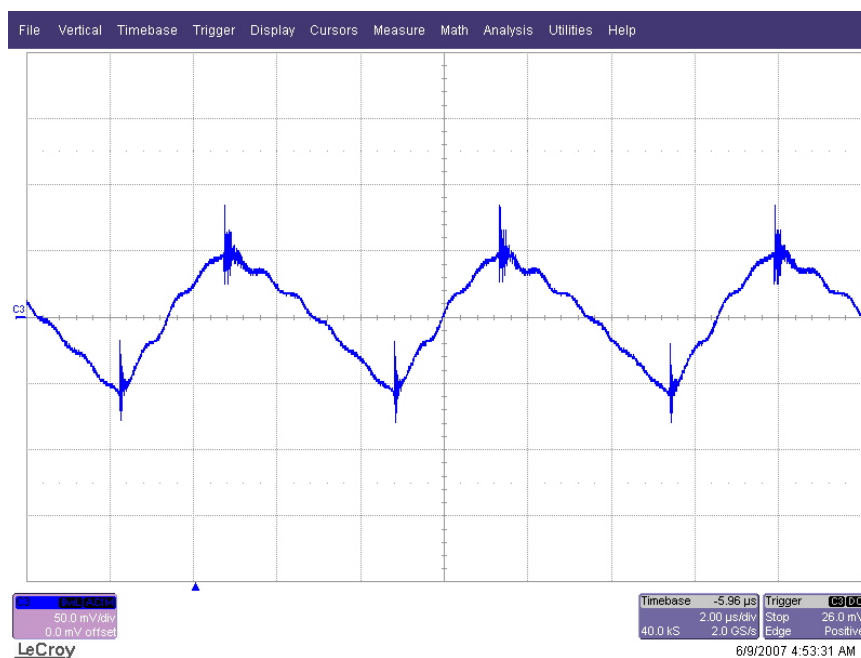
Efficiency

Efficiency vs output current:



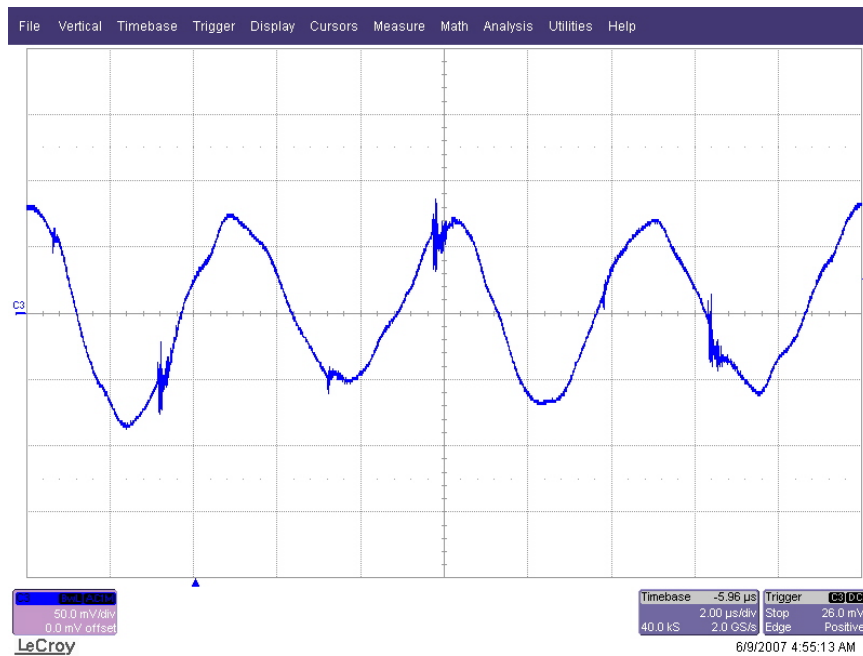
Output Voltage Ripple and Noise

Output ripple voltage with a 24 V input and a 1A load across J2 (20MHz BW):



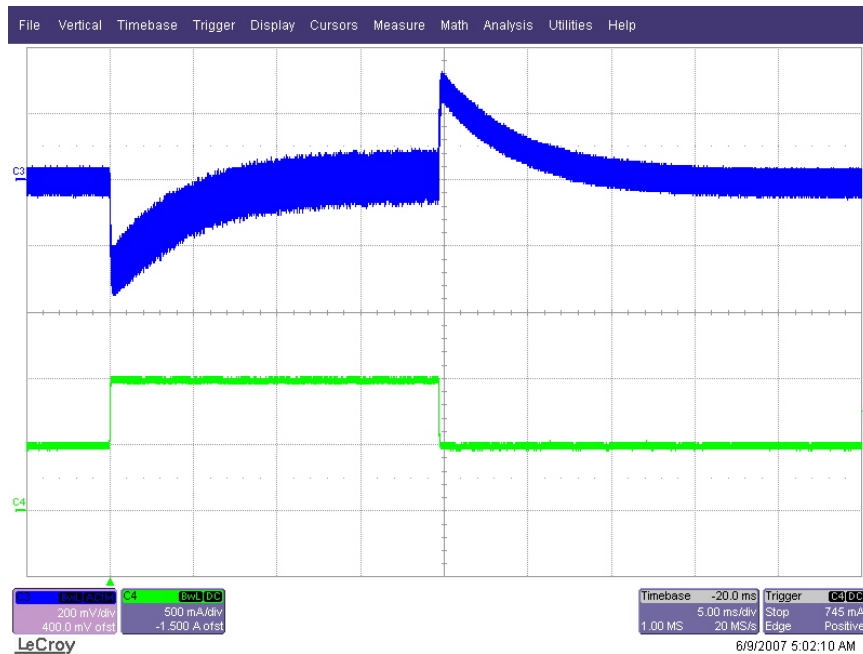
Input Voltage Ripple and Noise

Input ripple voltage with a 24 V input and a 1A load across J1 (20MHz BW):

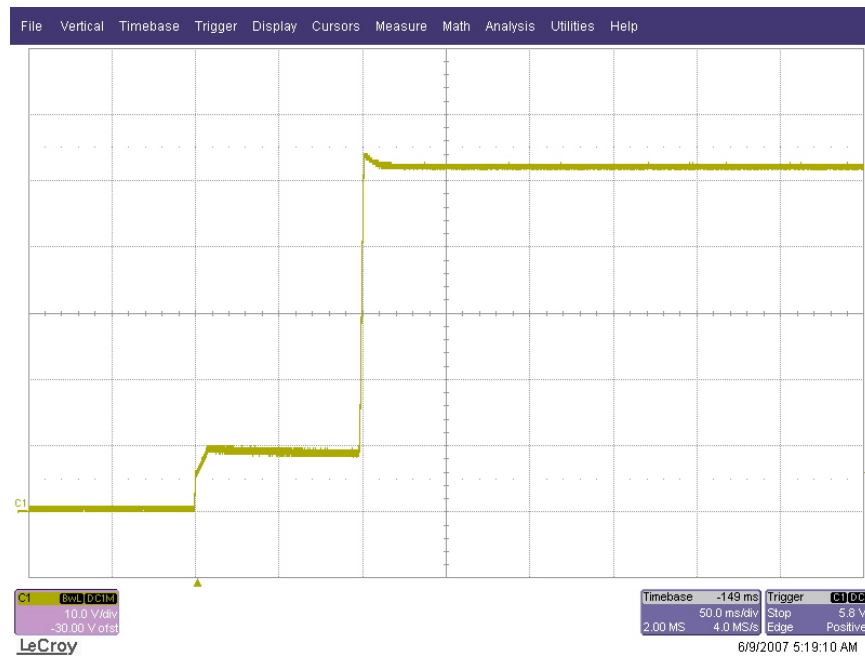


Dynamic Loading

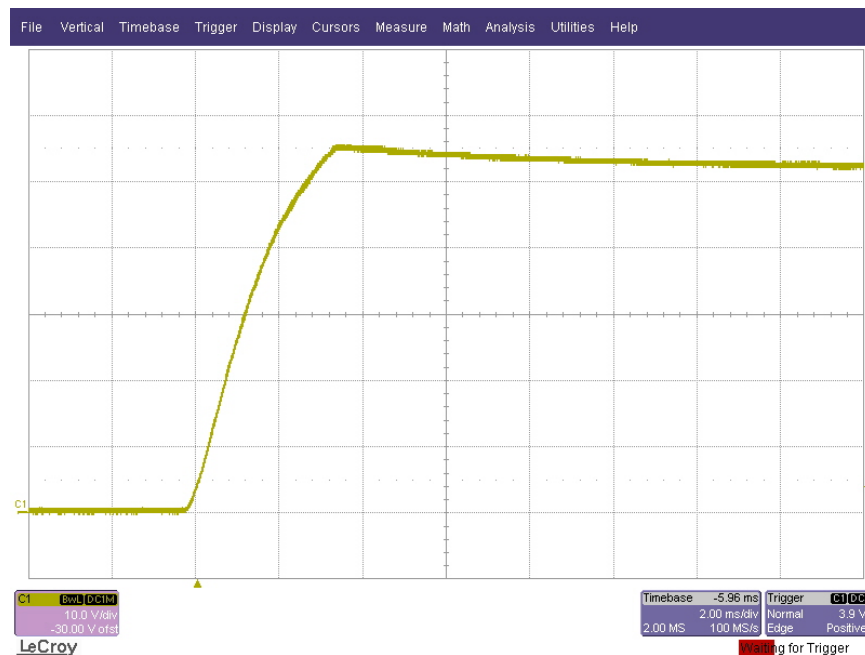
The output voltage transient response was measured with a load step from 0.5A to 1A:



The turn-on response with a 24 V input and 0A load is shown below:

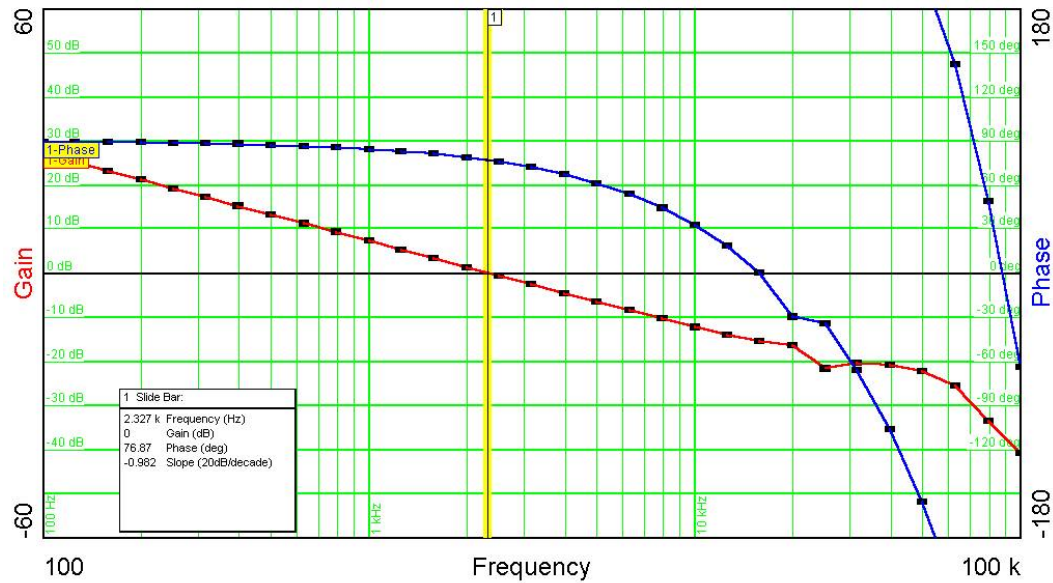


The turn-on response with a 24 V input and 5A load is shown below:



Stability Analysis (Loop Gain)

The figure below is the loop gain of the converter. The bandwidth is 2.3 KHz, the phase margin is 76 degrees, and the gain margin is 15 dB.



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