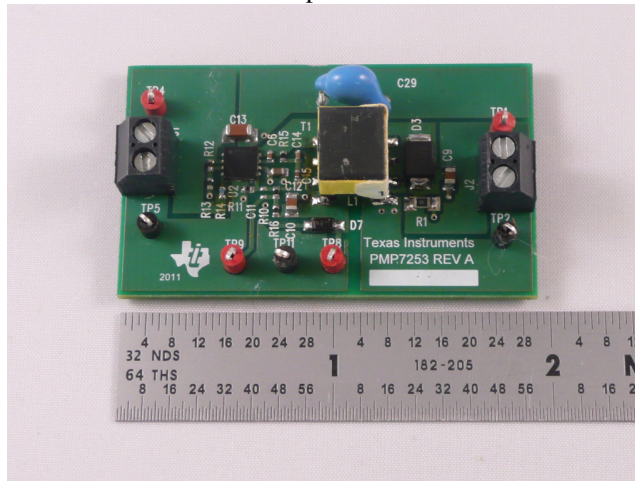
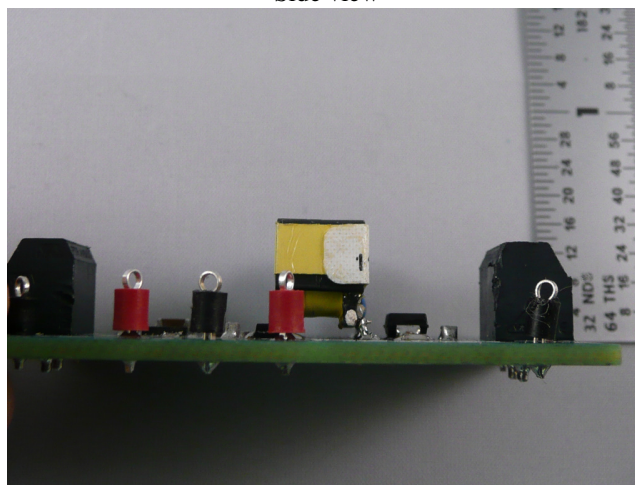


1 Photo

Top view

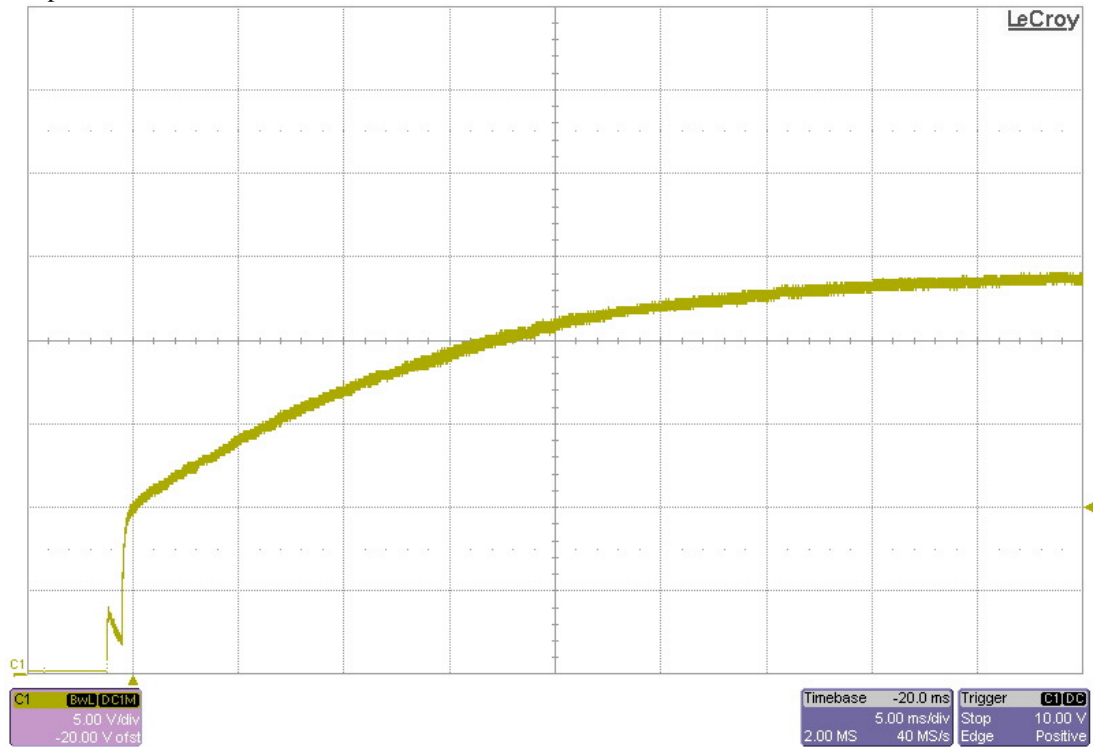


Side view



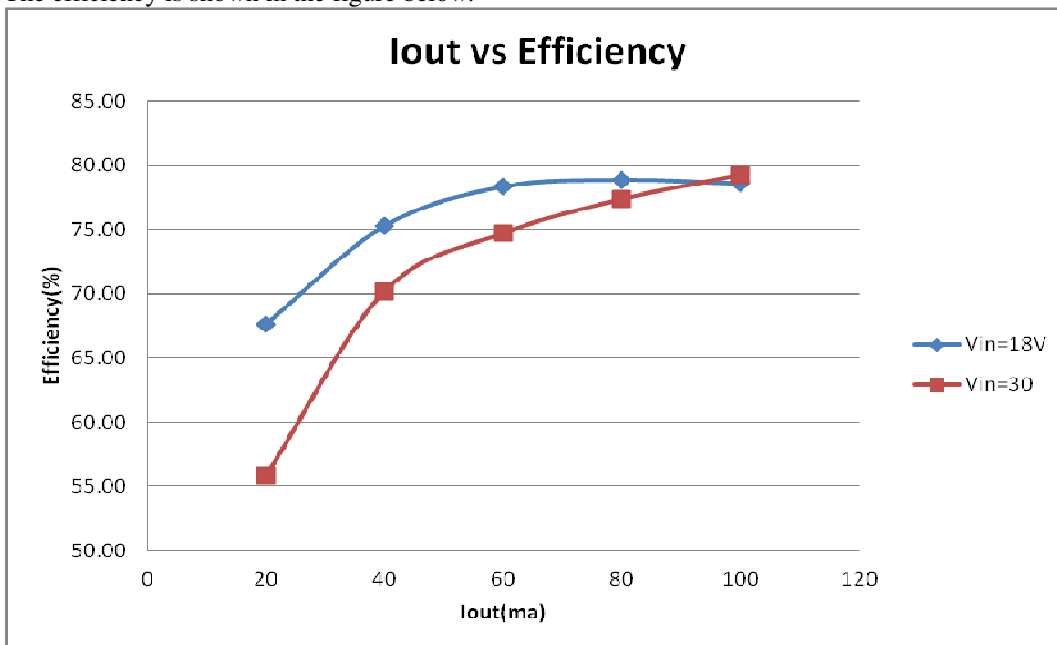
2 Startup

The startup waveform is shown in the figure below. The input voltage was set at 24V, with 0.050 A load on the output.



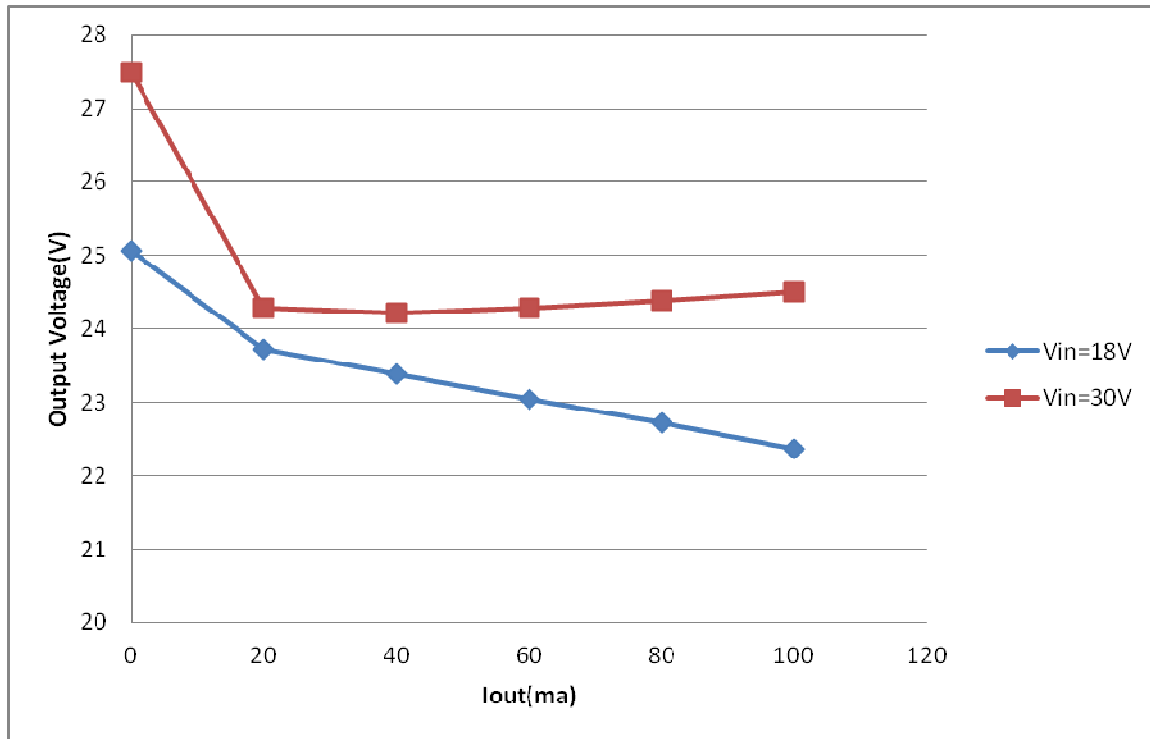
3 Efficiency

The efficiency is shown in the figure below.



4 Load Regulation

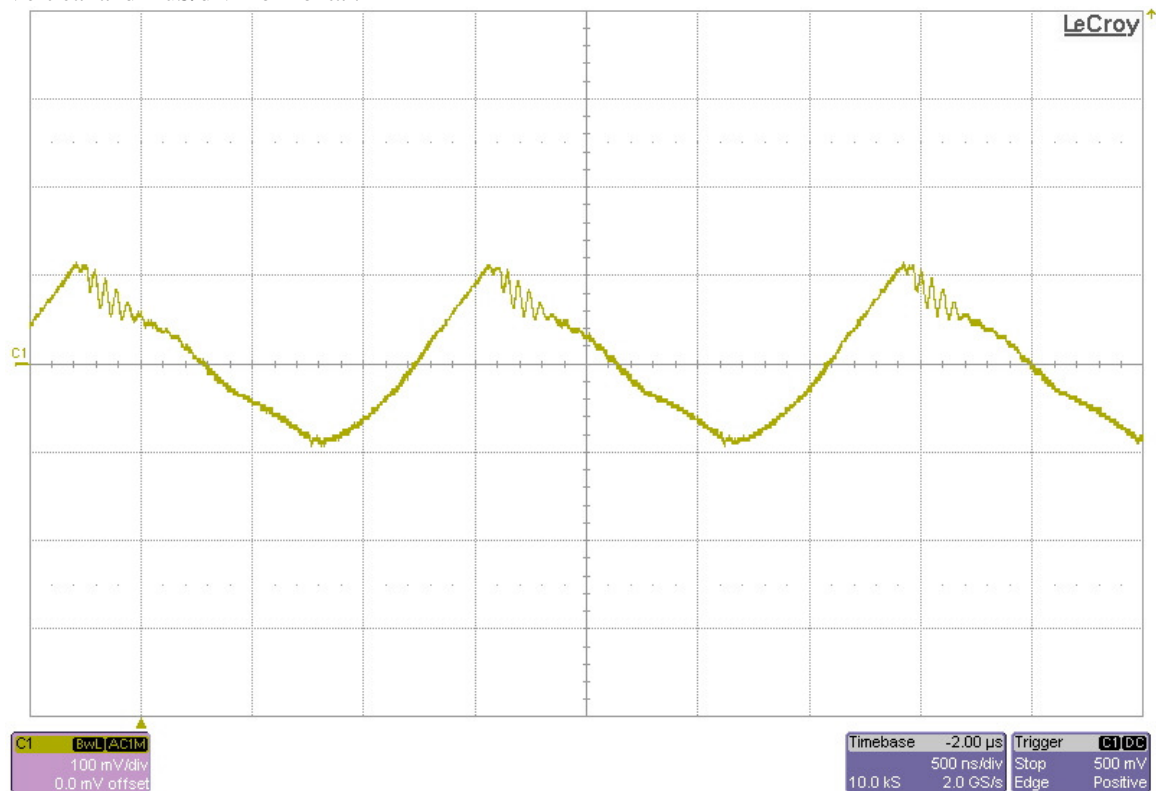
The load regulation of the output is shown in the graph below.



PMP7316 Test Results

5 Output Ripple Voltage

The output ripple voltage is shown in the figure below. The image was taken at 0.100 A out. 100 mV/div Vertical and 1 μ S/div Horizontal.



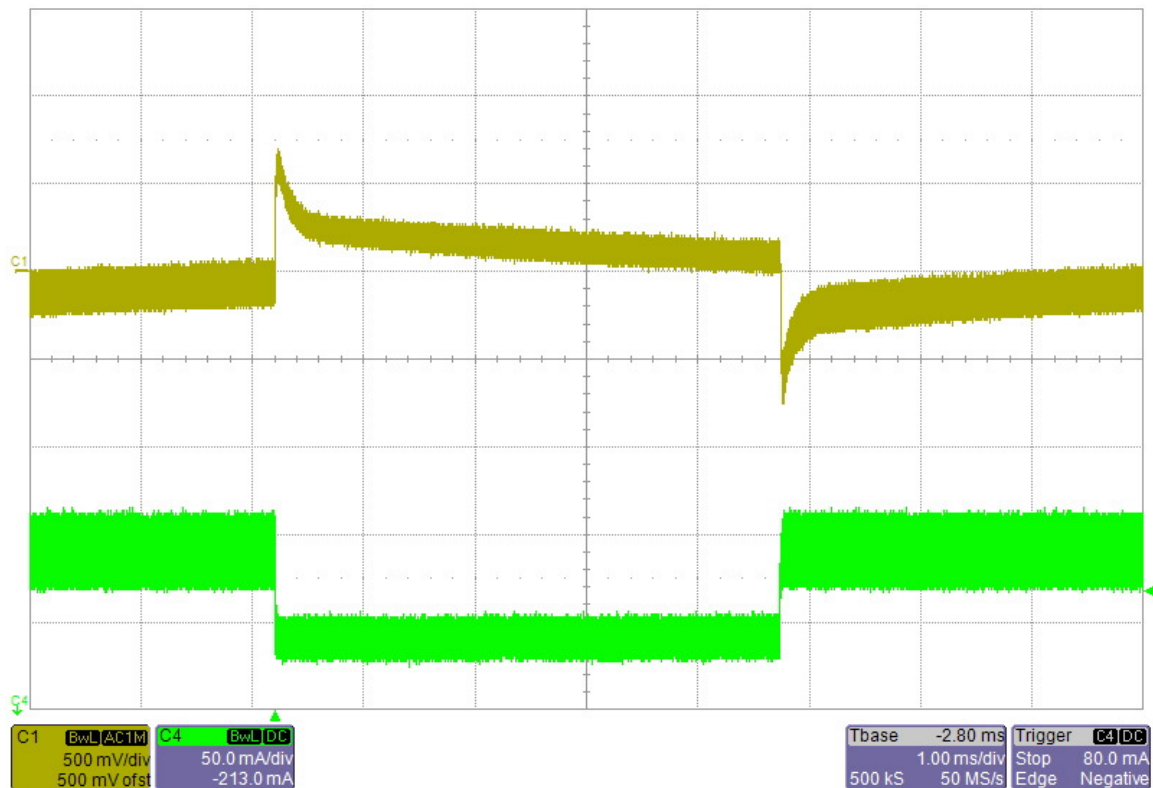
PMP7316 Test Results

6 Load Transients

The figures below show output response to load transients. The input voltage was set to 24V.

Channel 1 : Vout (AC coupled)

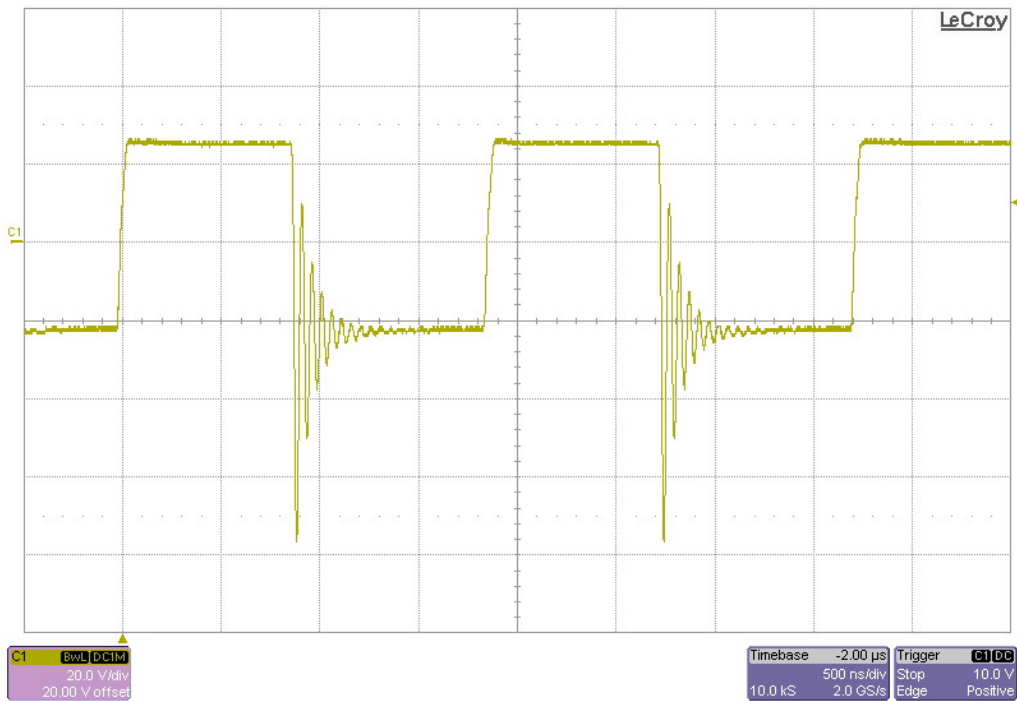
Channel 4 : Load current



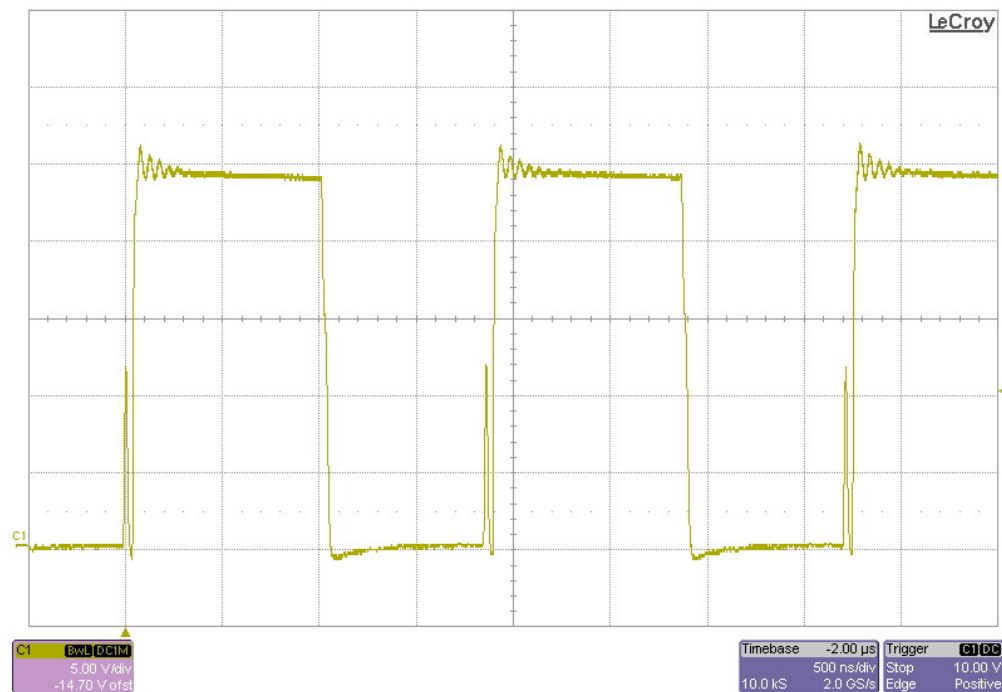
7 Switch Node Waveforms

The following figures show the switch node waveforms at 24 V_{in} and 0.100 A load

Output Diode Anode: Diode Stress ~100 Volts vs. 220 Volt Rating



Switch Node



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