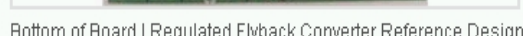


(ACTIVE) PMP8930.4

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The PMP8930 reference design uses the UCC28710 primary-side regulated flyback controller to generate a 20V output from a universal AC input. A bulk capacitor with slow-charge-fast-discharge circuit is placed at flyback output to provide long hold up time for the following step-down DC-DC stages. TPS54335 is used as the controller and power stage for the main 4V or 1.2V output. The valley switching of the UCC28710 allows this low-cost design to achieve a maximum load efficiency of 83%; no load losses are less than 70mW.



Get more information on the TI products in this reference design.

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	TO-220 (KCS) 3	Download	-
	(KTE) 3	Download	-
	TO-263 (KTT) 3	Download	Download
	SOT (DCY) 4	Download	Download

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