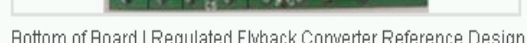


(ACTIVE) PMP8930 2

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The PMP8930 reference design uses the UCC28710 primary-side regulated flyback controller to generate a 20V output from a universal AC input, a bulk capacitor with slow-charge-fast-discharge circuit is placed at flyback output to provide long hold up time for the following step-down DC-DC stages. TPS54335 is used as the controller and power stage for the main 4V or 12V output. The valley switching of the UCC28710 allows this low-cost design to achieve a peak-to-peak efficiency of 99%, and a load regulation better than 20mV.



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