

18Vdc-72Vdc Input, 12V/10A Active Clamp Forward, 1/4 Brick

(ACTIVE) PMP8861

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Description

This reference design generates a 12V/10A output from a wide range 18V to 72V telecom input. The UCC2897A controls an active clamp forward converter power stage. The low gate charge and low RDSon of the CSD18533Q5A, implemented as a synchronous rectifier, provides for a highly efficient design. This design is laid out in a standard quarter brick footprint and achieves efficiencies near 95%.



Bottom of Board | Active Clamp Forward Reference Design Board



TI Devices (2)

Get more information on the TI products in this reference design.

Part Number ▲	Name	Product Family
CSD18533Q5A	60V N-Channel NexFET Power MOSFET	Power MOSFET
UCC2897A	Advanced Current-Mode Active Clamp PWM Controller	AC/DC and Isolated DC/DC Power Supply

Bill of Materials

Find the complete list of components in this reference design.



CAD/CAE Symbols

Part #	Package Pins	CAD File (.bxt)	STEP Model (.stp)
UCC2897A	 TSSOP (PW) 20	Download	Download
	 VQFN (RGP) 20	Download	-

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

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Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
 PMP8861Test Results		PDF	2583	02 Jul 2013	100

More Literature (2)

Title	Abstract	Type	Size (KB)	Date	Views
 PMP8861 BOM		PDF	79	02 Jul 2013	136
 PMP8861 Schematic		PDF	164	02 Jul 2013	192

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