Non-Isolated LDO for Industrial using TPS7A201 with Input of 19 VDC 17.2V@0.02A Status: ACTIVE

PMP7115.4

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Description

The PMP7115.4(17.2V@0.02A) reference design is a 13.2W LDO which uses a TPS7A201 and was intended for Industrial applications. It can generate a Non-Isolated 17.2 volt output at 0.02 Amps from an input of 19 Volts DC. This design has multiple outputs.

Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP7115 Test Report		PDF	1788	26 Sep 2012	181

More Literature (2)

Title	Abstract	Type	Size (KB)	Date	Views
PMP7115 BOM		PDF	99	26 Sep 2012	98
PMP7115 Schematic		PDF	108	26 Sep 2012	193

Related Products

TI Devices (1)

Part Number	Name	Product Family
TPS7A4201	28V Input, 50mA, Single Output Low-Dropout Linear Regulator	Linear Regulator (LDO)

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Training & Events

Name	Type	Available During
In-depth PFC Design Workshop sponsored by Biricha Digital Power	In-Person	15 Mar 2014 - 22 Mar 2014
In-depth PFC Design Workshop	In-Person	12 Nov 2013 - 14 Nov 2013
Digital Power Multi-Day Workshop	In-Person	05 Nov 2013 - 07 Nov 2013
Power Fundamentals: Switching Power Supply Design Considerations - Managing Thermal Performance	Recorded Content	On Demand
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