

Active Clamp Forward, Quarter Brick (18Vdc-60Vdc Input, 3.3V/12A)

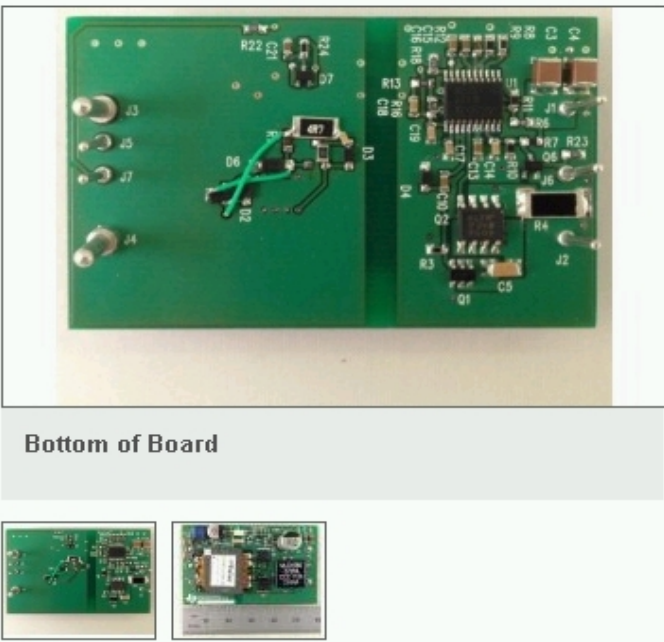
Status: ACTIVE

PMP8790

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Description

This reference design generates a 3.3V/12A output from a wide 18V-60V telecom input. The UCC2897A controls an active clamp forward converter power stage. The low gate charge and low RDSon of the CSD17501Q5A and CSD18502Q5B, implemented as self-driven synchronous rectifiers, provide for an efficient design. This design is laid out in a standard quarter brick footprint and includes enable, positive rail remote sense, and output trimming features.



Technical Documents

User Guides (2)

Title	Abstract	Type	Size (KB)	Date	Views
 PMP8790 PCB		PDF	250	25 Feb 2013	57
 PMP8790 Test Results		PDF	2555	25 Feb 2013	58

More Literature (2)

Title	Abstract	Type	Size (KB)	Date	Views
 PMP8790 BOM		PDF	18	25 Feb 2013	39
 PMP8790 Schematic		PDF	73	25 Feb 2013	53

Related Products

Software (1)

 [PMP8790 Gerber](#)
(ZIP, 301 KB) 35 views, 25 Feb 2013

TI Devices (4)

Part Number	Name	Product Family
CSD17501Q5A	30V, N-Channel NexFET™ Power MOSFET with 20 Volt Vgs	Power MOSFET
CSD18502Q5B	40-V, N-Channel NexFET™ Power MOSFET	Power MOSFET
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Training & Events

Name	Type	Available During
In-depth PFC Design Workshop sponsored by Biricha Digital Power	In-Person	15 Mar 2014 - 22 Mar 2014
In-depth PFC Design Workshop	In-Person	12 Nov 2013 - 14 Nov 2013
Digital Power Multi-Day Workshop	In-Person	05 Nov 2013 - 07 Nov 2013
Power Fundamentals: Switching Power Supply Design Considerations - Managing Thermal Performance	Recorded Content	On Demand
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