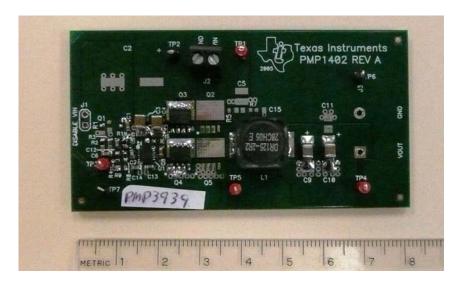


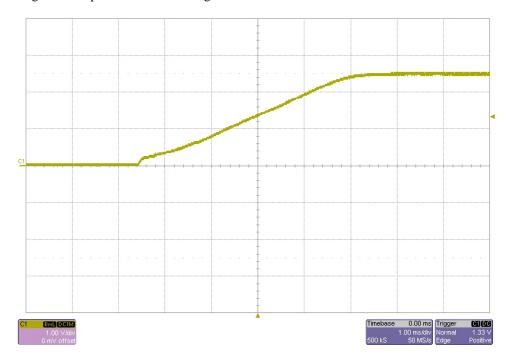
1 Photo

The photograph below shows the top view of the PMP3939 Rev B demo board. The circuit is built on a PMP1402 Rev A PWB.



2 Startup

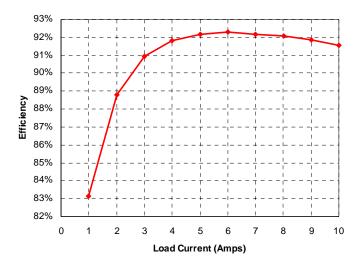
The output voltage at startup is shown in the image below.





3 Efficiency

The efficiency data is shown in the tables and graph below.



lout	Vout	Vin	lin	Pout	Losses	Efficiency
0.000	2.494	12.0	0.037	0.00	0.444	0.0%
0.996	2.494	12.0	0.249	2.48	0.504	83.1%
1.999	2.494	12.0	0.468	4.99	0.630	88.8%
2.989	2.494	12.0	0.683	7.45	0.741	91.0%
3.997	2.495	12.0	0.905	9.97	0.887	91.8%
5.01	2.495	12.0	1.130	12.50	1.060	92.2%
6.01	2.495	12.0	1.354	14.99	1.253	92.3%
6.99	2.495	12.0	1.577	17.44	1.484	92.2%
8.01	2.496	12.0	1.809	19.99	1.715	92.1%
9.01	2.497	12.0	2.041	22.50	1.994	91.9%
10.00	2.498	12.0	2.274	24.98	2.308	91.5%

4 Thermal Image

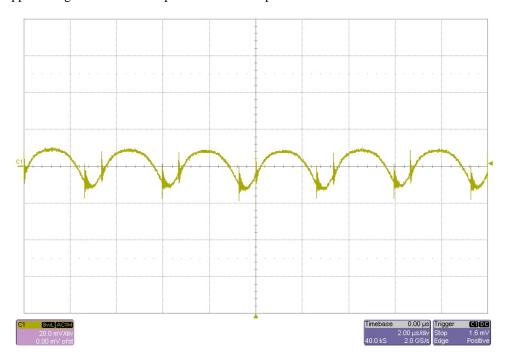
A thermal image of the top side of the board is shown with a 10A load. The ambient temperature was 27°C, with no forced air flow. The bottom MOSFET (Q4) was the hottest component on the board and measured 62.6°C.





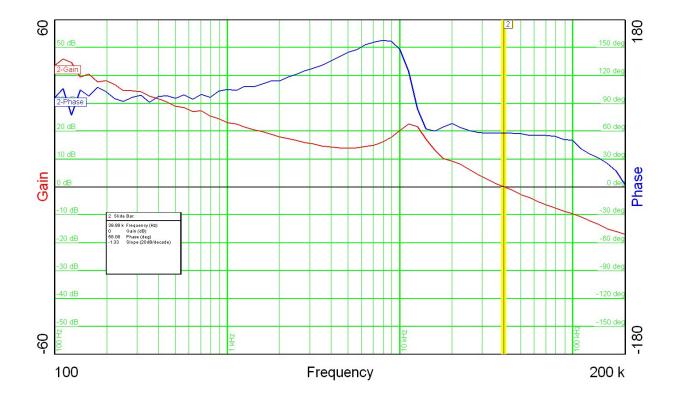
5 Output Ripple Voltage

The output ripple voltage is shown in the plot below. The input was set to 12Vand the load was set to 10A.



6 Frequency Response

The frequency response of the feedback loop is shown below. The input was set to 12Vand the load was set to 10A.





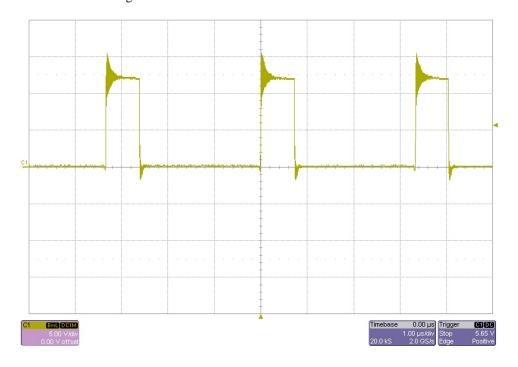
7 Load Transients

The image below shows the response to a 5A to 10A load transient with the input was set to 12V.



8 Switching Waveform

The image below shows the voltage on TP5 with a 10A load.



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