

Sync Buck for Intel Atom E6xx Tunnel Creek (3.3V @ 5A)

(ACTIVE) PMP5855.3

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Description

The PMP5855 is designed to use a regulated 12V (8V-14V) bus to produce 10 regulated outputs in sequence for an Atom E6xx Tunnel Creek Power System. The PMP5855 is specially designed to demonstrate the TP559610 Atom E6xx CPU and GPU Vcore regulators while providing a number of test points to evaluate their static and dynamic performance. PMP5855 provides hotswap protection and input OR-ing.

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Bill of Materials (BOM)

Find the complete list of components in this reference design.

 Download BOM

Parametrics

	PMP5855.3
V _{in} (Min) (V)	8
V _{in} (Max) (V)	14
V _{out} (Nom) (V)	3.3
I _{out} (Max) (A)	5
Output Power	16.5
Isolated/Non-Isolated	Non-Isolated
Input Type	DC
Topology	Buck- Synchronous Buck- Integrated Switch

TI Devices (1)

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Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
TP559610	Single Phase, D-CAP™ Sync Buck Controller for IMVP6+ CPU and Graphics Vcore in Embedded Computing	DC/DC Switching Regulator	Sample & Buy	View Design Kits & Evaluation Modules

Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP5855 Test Results		PDF	542	18 May 2011	16

Design Files (8)

Title	Abstract	Type	Size (KB)	Date	Views
 PMP5855 PCB (Daughter Card)		PDF	846	18 May 2011	16
 PMP5855 PCB (EVM)		PDF	1549	18 May 2011	14
PMP5855 Gerber (Daughter Card)		ZIP	336	18 May 2011	9
PMP5855 Gerber (EVM)		ZIP	724	18 May 2011	12
 PMP5855 BOM (Daughter Card)		PDF	25	18 May 2011	27
 PMP5855 BOM (EVM)		PDF	37	18 May 2011	26
 PMP5855 Schematic (Daughter Card)		PDF	111	18 May 2011	27
 PMP5855 Schematic (EVM)		PDF	466	18 May 2011	29

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