10 - 28V Input, 12V@2A SEPIC using the LM5022

(ACTIVE) PMP7305

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Description

The PMP7305 reference design uses the LM5022 in a SEPIC configuration to generate 12V@2A. The design offers efficiency of up to 90% and has a small size.

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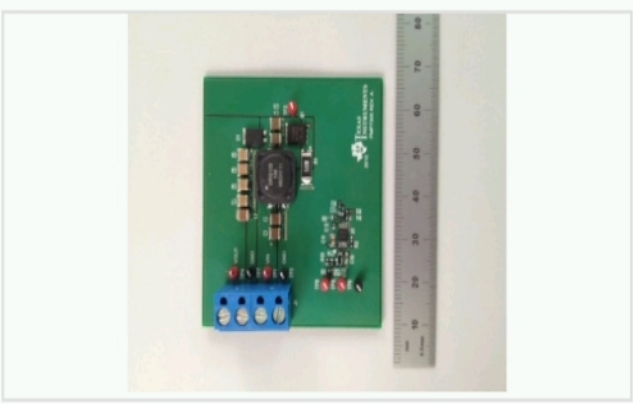

Top view

Fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

Bill of Materials (BOM)

Find the complete list of components in this reference design.

Download BOM

Parametrics

	PMP7305
Vin (Max) (V)	28
Vin (Min) (V)	10
Vout (Nom) (V)	12
Iout (Max) (A)	2
Output Power	24
Isolated/Non-Isolated	Non-Isolated
Input Type	DC
Topology	Sepic- CCM

TI Devices (1)

Order samples, get tools and find more information on the TI products in this reference design.

Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
LM5022	6-60V Wide Vin, Current Mode Boost and SEPIC Controller	Controller (External Switch)	Sample & Buy	View Design Kits & Evaluation Modules

CAD/CAE symbols

Part #	Package | Pins	CAD File (.bxl)	STEP Model (.stp)
LM5022	VSSOP (DGS) | 10	Download	Download

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

Step 1: Download and install the Ultra Librarian software ZIP: 62MB

Step 2: Download the Symbol and Footprint .bxl file.

- read more -

Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP7305 Test Results		PDF	937	10 Feb 2012	53

Design Files (4)

Title	Abstract	Type	Size (KB)	Date	Views
PMP7305 PCB		PDF	139	10 Feb 2012	37
PMP7305 Gerber		ZIP	326	10 Feb 2012	17
PMP7305 BOM		PDF	27	10 Feb 2012	44
PMP7305 Schematic		PDF	52	10 Feb 2012	106

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