

(ACTIVE) PMP5783.3

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PMP5783 is a complete Intel IMVP6.5 Arrandale SV/Clarksfield SV Power Reference Design. Vin is 8-15V. Main CPU is powered by two phase TP559621. TP559611 powers graphics processor. DDR3 memory powered by TP55126. Both 1.05V's are each powered by TP551218. Dual TP551125A powers 5V and 3.3V. Each of above power trains uses dual NexFET CSD86350. TP562290 powers 1.8V's.

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1 Fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

Find the complete list of components in this reference design.

	PMP5783.3
Vin (Min) (V)	8
Vin (Max) (V)	15
Vout (Nom) (V)	5
Iout (Max) (A)	10
Output Power	50
Isolated/Non-Isolated	Non-Isolated
Input Type	DC
Topology	Buck- Synchronous

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Part #	Package Pins	CAD File (.bxl)	STEP Model (-stp)
CSD86330Q3D	LSON-CLIP (DQZ) 8	Download	-
CSD86350Q5D	LSON-CLIP (DQY) 8	Download	-
TP551116	HTSSOP (PWP) 20	Download	Download
	VQFN (RGE) 24	Download	-
TP551124	VQFN (RGE) 24	Download	-
TP551125A	VQFN (RGE) 24	Download	-
TP562290	WSON (DRV) 6	Download	-

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Title	Abstract	Type	Size (KB)	Date	Views
PMP5783 Test Results		PDF	135	18 May 2011	33

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PMP5783 PCB		PDF	1211	18 May 2011	18
PMP5783 Gerber		ZIP	676	18 May 2011	14
PMP5783 BOM		PDF	34	18 May 2011	46
PMP5783 Schematic		PDF	672	18 May 2011	54

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