18Vdc-60Vdc Input, 9.6V @ 15A Output, 94% Efficient Active Clamp Forward

(ACTIVE) PMP6933

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Description

The PMP6933 reference design provides 9.6V at 15.6A (150W) from a wide range 18V-60V telecom input with greater than 94% efficiency. This design uses the UCC2897A active clamp controller with self-driven synchronous rectifiers to achieve high efficiency.

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Top View

Fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

Bill of Materials (BOM)

Find the complete list of components in this reference design.

Download BOM

Parametrics

	PMP6933
Vin (Max) (V)	60
Vin (Min) (V)	18
Vout (Nom) (V)	9.6
Iout (Max) (A)	15.6
Output Power	149.76
Isolated/Non-Isolated	Isolated
Input Type	DC
Topology	Forward- Active Clamp Forward- Synchronous

TI Devices (2)

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Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
TL431	Adjustable Precision Shunt Regulator	Voltage Reference	Sample & Buy	Not Available
UCC2897A	Advanced Current-Mode Active Clamp PWM Controller	AC/DC and Isolated DC/DC Power Supply	Sample & Buy	View Design Kits & Evaluation Modules

CAD/CAE symbols

Part #	Package | Pins	CAD File (.bxl)	STEP Model (.stp)
TL431	SOT (DBZ) | 3	Download	Download
	TO-92 (LP) | 3	Download	Download
	SIP (PK) | 3	Download	-
	SOT (DBV) | 5	Download	Download
	SOT (DCK) | 6	Download	Download
	SOIC (D) | 8	Download	Download
	PDIP (P) | 8	Download	Download
	SOP (PS) | 8	Download	-
	TSSOP (PW) | 8	Download	-
UCC2897A	TSSOP (PW) | 20	Download	Download
	VQFN (RGP) | 20	Download	-

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Step 1: Download and install the Ultra Librarian software ZIP: 62MB

Step 2: Download the Symbol and Footprint .bxl file.

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Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP6933 Test Results		PDF	2482	03 May 2012	19

Design Files (2)

Title	Abstract	Type	Size (KB)	Date	Views
PMP6933 Bill of Materials		PDF	30	03 May 2012	22
PMP6933 Schematic		PDF	100	03 May 2012	36

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