14V-35V Input, 12V/7A5, 5V/100mA, Active Clamp

(ACTIVE) PMP5549

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Description

This Active clamp Forward supplies two outputs, 12V @ 7.5A and 5V @ 100mA, with a LDO, starting from a 14V...35V input source, reaching 94% efficiency with sync. Rectification.

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Bill of Materials (BOM)

Find the complete list of components in this reference design.

Download BOM

Parametrics

	PMP5549
Vin (Max) (V)	35
Vin (Min) (V)	14
Vout (Nom) (V)	12
Iout (Max) (A)	7.5
Output Power	90
Isolated/Non-Isolated	Isolated
Input Type	DC
Topology	Forward- Synchronous Linear Regulator

TI Devices (2)

Order samples, get tools and find more information on the TI products in this reference design.

Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
LP2985-50	Single Output LDO, 150mA, Fixed(5.0V), 1.5% Tolerance, Low Quiescent Current, Low Noise	Linear Regulator (LDO)	Sample & Buy	View Design Kits & Evaluation Modules
UCC2897	Current Mode Active Clamp PWM Controller	AC/DC and Isolated DC/DC Power Supply	Sample & Buy	Not Available

CAD/CAE symbols

Part #	Package | Pins	CAD File (.bxl)	STEP Model (.stp)
LP2985-50	SOT (DBV) | 5	Download	Download

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

Step 1: Download and install the Ultra Librarian software ZIP: 62MB

Step 2: Download the Symbol and Footprint .bxl file.

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Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP5549 Test Results		PDF	255	24 Oct 2011	22

Design Files (2)

Title	Abstract	Type	Size (KB)	Date	Views
PMP5549 BOM		PDF	31	24 Oct 2011	25
PMP5549 Schematic		PDF	110	24 Oct 2011	33

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