LED Testboard

(ACTIVE) PMP5557

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Description

The PMP5557 is a reference design for a testboard. It's a sepic topology.

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Bill of Materials (BOM)

Find the complete list of components in this reference design.

Download BOM

Parametrics

	PMP5557
Vin (Max) (V)	15
Vin (Min) (V)	14
Vout (Nom) (V)	3.3
Iout (Max) (A)	.5
Output Power	1.65
Isolated/Non-Isolated	Non-Isolated
Input Type	DC
Topology	Sepic- CCM

TI Devices (1)

Order samples, get tools and find more information on the TI products in this reference design.

Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
TPS40210	Wide Input Range Current Mode Boost Controller	Controller (External Switch)	Sample & Buy	View Design Kits & Evaluation Modules

CAD/CAE symbols

Part #	Package | Pins	CAD File (.bxl)	STEP Model (.stp)
TPS40210	HVSSOP (DGQ) | 10	Download	Download
	VSON (DRC) | 10	Download	-

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Step 1: Download and install the Ultra Librarian software ZIP: 62MB

Step 2: Download the Symbol and Footprint .bxl file.

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Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP5557 Test Results		PDF	539	14 Oct 2011	12

Design Files (2)

Title	Abstract	Type	Size (KB)	Date	Views
PMP5557 BOM		PDF	30	14 Oct 2011	13
PMP5557 Schematic		PDF	48	14 Oct 2011	27

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