Core voltage ~1.0V at up to 15A for Nyquist uP; 4 of them per UCD9244 plus Test load for one output

(ACTIVE) PMP6594

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Description

PMP6594 is a UCD9244 digital controller with four UCD74110 drive / power stages for core voltages (~1VDC) with VID requirements. Input can be 5V or 9VDC. VID is where the processor tells the power supply what voltage it wants for its main functions to optimize speed and power loss in the processor. 4, 6 and 8 bit VID schemes are supported.

TI's Standard Terms and Conditions for Evaluation Modules apply.

Fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

Bill of Materials (BOM)

Find the complete list of components in this reference design.

Download BOM

Parametrics

	PMP6594
Vin (Min) (V)	4.5
Vin (Max) (V)	10
Vout (Nom) (V)	1.2
Iout (Max) (A)	15
Output Power	18
Isolated/Non-Isolated	Non-Isolated
Input Type	DC
Topology	Buck- Synchronous

TI Devices (2)

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Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
UCD9244	Digital PWM System Controller	Digital Power Control Solutions	Sample & Buy	Not Available
UCD9244-EP	Digital PWM System Controller with 4-bit, 6-bit, or 8-bit VID Support	Digital Power Control Solutions	Sample & Buy	Not Available

CAD/CAE symbols

Part #	Package | Pins	CAD File (.bxl)	STEP Model (.stp)
UCD9244	VQFN (RGC) | 64	Download	-

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

Step 1: Download and install the Ultra Librarian software ZIP: 62MB

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Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP6594 Test Results		PDF	155	16 Aug 2011	30

Selection Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
Complete PMBus Portfolio (Rev. A)		PDF	473	20 Jun 2014	861

Design Files (2)

Title	Abstract	Type	Size (KB)	Date	Views
PMP6594 BOM		PDF	71	05 Aug 2011	51
PMP6594 Schematic		PDF	366	05 Aug 2011	68

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