220V-400VDC Input, 12V/100W Active Clamp Forward

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220V-400VDC Input, 12V/100W Active Clamp Forward Evaluation Board

Description

This reference design generates an isolated 12V/100W output from a high voltage DC input. This design is intended to operate from the output of a PFC front end regulator. The UCC2897A IC is used to control an active clamp forward converter. Self-driven synchronous rectifiers allow for over 91% efficiency. An accurate current limit is provided by directly sensing the output current. A clock pulse output is also provided for synchronizing with other converters.

Features

- 220V-400VDC to 12V/8.333A Output (100W)
- 91% efficiency
- Output Current Sensing
- Features UCC2897A Controller along with TI NexFET Synchronous Rectifiers
- Active Clamp Forward Topology
- Reduced conduction losses with synchronous rectifiers

fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

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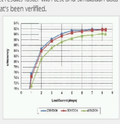
Schematic/Block Diagram

Quickly understand overall system functionality.

Download Schematic

Test Data

Get results faster with test and simulation data that's been verified.



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Design Files

Download ready-to-use system files to speed your design process. Get Viewer.

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Bill of Materials (BOM)

Find the complete list of components in this reference design.

Download BOM

Parametrics

	PMP6961
Vin (Max) (V)	400
Vin (Min) (V)	220
Vout (Nom) (V)	12
Iout (Max) (A)	8.3
Output Power	99.6
Isolated/Non-Isolated	Isolated
Input Type	DC
Topology	Forward- Active Clamp Forward- Synchronous

TI Devices (5)

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Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
CSD18531Q5A	60V N-Channel NexFET Power MOSFETs, CSD18531Q5A	Power MOSFET	Sample & Buy	View Design Kits & Evaluation Modules
SN74AHCT74	Dual Positive-Edge-Triggered D-Type Flip-Flops With Clear and	Flip-Flops/Latchs/Register	Sample & Buy	Not Available

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CAD/CAE symbols

Part #	Package | Pins	CAD File (.bxl)	STEP Model (.stp)
SN74AHCT74	SOIC (D) | 14	Download	Download
	SSOP (DB) | 14	Download	-
	TVSOP (DGV) | 14	Download	-
	PDIP (N) | 14	Download	-
	SOP (NS) | 14	Download	-
	TSSOP (PW) | 14	Download	Download
	VQFN (RGY) | 14	Download	-
TL1039WA	SOIC (D) | 8	Download	Download
TL431A	SOT (DBZ) | 3	Download	Download
	TO-92 (LP) | 3	Download	Download
	SIP (PK) | 3	Download	-
	SOT (DBV) | 5	Download	Download
	SOT (DCK) | 6	Download	Download
	SOIC (D) | 8	Download	Download
	PDIP (P) | 8	Download	Download
	SOP (PS) | 8	Download	-
	TSSOP (PW) | 8	Download	-
UCC2897A	TSSOP (PW) | 20	Download	Download
	VQFN (RGP) | 20	Download	-

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

Step 1: Download and Install the Ultra Librarian software ZIP; 62MB

Step 2: Download the Symbol and Footprint .bxl file.

- read more -

Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP6961 Test Results		PDF	2931	30 Mar 2012	84

Design Files (4)

Title	Abstract	Type	Size (KB)	Date	Views
PMP6961 Gerber		ZIP	223	26 Feb 2014	41
PMP6961 PCB		PDF	380	30 Mar 2012	54
PMP6961 Schematic		PDF	177	30 Mar 2012	181
PMP6961 Bill of Materials		PDF	83	30 Mar 2012	55

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