



Texas Instruments

PMP4432 Test Procedure

China Power Reference Design

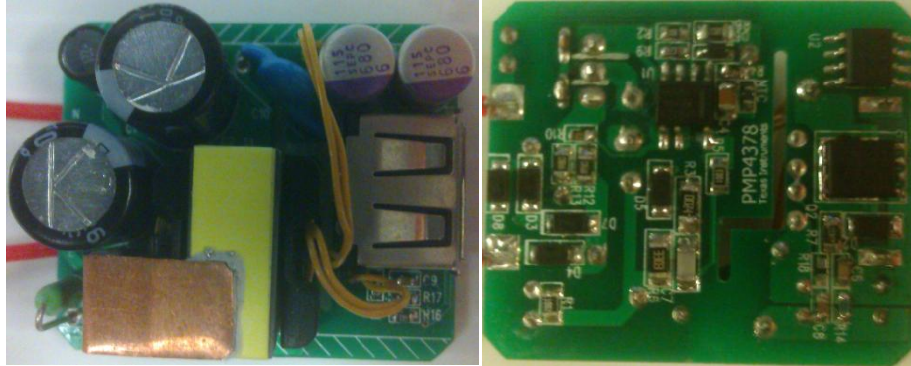
REV A

7/25/2014

1 GENERAL

1.1 PURPOSE

To provide detailed data for evaluating and verifying the PMP4432, which uses TI new Primary Side Controller UCC28713 5V2A adapter with size 37mmx31mmx15mm. The below photo shows this demo board.



1.2 REFERENCE DOCUMENTATION

Schematic PMP4432_SCH.PDF

Assembly PMP4432_PCB.PDF

BOM

Promotion tools

1.3 TEST EQUIPMENTS

Power-meter: YOKOGAWA WT210

Multi-meter(current): Fluke 8845A

Multi-meter(voltage): Fluke 187

AC Source: Chroma 61530

Electronic load: Chroma 63105A module

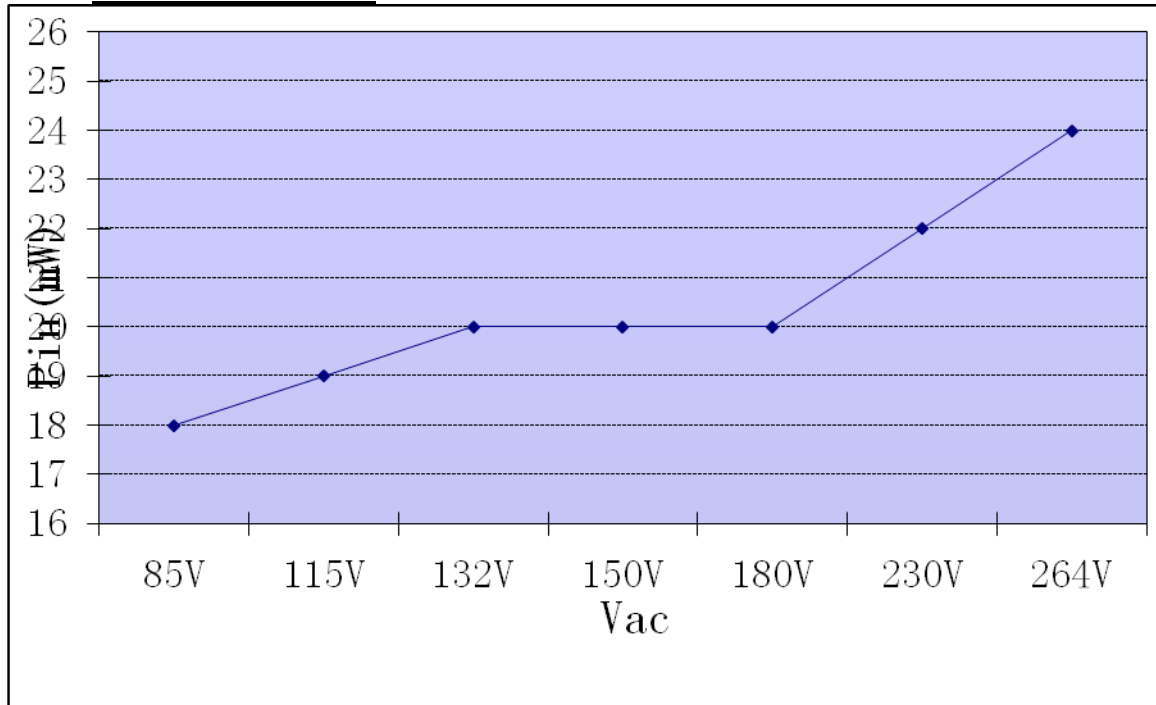
Testing demoboard

2 INPUT CHARACTERISTICS

Efficiency is tested on USB-end

Otherwise Specified, the test is under the condition with 100cm cable

2.1 STANDBY POWER



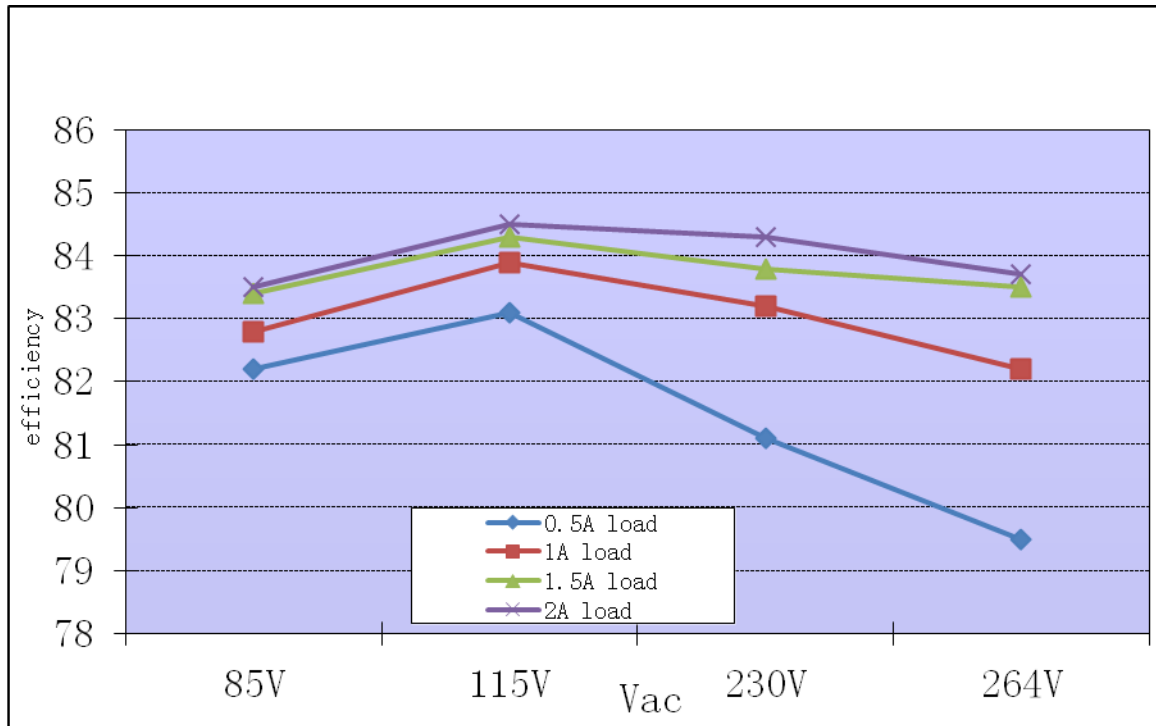
2.2 EFFICIENCY DATA

Notes: efficiency test is based USB port

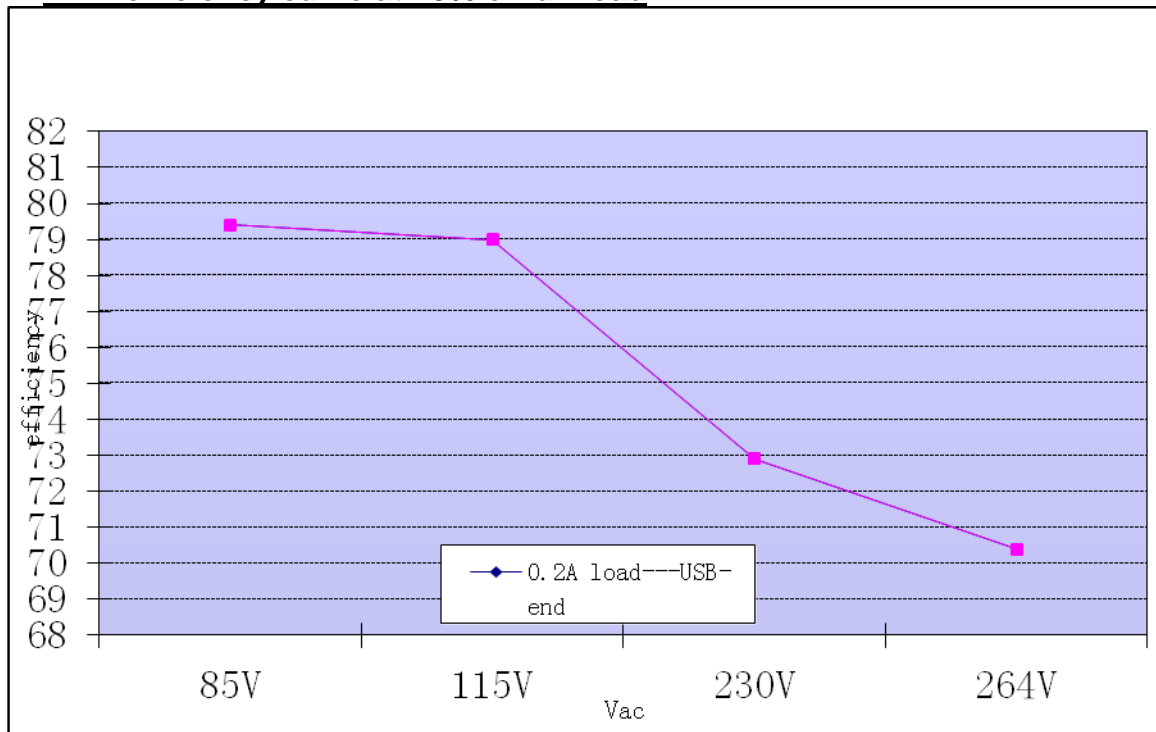
85v			
Pi (w)	Io (A)	Vo (V)	efficiency
1.264	0.2	5.019	0.794
3.077	0.5	5.056	0.822
6.207	1	5.127	0.826
9.35	1.5	5.2	0.834
12.62	2	5.272	0.835
230v			
Pi (w)	Io (A)	Vo (V)	efficiency
1.375	0.2	5.011	0.729
3.108	0.5	5.04	0.811
6.153	1	5.119	0.832
9.286	1.5	5.186	0.838
12.49	2	5.262	0.843

115v			
Pi (w)	Io (A)	Vo (V)	efficiency
1.268	0.2	5.011	0.79
3.033	0.5	5.043	0.831
6.104	1	5.12	0.839
9.25	1.5	5.199	0.843
12.48	2	5.27	0.845
264v			
Pi (w)	Io (A)	Vo (V)	efficiency
1.422	0.2	5.005	0.704
3.165	0.5	5.033	0.795
6.171	1	5.112	0.828
9.314	1.5	5.183	0.835
12.56	2	5.258	0.837

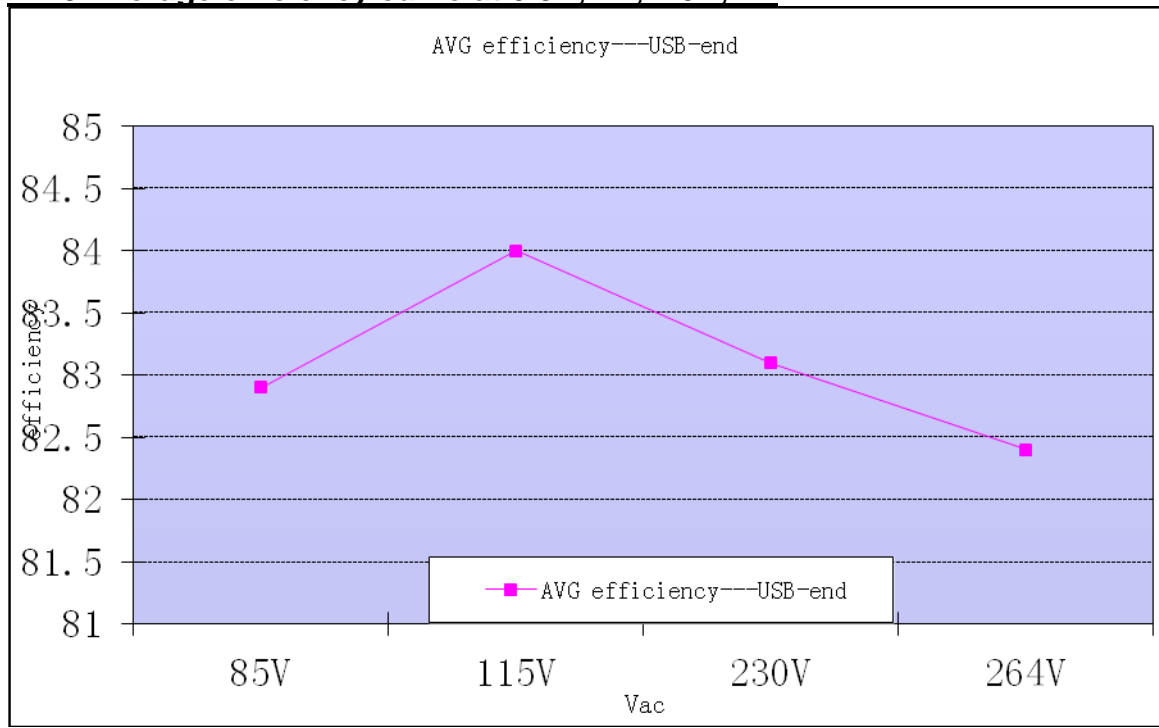
2.2.1 Load and input voltage Vs efficiency curve tested at USB-end



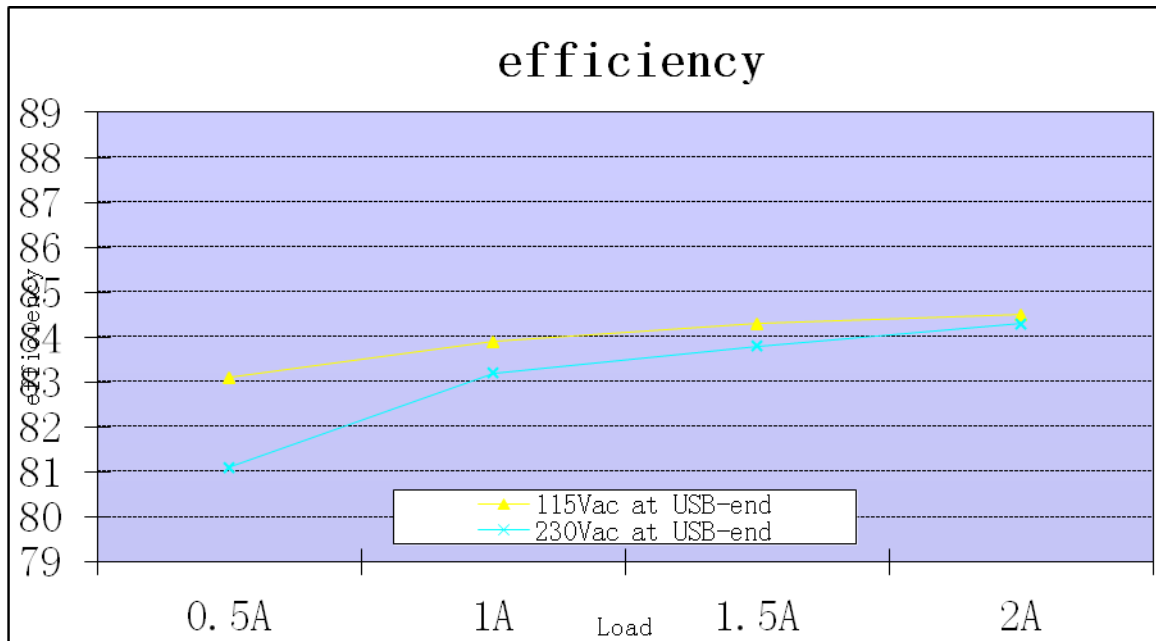
2.2.2 efficiency curve at 10% of full load



2.2.3 Average efficiency curve at 0.5A, 1A, 1.5A, 2A



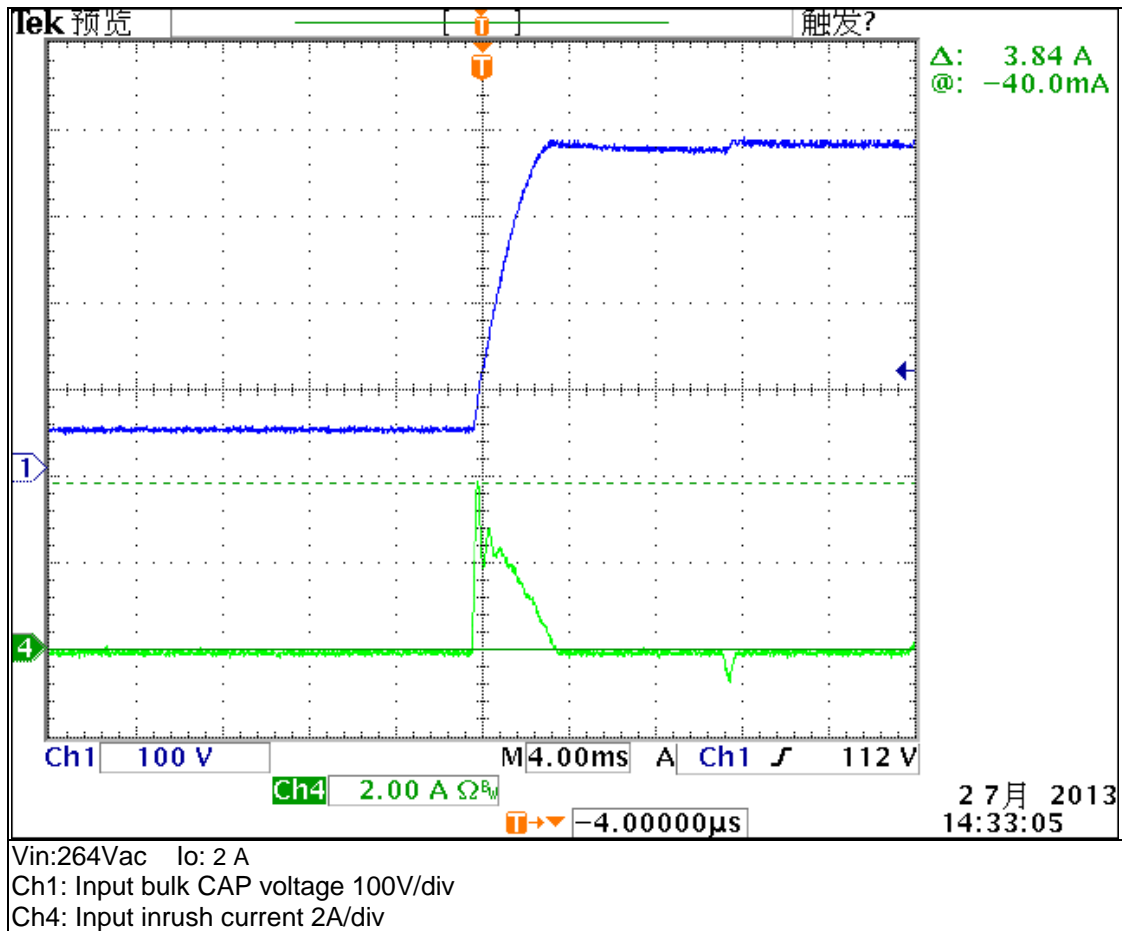
2.2.4 Efficiency Vs load curve



2.3 INPUT CURRENT

Vin(Vac)	Freq(Hz)	Iin(Arms)	Pass/Fail
85	60	0.28	

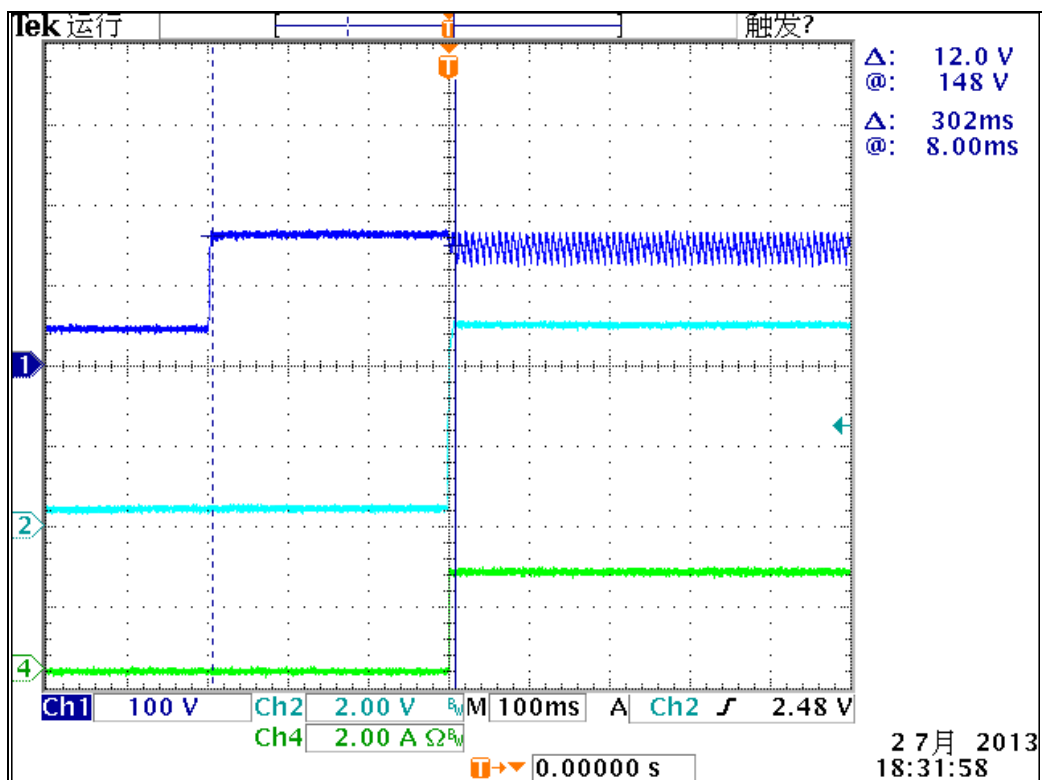
2.4 INPUT INRUSH CURRENT



3 OUTPUT CHARACTERISTICS

3.1 STARTUP TIME

Input voltage	Output current	Startup time	Pass/Fail
115Vac	2A	302mS	
230Vac	2A	66.4mS	

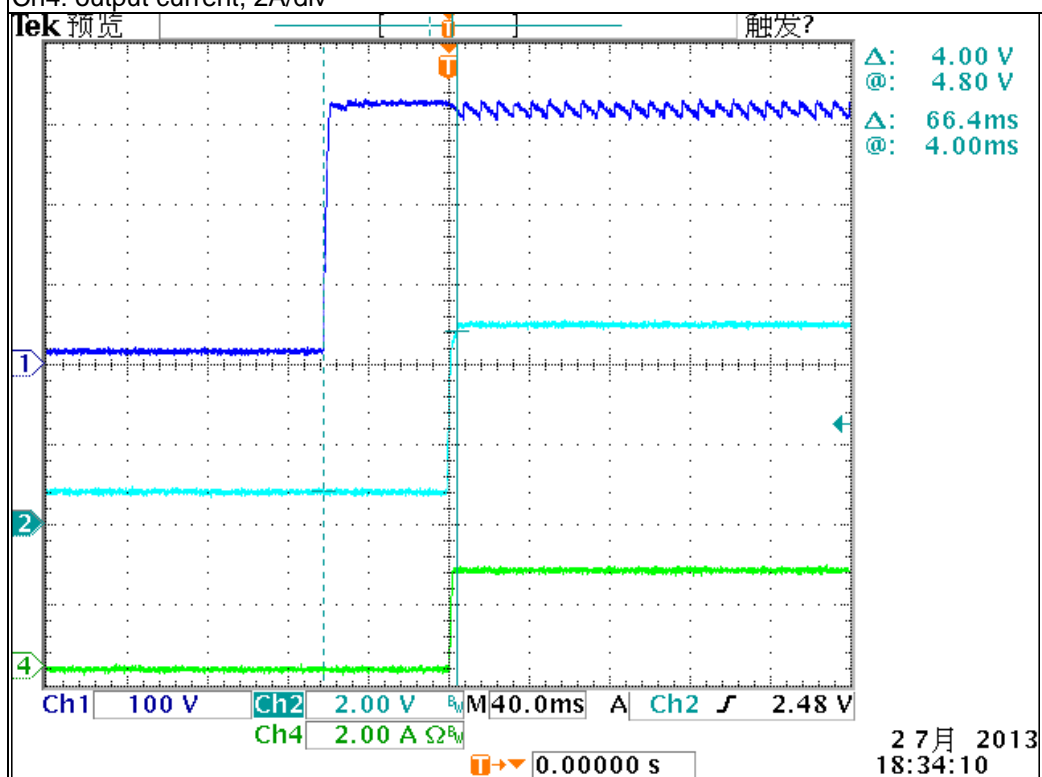


Vin:115Vac Io: 2A

Ch1: MOSFET's drain voltage, 100V/div

Ch2: output voltage, 2V/div

Ch4: output current, 2A/div



Vin:230Vac Io: 2A

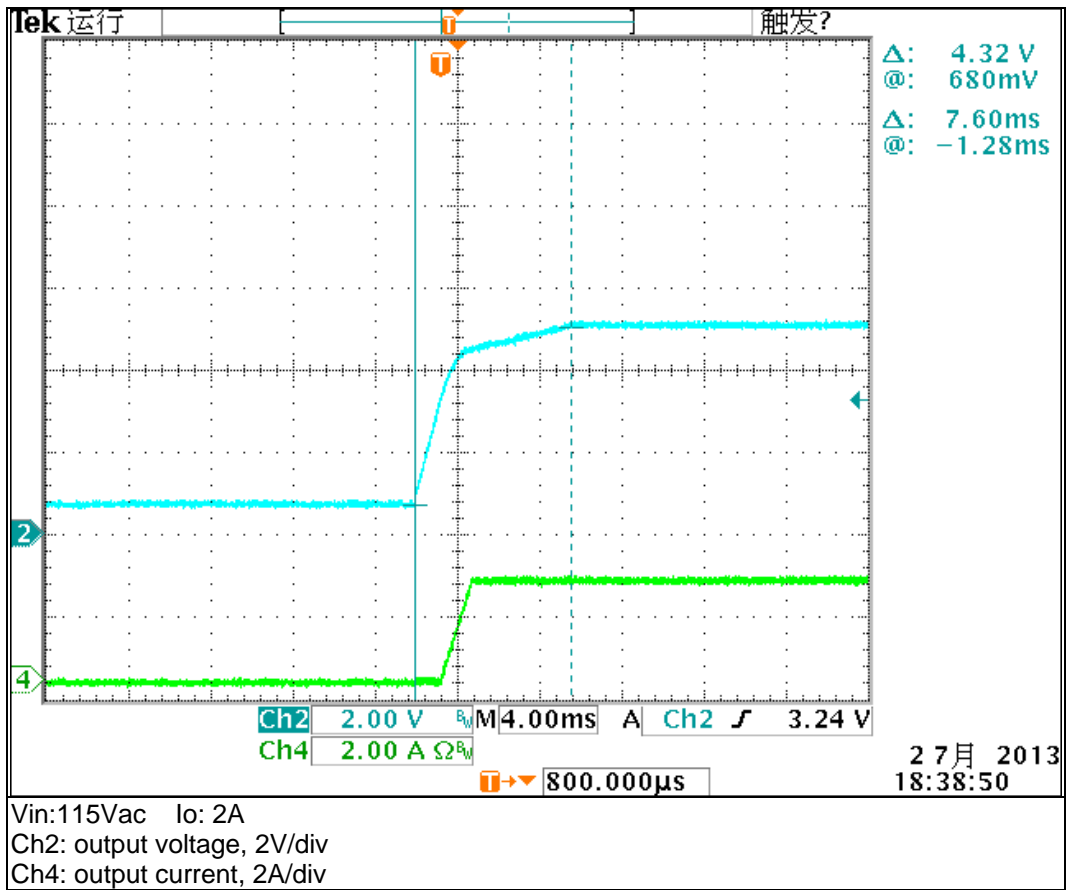
Ch1: MOSFET's drain voltage, 100V/div

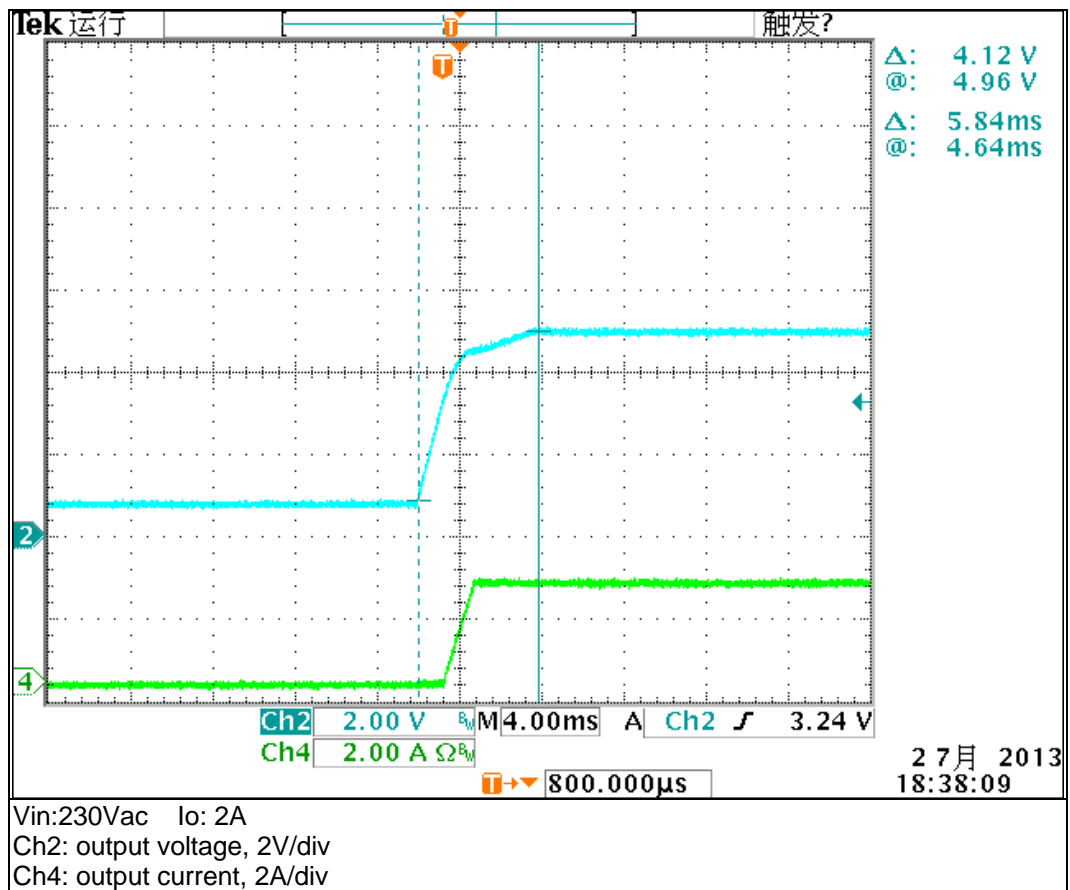
Ch2: output voltage, 2V/div

Ch4: output current, 2A/div

3.2 OUTPUT VOLTAGE RISE TIME

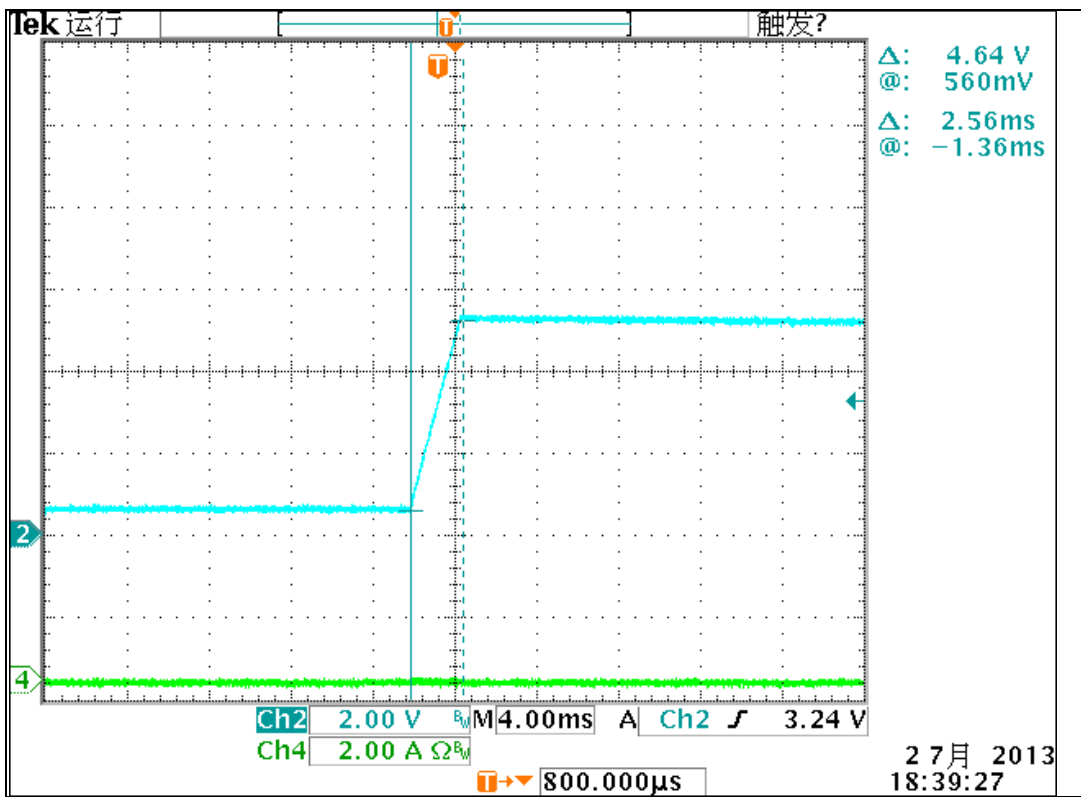
Input voltage	Output current	Startup time	Pass/Fail
115Vac	2A	7.6mS	
230Vac	2A	5.84mS	



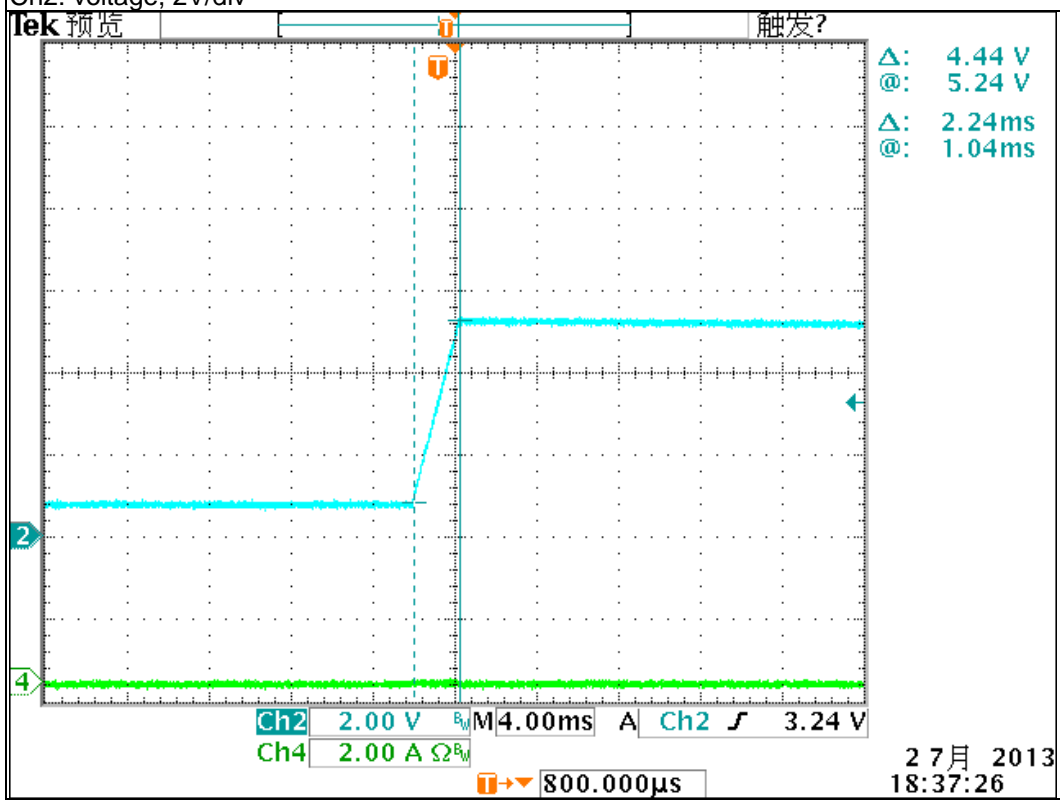


3.3 OUTPUT VOLTAGE OVERSHOOT

Input voltage	Output current	overshoot voltage	Pass/Fail
115Vac	0A	<1%	
230Vac	0A	<1%	



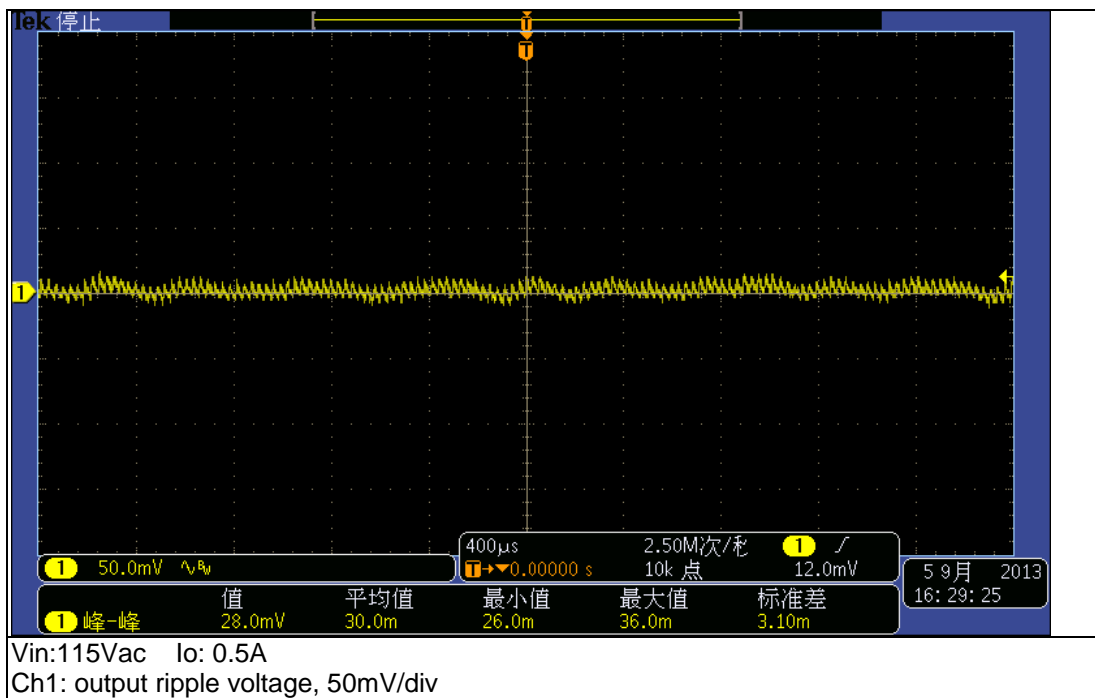
Vin:115Vac Io: 0A
Ch2: voltage, 2V/div

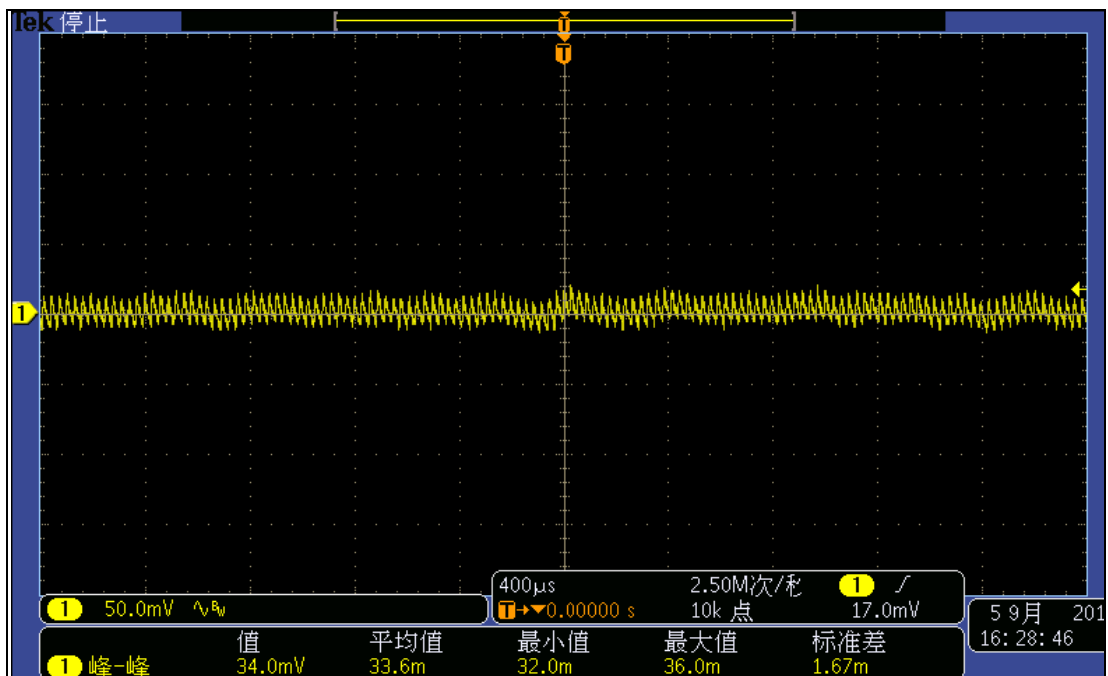


Vin:230Vac Io: 0A
Ch2: output voltage, 2V/div

3.4 RIPPLE VOLTAGE

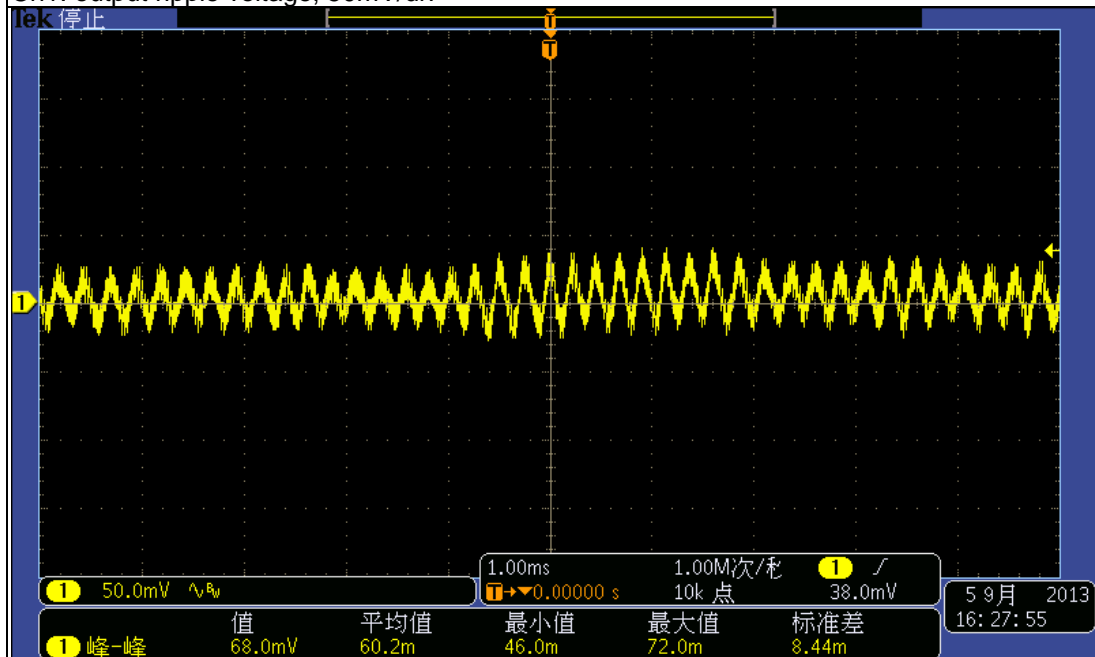
Input voltage	Output current	Ripple voltage	Pass/Fail
115Vac	0.5A	28mV	
115Vac	1A	34mV	
115Vac	1.5A	68mV	
115Vac	2A	56mV	
230Vac	0.5A	30mV	
230Vac	1A	32mV	
230Vac	1.5A	48mV	
230Vac	2A	66mV	





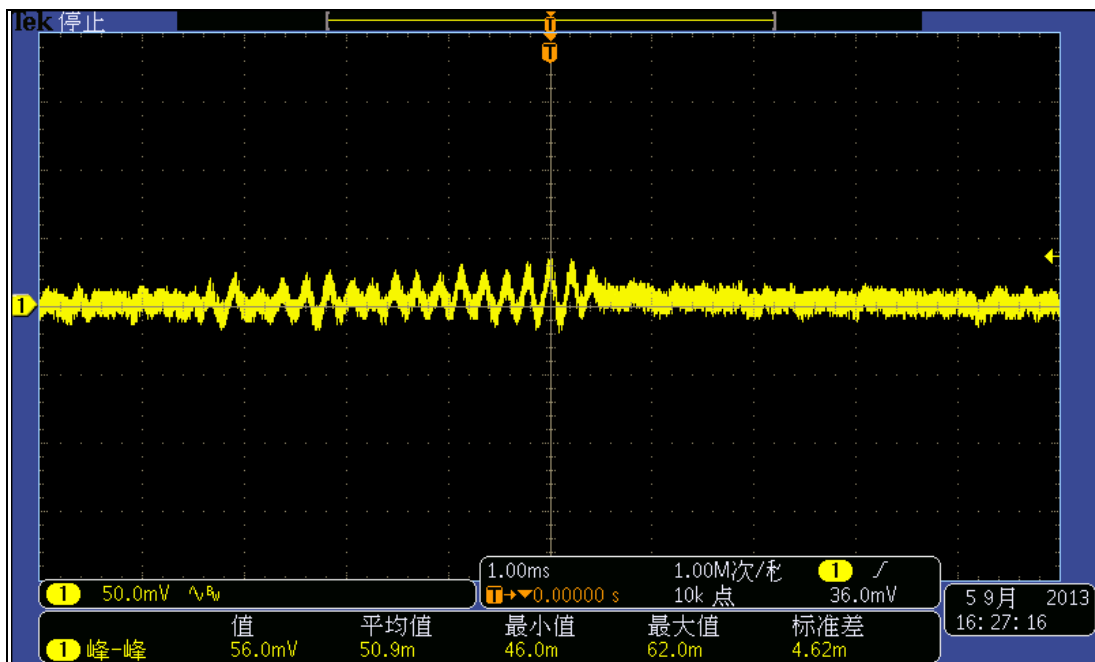
Vin:115Vac Io: 1A

Ch1: output ripple voltage, 50mV/div

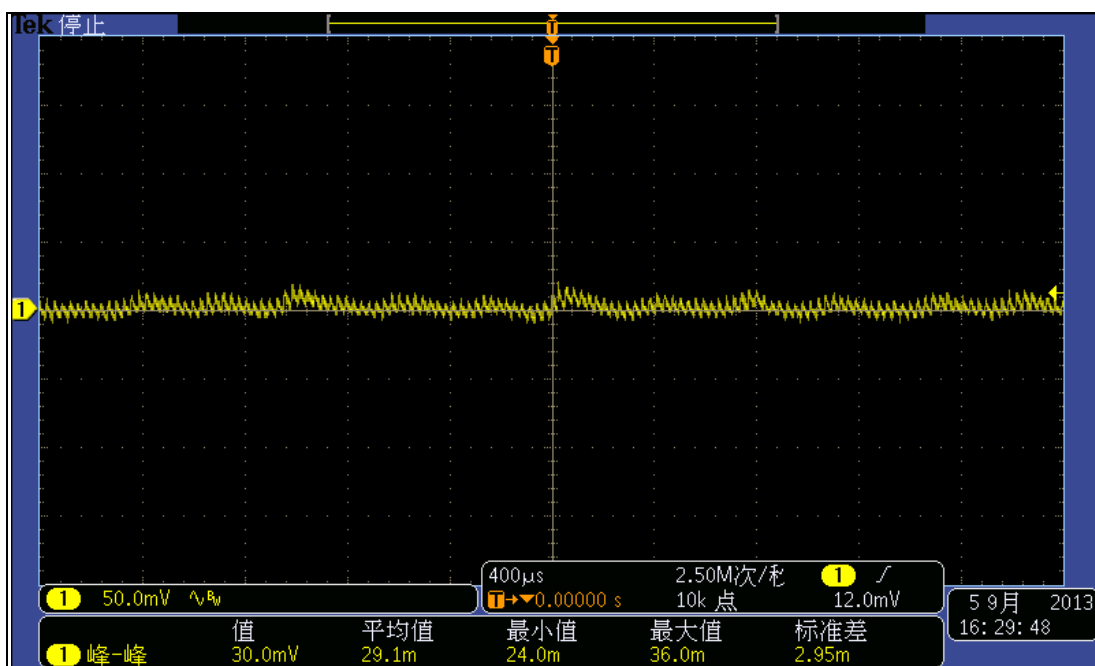


Vin:115Vac Io: 1.5A

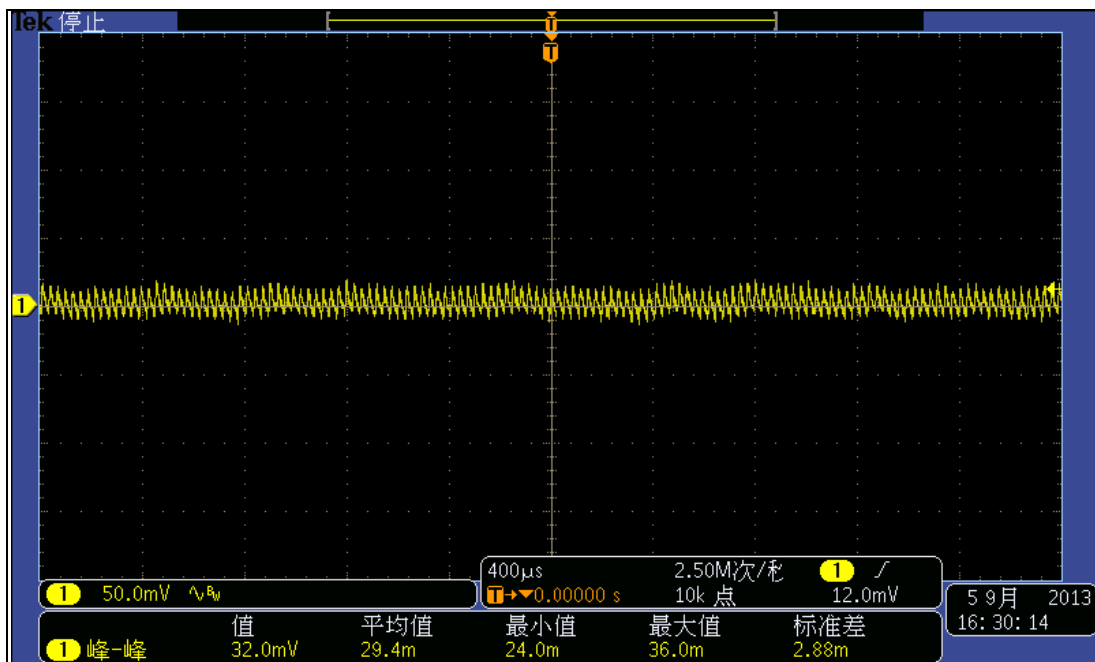
Ch1: output ripple voltage, 50mV/div



Vin:115Vac Io: 2A
Ch1: output ripple voltage, 50mV/div

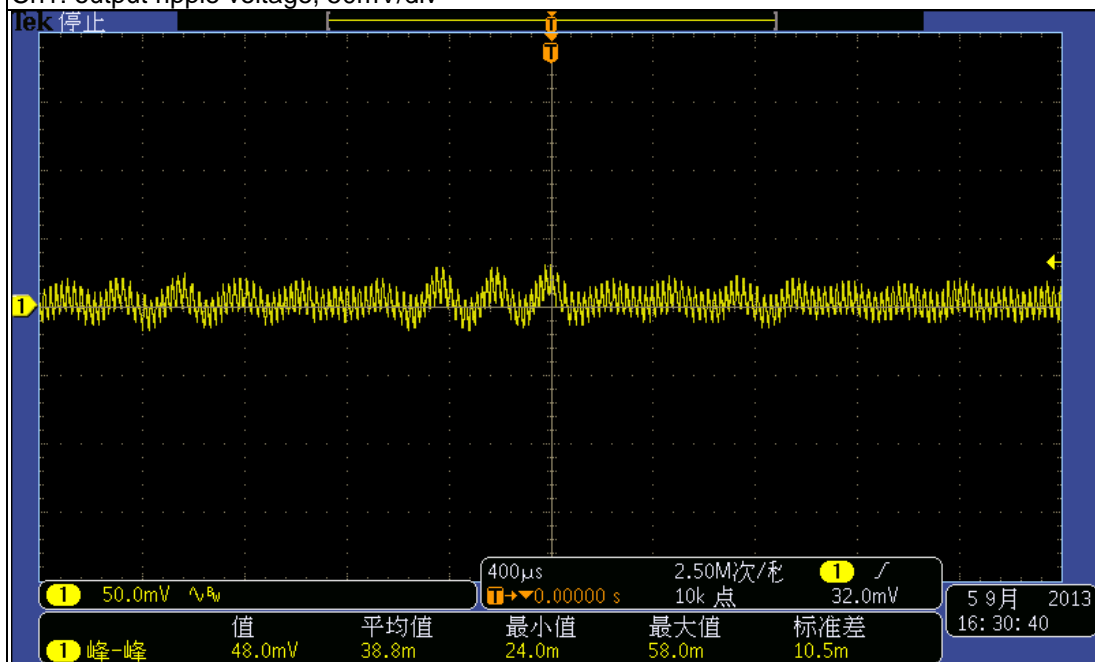


Vin:230Vac Io: 0.5A
Ch1: output ripple voltage, 50mV/div



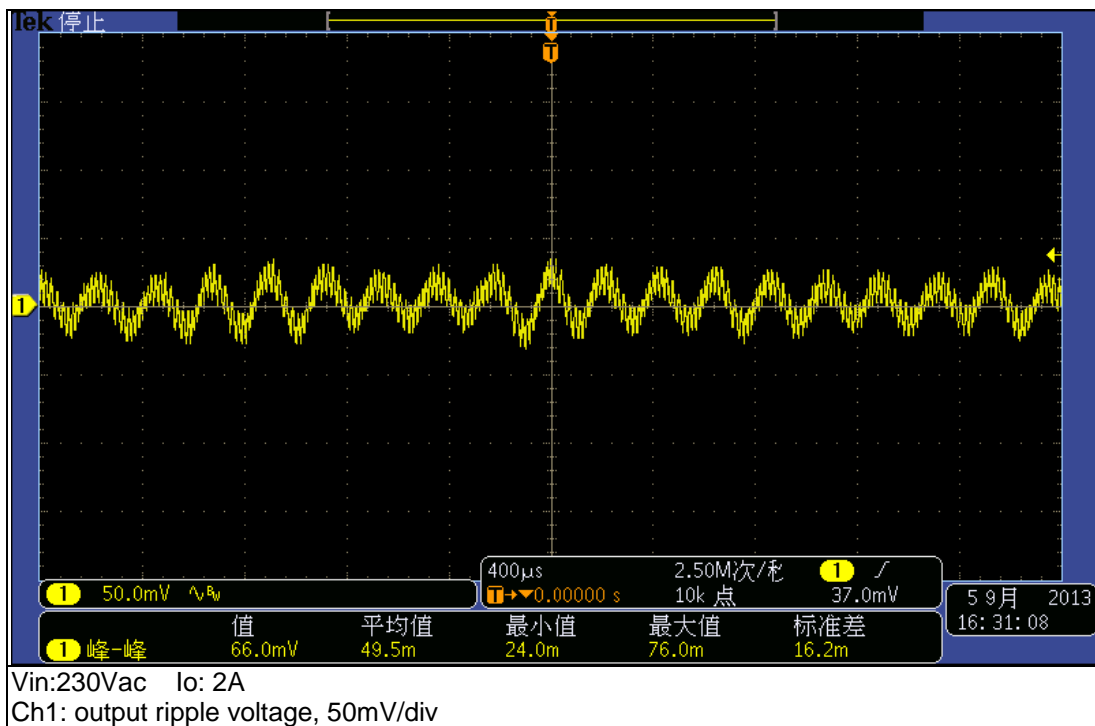
Vin:230Vac Io: 1A

Ch1: output ripple voltage, 50mV/div



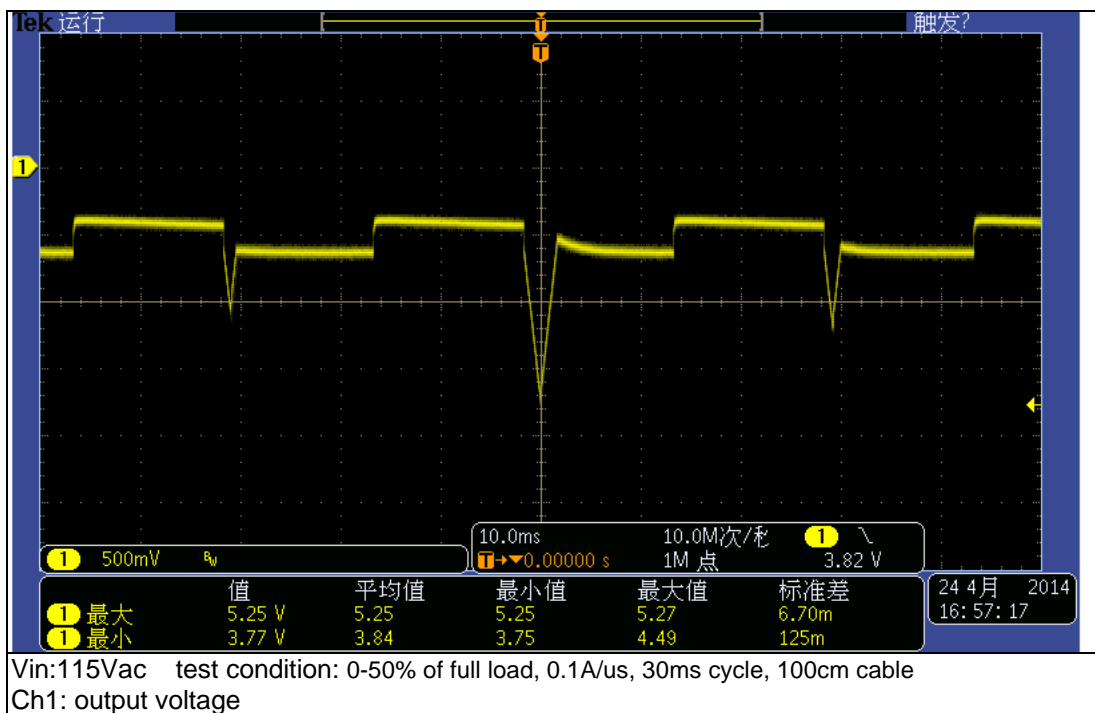
Vin:230Vac Io: 1.5A

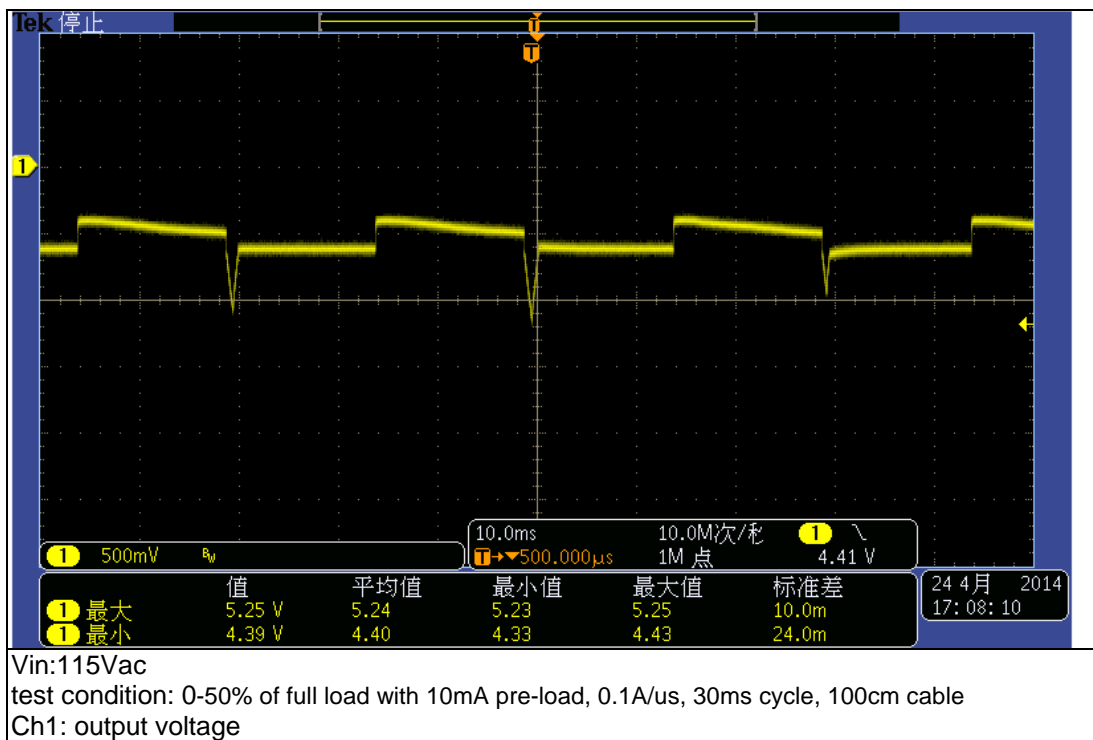
Ch1: output ripple voltage, 50mV/div



3.5 DYNAMIC RESPONSE

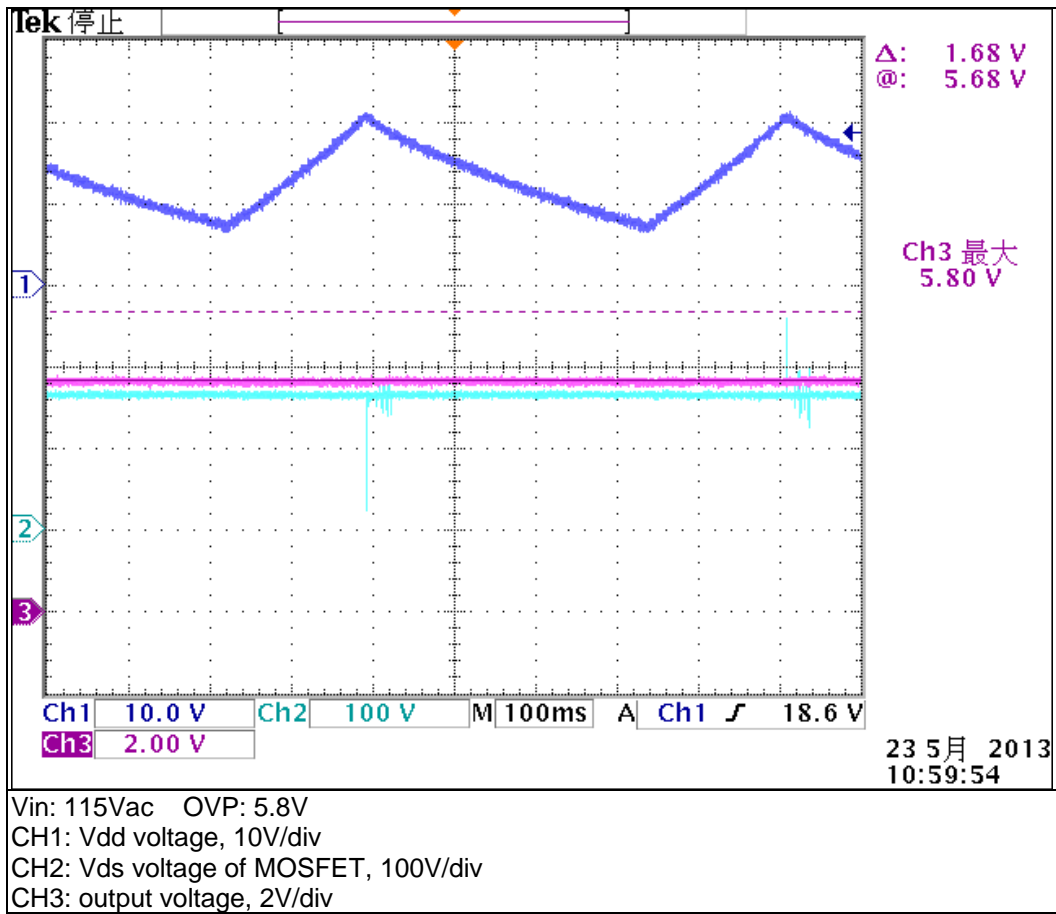
Input voltage	Output current	Max voltage	Min voltage
115Vac	0-50% of full load	5.25V	3.77V
115Vac	0-50% of full load with 10mA pre-load	5.25V	4.39V





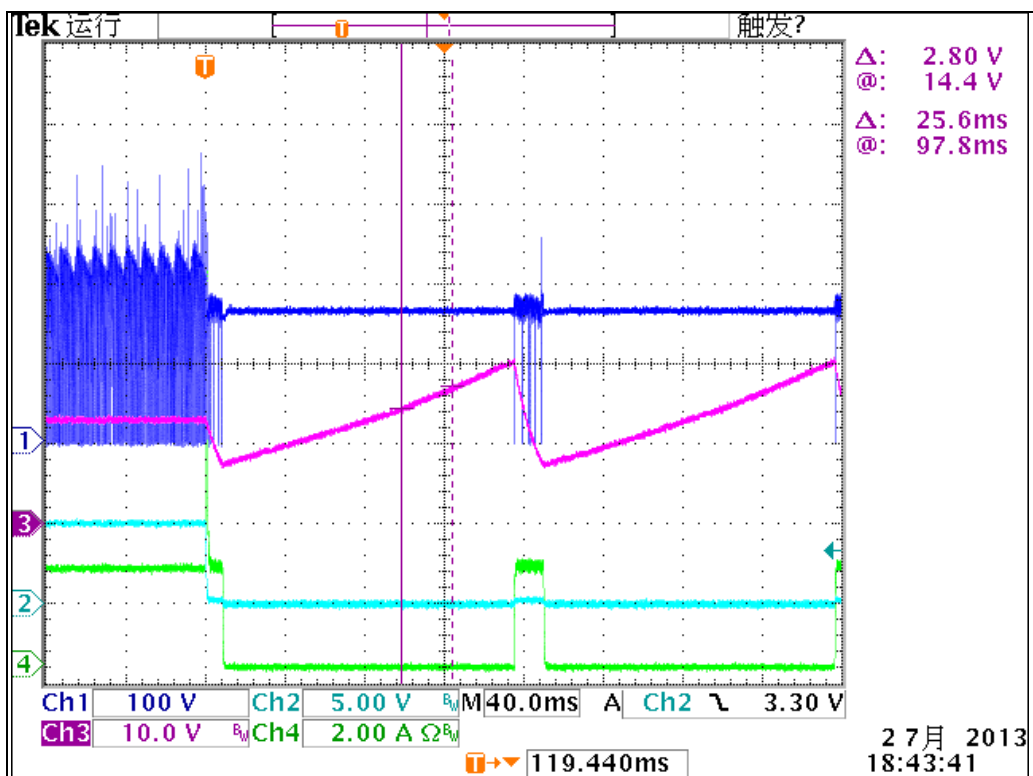
3.6 OUTPUT VOLTAGE PROTECTION

CONDITIONS	Protection voltage (V)	Pass/Fail
Vin (Vac)		
115&230	5.8	

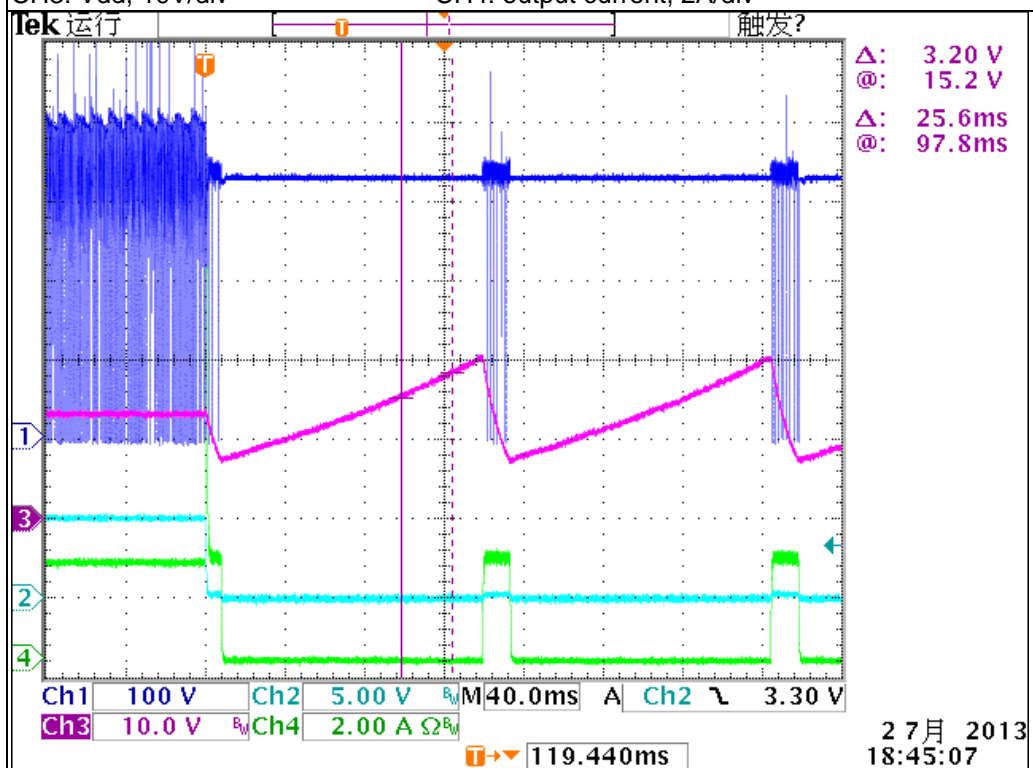


3.7 OUTPUT SHORT PROTECTION

Input voltage	Output short protection
115&230Vac	Hiccup up mode

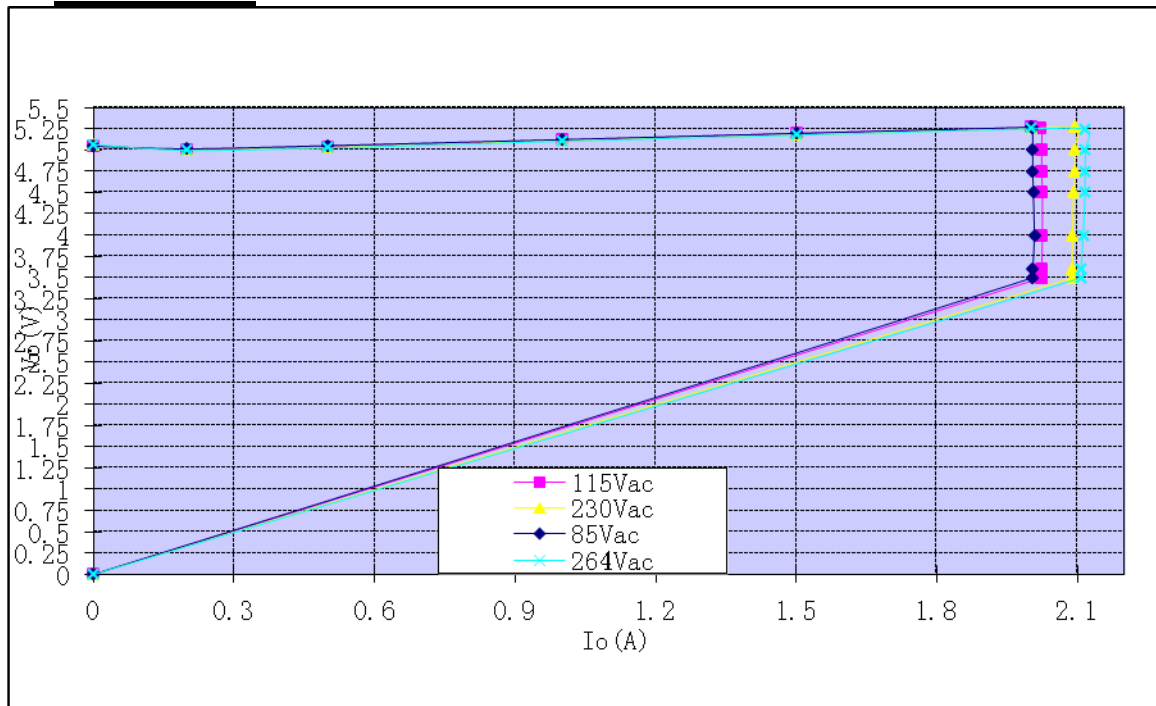


Vin: 115Vac
CH1: Vds of MOSFET, 100V/div CH2: output voltage, 5V/div
CH3: Vdd, 10V/div CH4: output current, 2A/div



Vin: 230Vac
CH1: Vds of MOSFET, 100V/div CH2: output voltage, 5V/div
CH3: Vdd, 10V/div CH4: output current, 2A/div

4 IV CURVE



5 EMI Test

5.1 Conduction emission

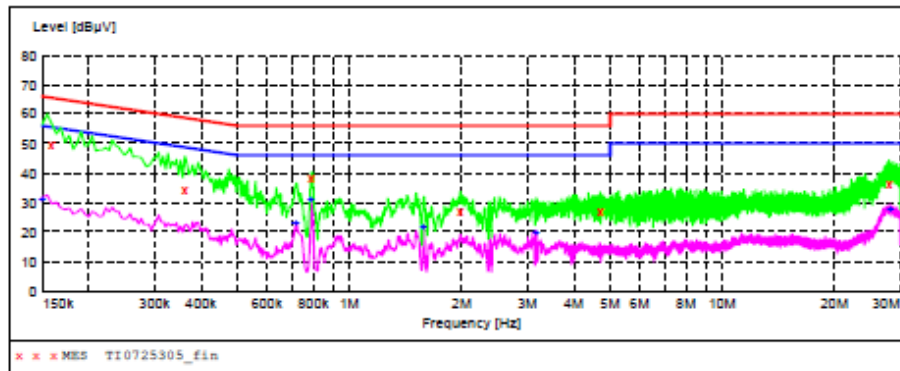
Shenzhen Huatongwei International Inspection CO.,Ltd

Voltage Mains Test EN 55022 CLASS B

EUT: FMP4378
Manufacturer: TI
Operating Condition: LOAD
Test Site: 3# SHIELDED ROOM
Operator: ZHANGBAO.SUN
Test Specification: AC 230V/50Hz
Comment:
Start of Test: 7/25/2013 / 6:41:43PM

SCAN TABLE: "Voltage (9K-30M)FIN"

Short Description: 150K-30M Voltage



MEASUREMENT RESULT: "TI0725305_fin"

7/25/2013 6:44PM

Frequency MHz	Level dBuV	Transd dB	Limit dBuV	Margin dB	Detector	Line	FE
0.159000	49.60	10.1	66	15.9	QP	L1	GND
0.361500	34.10	10.1	59	24.6	QP	L1	GND
0.789000	38.60	10.1	56	17.4	QP	L1	GND
1.977000	27.20	10.2	56	28.8	QP	L1	GND
4.695000	26.80	10.2	56	29.2	QP	L1	GND
28.018500	36.70	10.9	60	23.3	QP	L1	GND

MEASUREMENT RESULT: "TI0725305_fin2"

7/25/2013 6:44PM

Frequency MHz	Level dBuV	Transd dB	Limit dBuV	Margin dB	Detector	Line	FE
0.159000	30.70	10.1	56	25.3	AV	L1	GND
0.717000	22.90	10.1	46	23.1	AV	L1	GND
0.789000	30.80	10.1	46	15.2	AV	L1	GND
1.576500	21.80	10.2	46	24.5	AV	L1	GND
3.160500	19.90	10.2	46	26.1	AV	L1	GND
28.122000	28.00	10.9	50	22.0	AV	L1	GND

Page 1/1 7/25/2013 6:44PM TI0725305

Vin: 230Vac, Line, Io: 2A

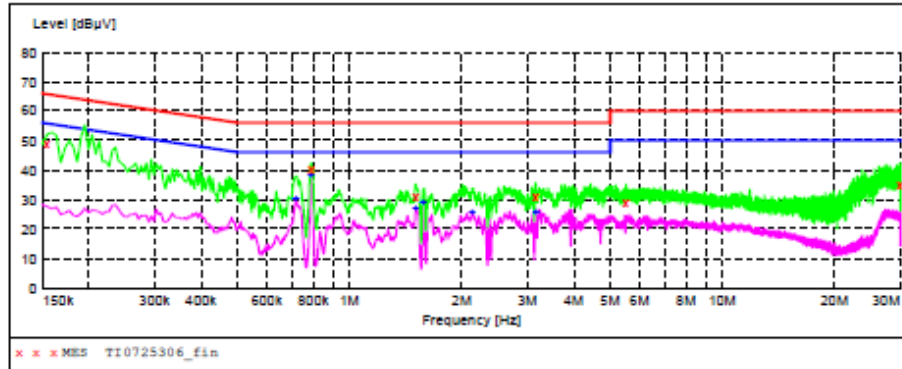
Test condition: 1.5m cable with 2.5R load resistor

Shenzhen Huatongwei International Inspection CO.,Ltd

Voltage Mains Test EN 55022 CLASS B

EUT: FMP4378
 Manufacturer: TI
 Operating Condition: LOAD
 Test Site: 3# SHIELDED ROOM
 Operator: ZHANGBAO.SUN
 Test Specification: AC 230V/50Hz
 Comment:
 Start of Test: 7/25/2013 / 6:44:29PM

SCAN TABLE: "Voltage (9K-30M)FIN"
 Short Description: 150K-30M Voltage



MEASUREMENT RESULT: "TI0725306_fin"

7/25/2013 6:46PM

Frequency MHz	Level dBuV	Transd dB	Limit dBuV	Margin dB	Detector	Line	PE
0.154500	48.80	10.1	66	17.0	QP	N	GND
0.789000	40.30	10.1	56	15.7	QP	N	GND
1.504500	30.90	10.2	56	25.1	QP	N	GND
3.156000	31.20	10.2	56	24.8	QP	N	GND
5.514000	29.30	10.2	60	30.7	QP	N	GND
29.881500	35.00	11.0	60	25.0	QP	N	GND

MEASUREMENT RESULT: "TI0725306_fin2"

7/25/2013 6:46PM

Frequency MHz	Level dBuV	Transd dB	Limit dBuV	Margin dB	Detector	Line	PE
0.717000	30.10	10.1	46	15.9	AV	N	GND
0.789000	38.60	10.1	46	7.4	AV	N	GND
1.504500	27.10	10.2	46	18.9	AV	N	GND
1.576500	28.70	10.2	46	17.3	AV	N	GND
2.134500	25.80	10.2	46	20.2	AV	N	GND
3.160500	25.90	10.2	46	20.1	AV	N	GND

Vin:230Vac, Neutral, Io: 2A
 Test condition: 1.5m cable with 2.5R load resistor

6.2 Radiated emission

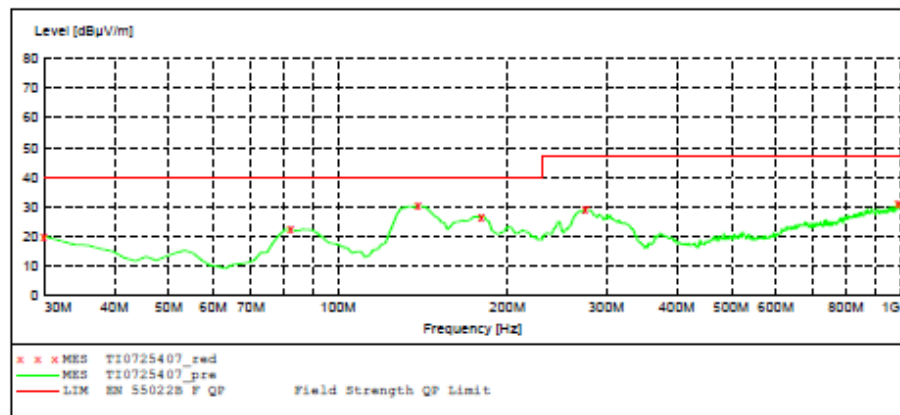
SHENZHEN HUATONGWEI INTERNATIONAL INSPECTION CO.,LTD

RADIATED EMISSION TEST EN 55022 CLASSB

EUT: FMP4378
Manufacturer: TI
Operating Condition: LOAD
Test Site: 3M CHAMBER
Operator: MINGHUA.FAN
Test Specification: AC 230V/50Hz
Comment:
Start of Test: 7/25/2013 / 5:13:25PM

SWEEP TABLE: "test (30M-1G)"

Short Description:		Field Strength			
Start	Stop	Detector	Meas.	IF	Transducer
Frequency	Frequency		Time	Bandw.	
30.0 MHz	1.0 GHz	MaxPeak	Coupled	120 kHz	HL562 201106



MEASUREMENT RESULT: "TI0725407_red"

7/25/2013 5:15PM

Frequency MHz	Level dBuV/m	Transd dB	Limit dBuV/m	Margin dB	Det.	Height cm	Asimuth deg	Polarisation
30.000000	19.90	-10.0	40.0	20.1	---	300.0	76.00	HORIZONTAL
82.484970	22.40	-19.9	40.0	17.6	---	300.0	221.00	HORIZONTAL
138.857715	30.20	-19.8	40.0	9.8	---	300.0	197.00	HORIZONTAL
179.679359	26.70	-20.5	40.0	13.3	---	100.0	358.00	HORIZONTAL
274.929860	29.00	-16.3	47.0	18.0	---	100.0	45.00	HORIZONTAL
990.280561	31.10	-3.1	47.0	15.9	---	100.0	316.00	HORIZONTAL

Vin:230Vac, HORIZONTAL, Io: 2A
Test condition: 1.5m cable with 2.5R resistor

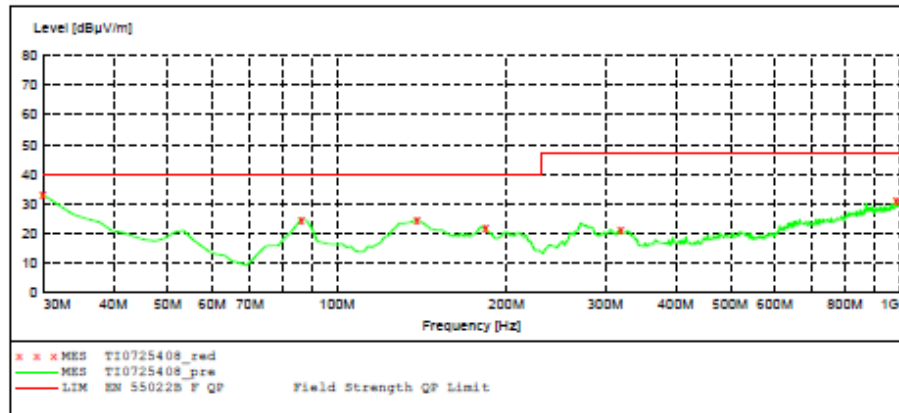
SHENZHEN HUATONGWEI INTERNATIONAL INSPECTION CO.,LTD

RADIATED EMISSION TEST EN 55022 CLASSB

EUT: FMP4378
 Manufacturer: TI
 Operating Condition: LOAD
 Test Site: 3M CHAMBER
 Operator: MINGHUA.FAN
 Test Specification: AC 230V/50Hz
 Comment:
 Start of Test: 7/25/2013 / 5:15:48PM

SWEEP TABLE: "test (30M-1G)"

Short Description: Field Strength
 Start Stop Detector Meas. IF Transducer
 Frequency Frequency Time Bandw.
 30.0 MHz 1.0 GHz MaxPeak Coupled 120 kHz HL562 201106



MEASUREMENT RESULT: "TI0725408_red"

7/25/2013 5:17PM

Frequency MHz	Level dBuV/m	Transd dB	Limit dBuV/m	Margin dB	Det.	Height cm	Asimuth deg	Polarisation
30.000000	33.00	-10.0	40.0	7.0	---	100.0	53.00	VERTICAL
86.372745	24.80	-19.4	40.0	15.2	---	100.0	296.00	VERTICAL
138.857715	24.80	-19.8	40.0	15.2	---	100.0	242.00	VERTICAL
183.567134	22.20	-20.5	40.0	17.8	---	100.0	94.00	VERTICAL
319.639279	21.10	-14.6	47.0	25.9	---	100.0	248.00	VERTICAL
988.336673	31.00	-3.1	47.0	16.0	---	100.0	278.00	VERTICAL

Vin:230Vac, VERTICAL, Io: 2A

Test condition: 1.5m cable with 2.5R resistor

IMPORTANT NOTICE FOR TI REFERENCE DESIGNS

Texas Instruments Incorporated ("TI") reference designs are solely intended to assist designers ("Buyers") who are developing systems that incorporate TI semiconductor products (also referred to herein as "components"). Buyer understands and agrees that Buyer remains responsible for using its independent analysis, evaluation and judgment in designing Buyer's systems and products.

TI reference designs have been created using standard laboratory conditions and engineering practices. **TI has not conducted any testing other than that specifically described in the published documentation for a particular reference design.** TI may make corrections, enhancements, improvements and other changes to its reference designs.

Buyers are authorized to use TI reference designs with the TI component(s) identified in each particular reference design and to modify the reference design in the development of their end products. HOWEVER, NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY THIRD PARTY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT, IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI REFERENCE DESIGNS ARE PROVIDED "AS IS". TI MAKES NO WARRANTIES OR REPRESENTATIONS WITH REGARD TO THE REFERENCE DESIGNS OR USE OF THE REFERENCE DESIGNS, EXPRESS, IMPLIED OR STATUTORY, INCLUDING ACCURACY OR COMPLETENESS. TI DISCLAIMS ANY WARRANTY OF TITLE AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, QUIET ENJOYMENT, QUIET POSSESSION, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS WITH REGARD TO TI REFERENCE DESIGNS OR USE THEREOF. TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY BUYERS AGAINST ANY THIRD PARTY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON A COMBINATION OF COMPONENTS PROVIDED IN A TI REFERENCE DESIGN. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, SPECIAL, INCIDENTAL, CONSEQUENTIAL OR INDIRECT DAMAGES, HOWEVER CAUSED, ON ANY THEORY OF LIABILITY AND WHETHER OR NOT TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES, ARISING IN ANY WAY OUT OF TI REFERENCE DESIGNS OR BUYER'S USE OF TI REFERENCE DESIGNS.

TI reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques for TI components are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

Reproduction of significant portions of TI information in TI data books, data sheets or reference designs is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards that anticipate dangerous failures, monitor failures and their consequences, lessen the likelihood of dangerous failures and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in Buyer's safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed an agreement specifically governing such use.

Only those TI components that TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components that have **not** been so designated is solely at Buyer's risk, and Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.