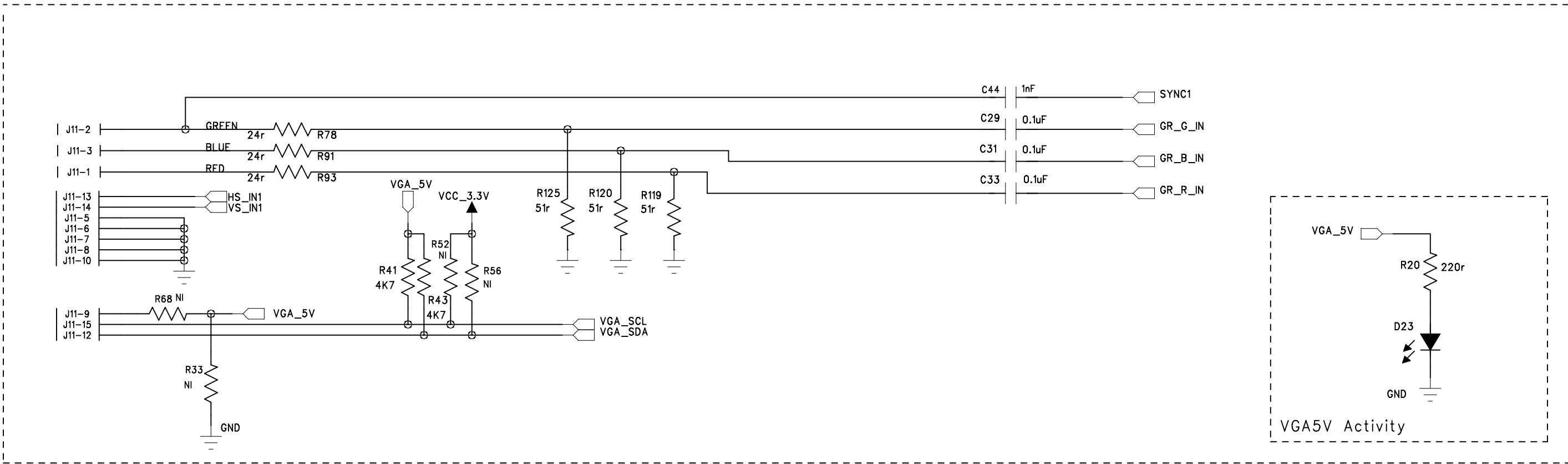
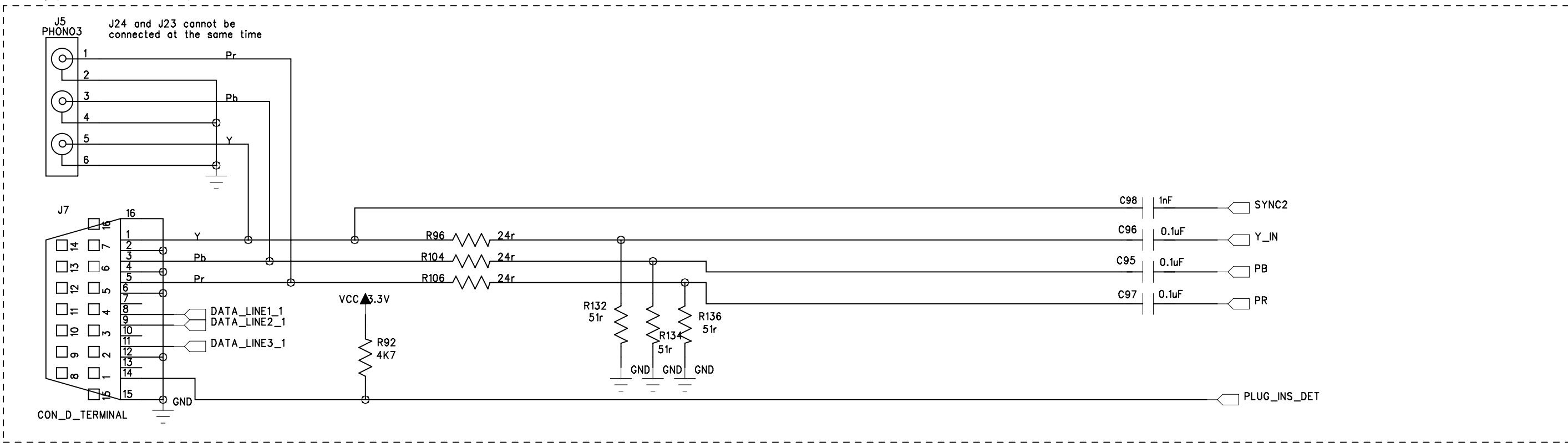


VGA Connector



D-Type and Component Connectors



Place analog inputs as far apart as possible while maintaining similar trace lengths
Trace impedance of analog inputs to be 75 ohms
Termination resistors should be placed as close as possible to end of line

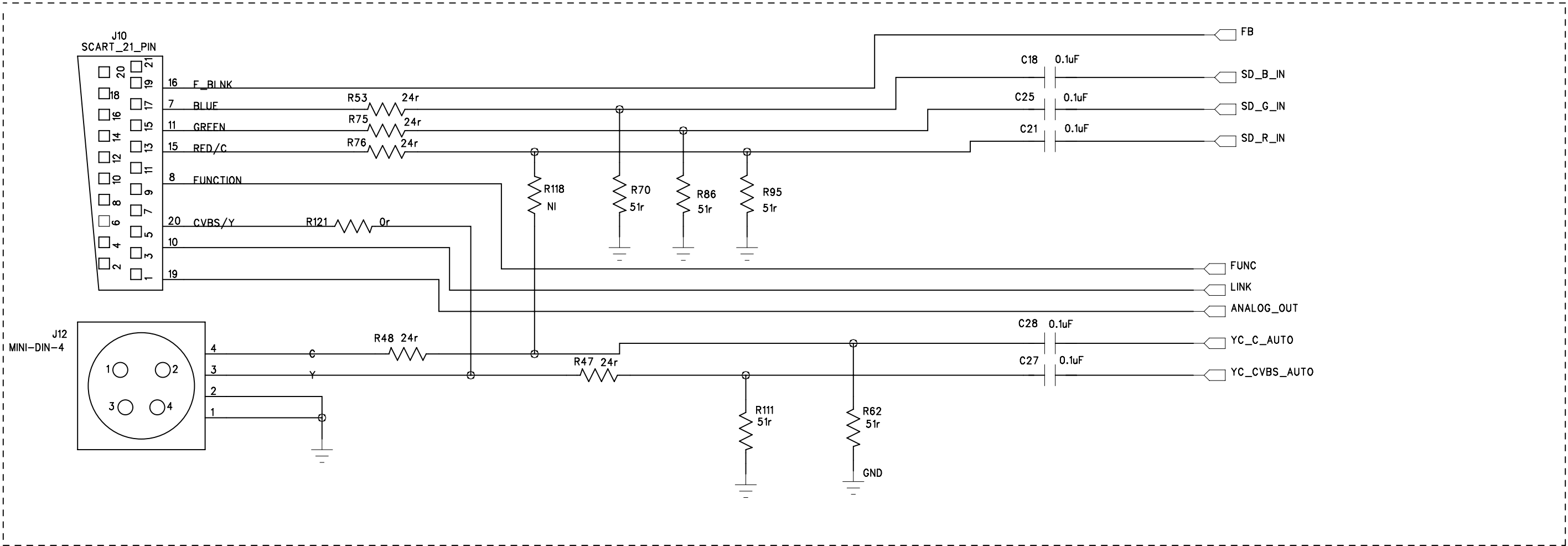
ADV7844 Input Module

Sheet: No. 1 – Analog Inputs 1

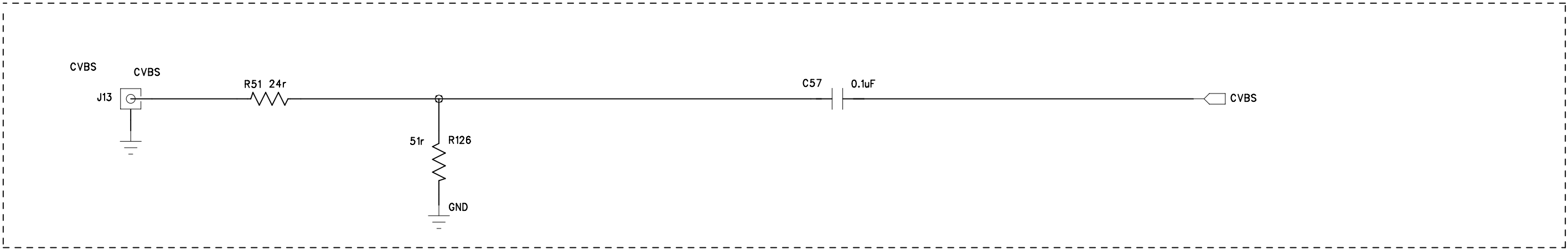
Video Applications

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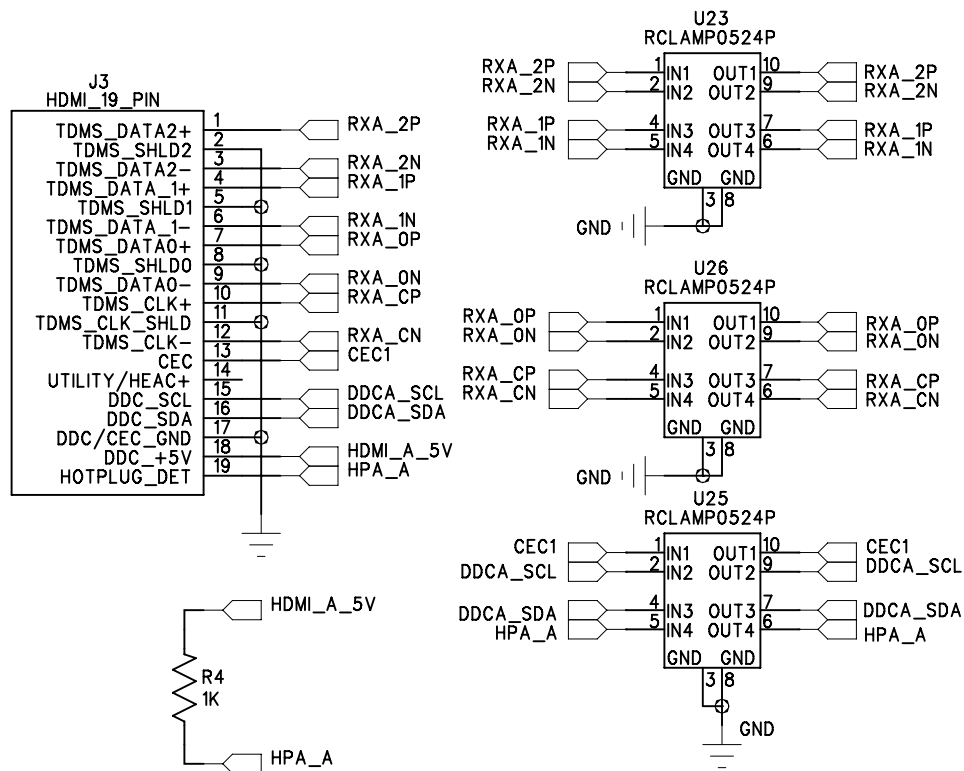
SCART and S-VIDEO Connectors



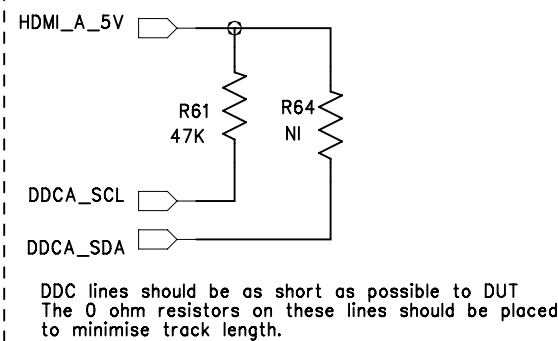
CVBS Connector



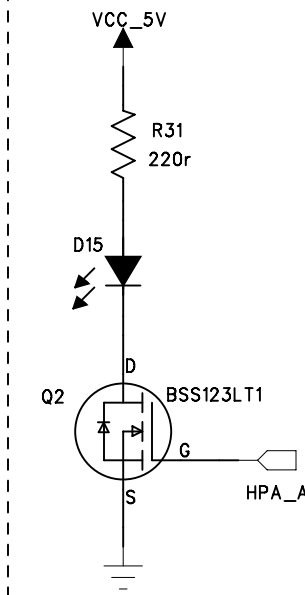
Place analog inputs as far apart as possible while maintaining similar trace lengths
Trace impedance of analog inputs to be 75 ohms
Termination resistors should be placed as close as possible to end of line



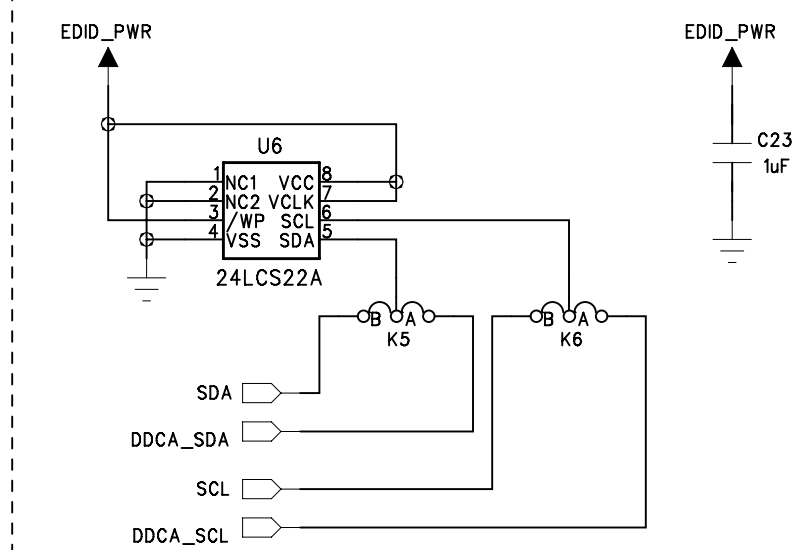
DDC A Circuitry



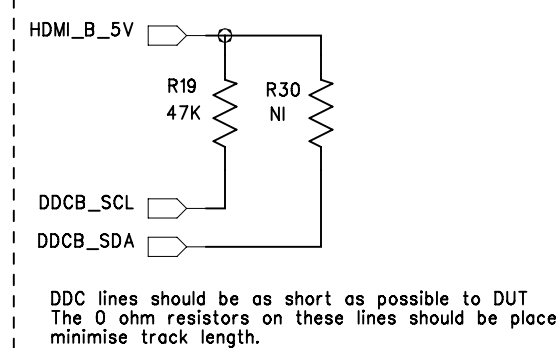
HPD A LED



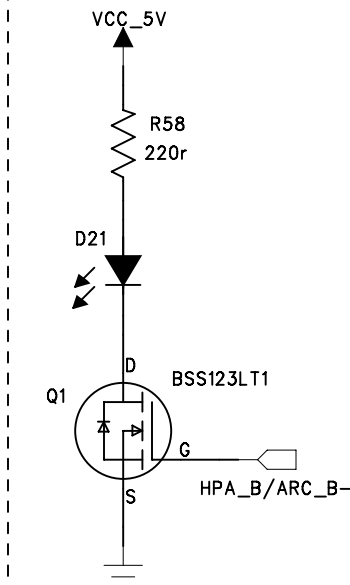
EDID1



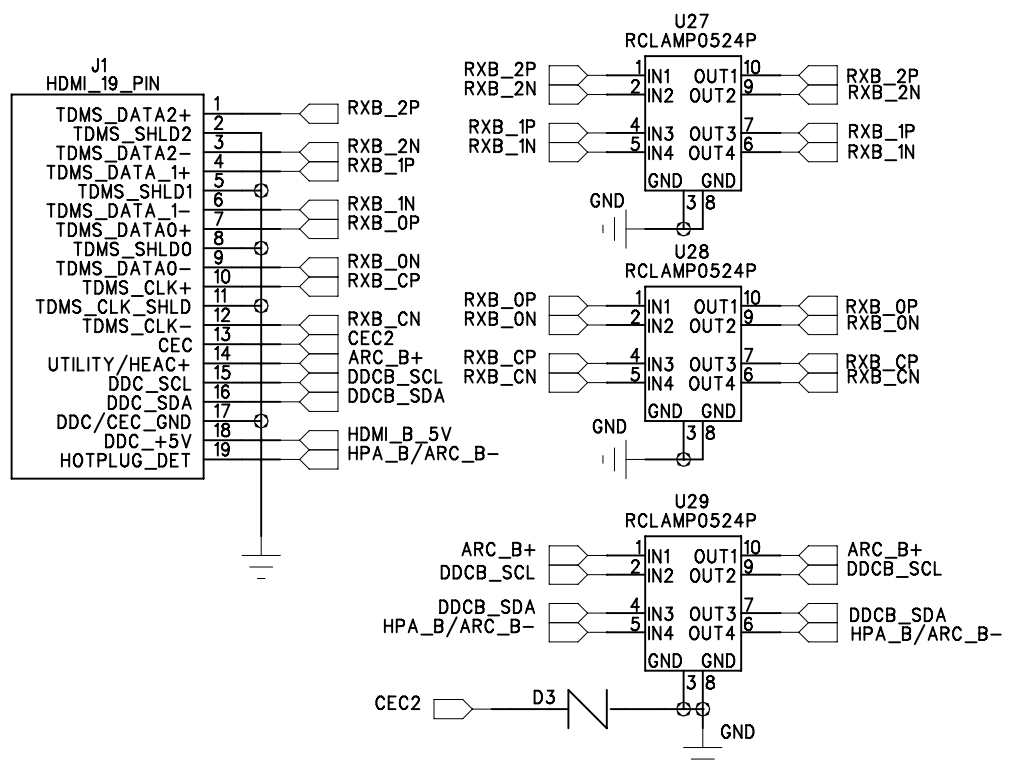
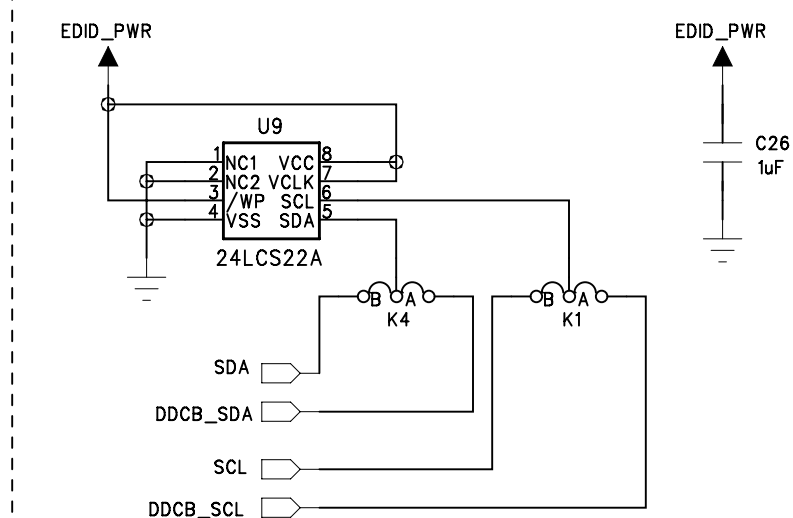
DDC B Circuitry



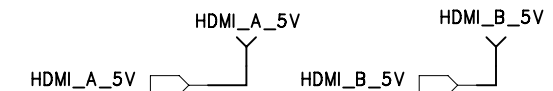
HPD B LED



EDID2



TEST POINTS



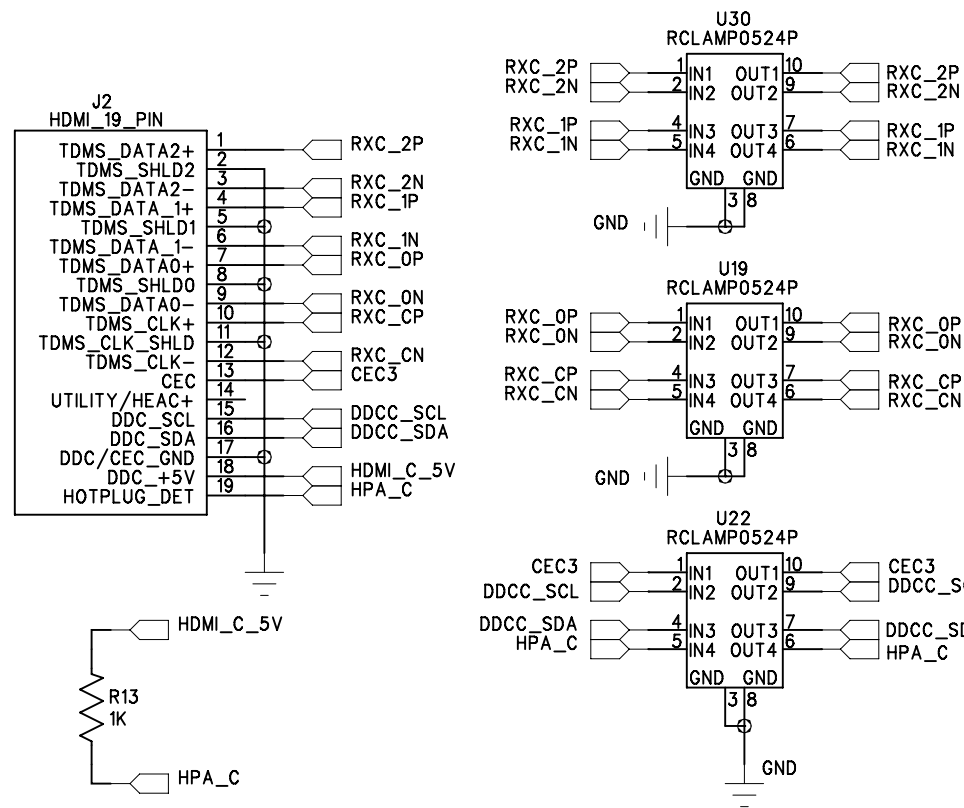
ADV7844 Input Module

Sheet: No. 3 – HDMI Inputs 1

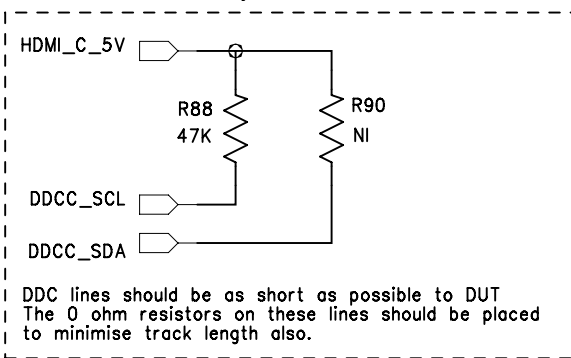
Video Applications

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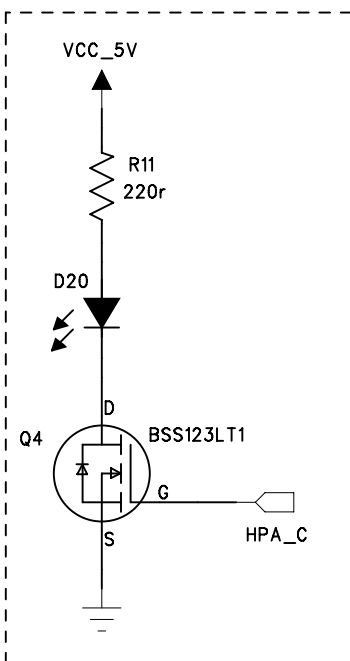
PCB Tracks for each differential pair must be tracked side by side.
Each Diff pair should have 105R impedance between each other
PCB track lengths to connector should be minimum distance



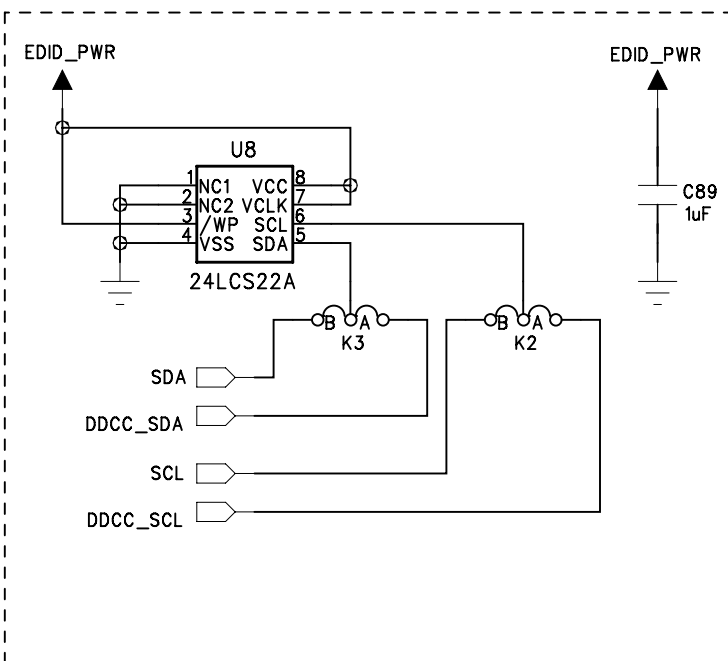
DDC C Circuitry



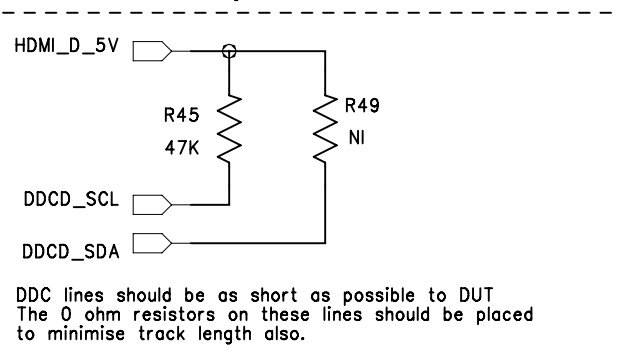
HPD C LED



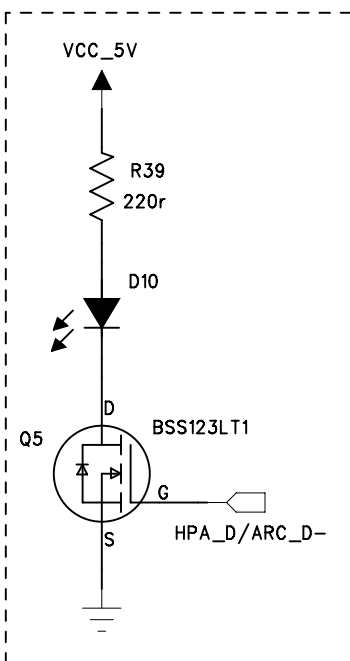
EDID3



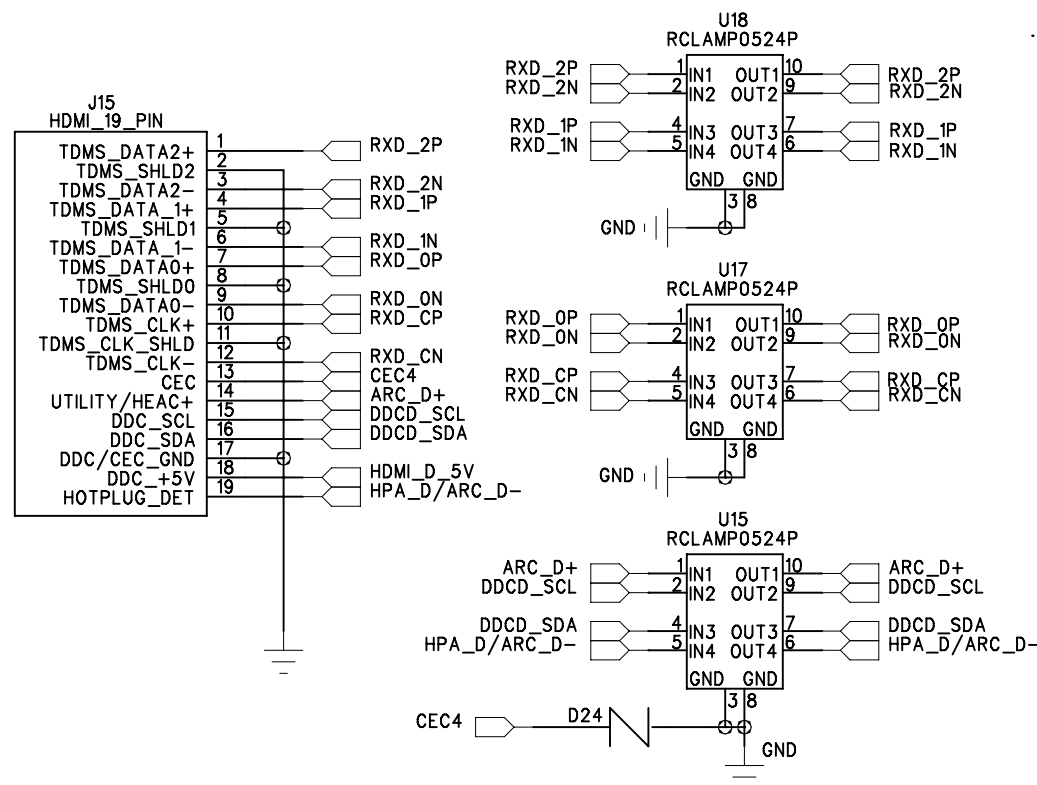
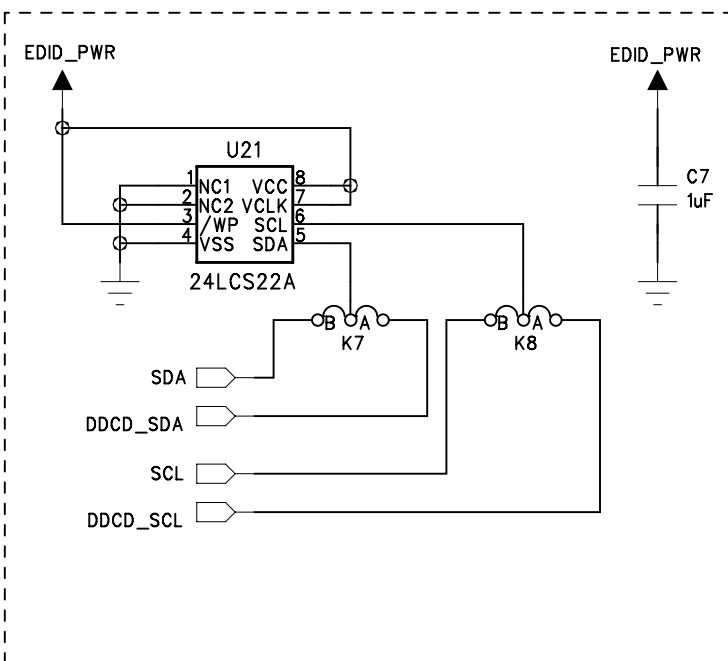
DDC D Circuitry



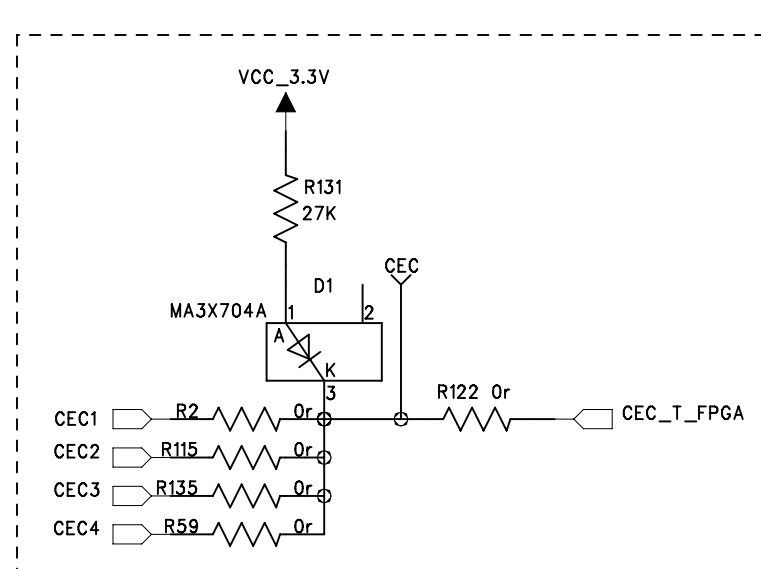
HPD D LED



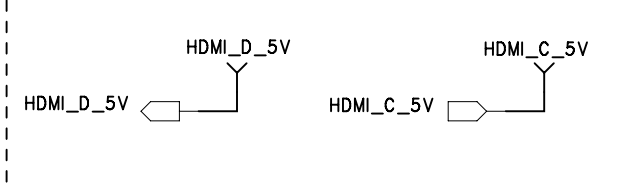
EDID4



CEC To 168-Pin Connector



TEST POINTS



ADV7844 Input Module

Sheet: No. 4 – HDMI Inputs 2

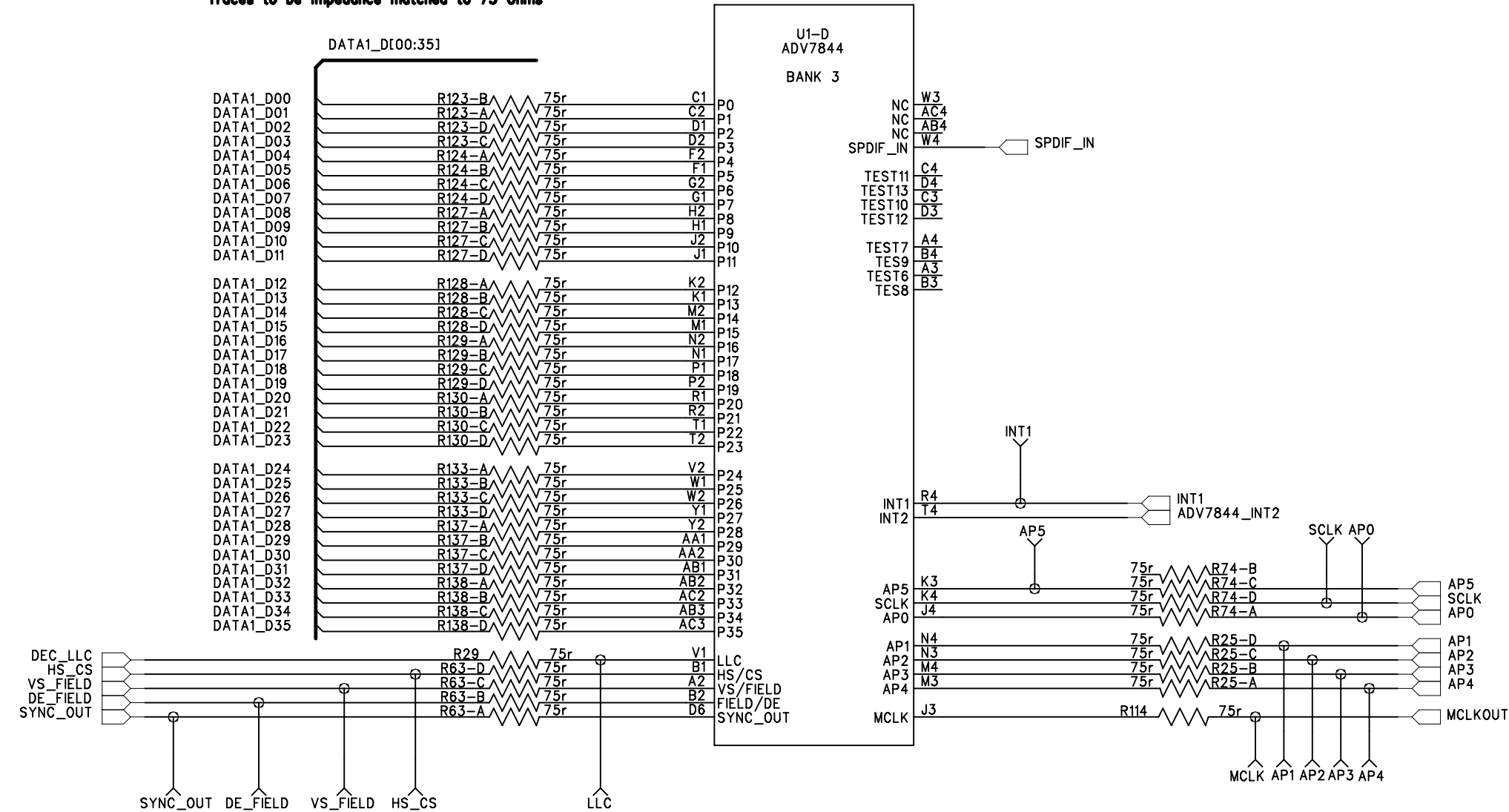
Video Applications

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PCB Tracks for each differential pair must be tracked side by side.
Each Diff pair should have 105R impedance between each other
PCB track lengths to connector should be minimum distance

PCB Tracks for each differential pair must be tracked side by side.
Each Diff pair should have 105R impedance between each other
PCB track lengths to connector should be minimum distance

Series Resistors to be placed as close to the Dut as possible
Traces to be impedance matched to 75 Ohms

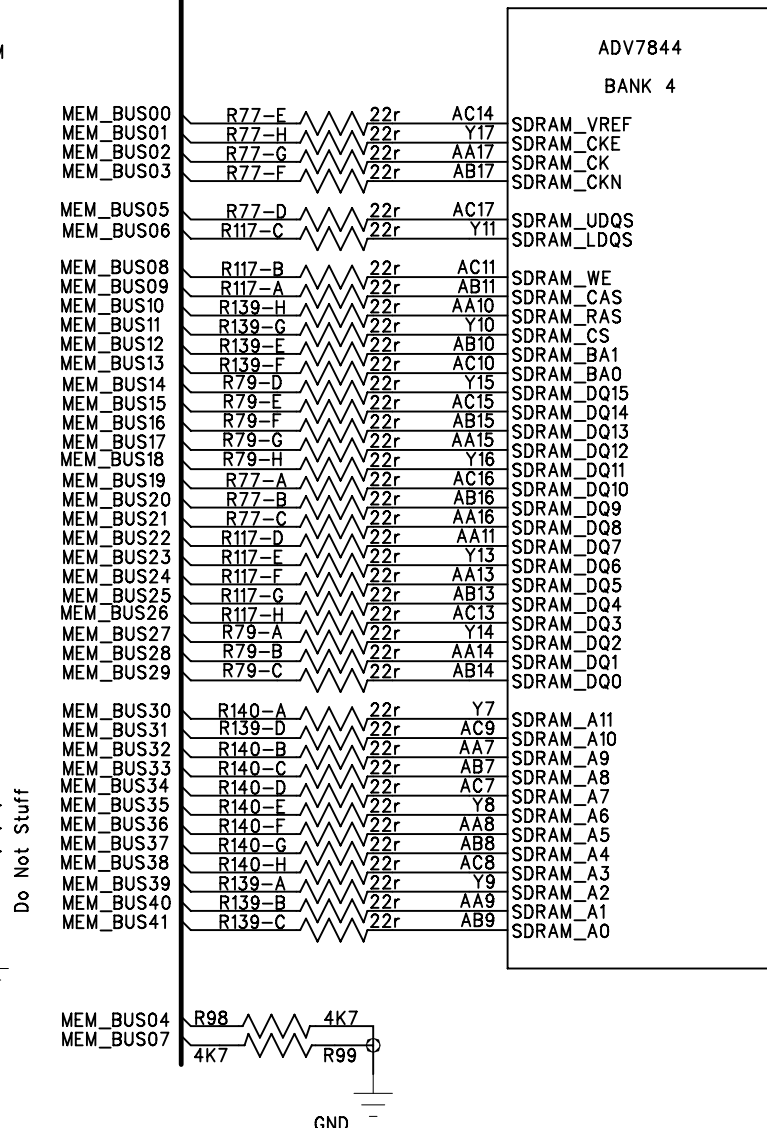
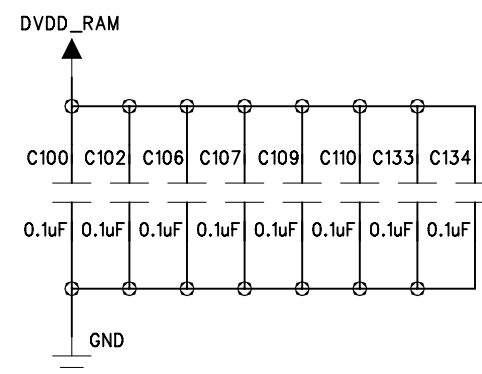


ADV7844 Input Module

Sheet: No. 6 – ADV7844 # 2

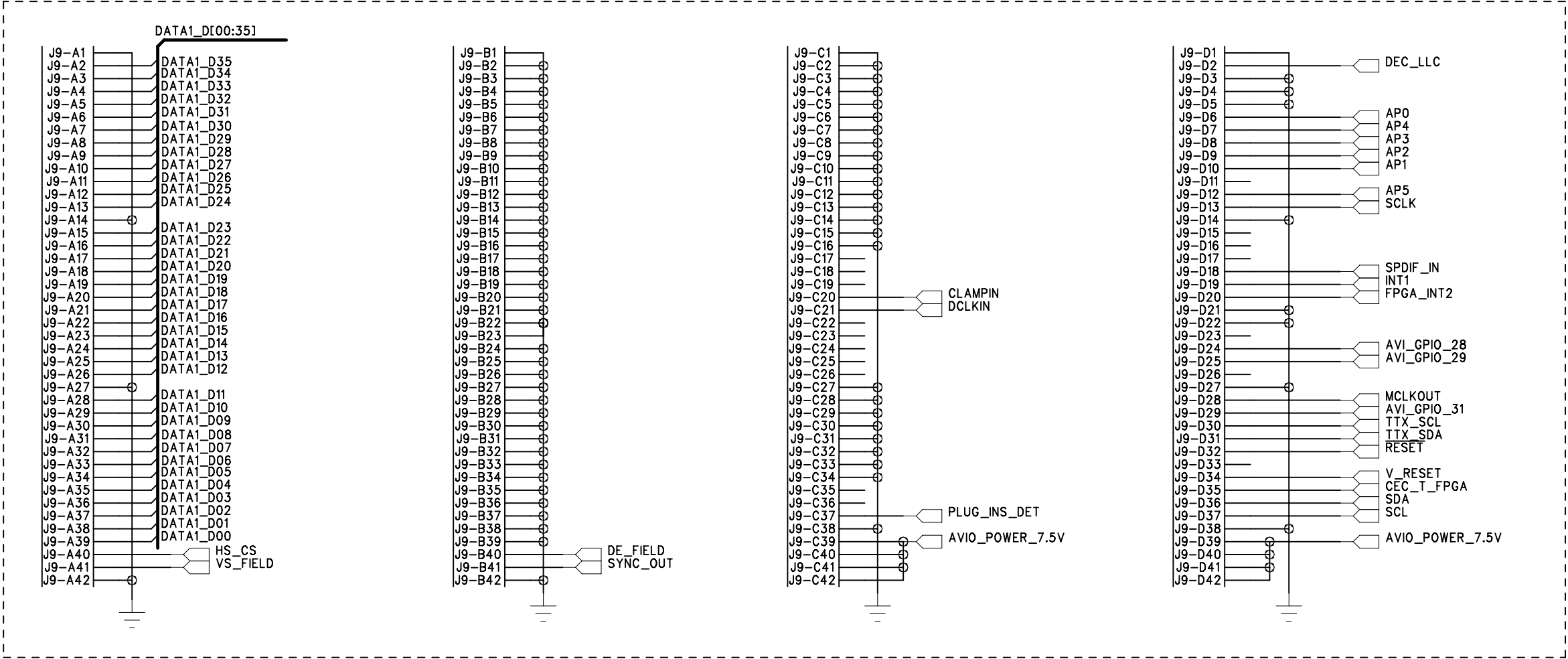
Video Applications

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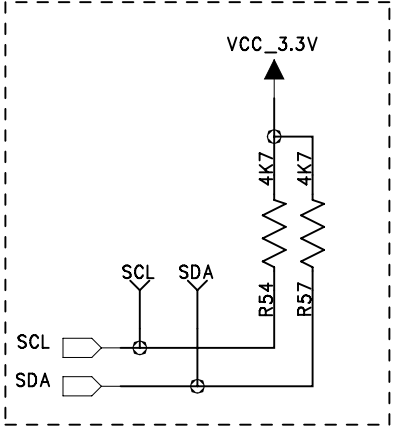


Date: July 2010 Revision: C

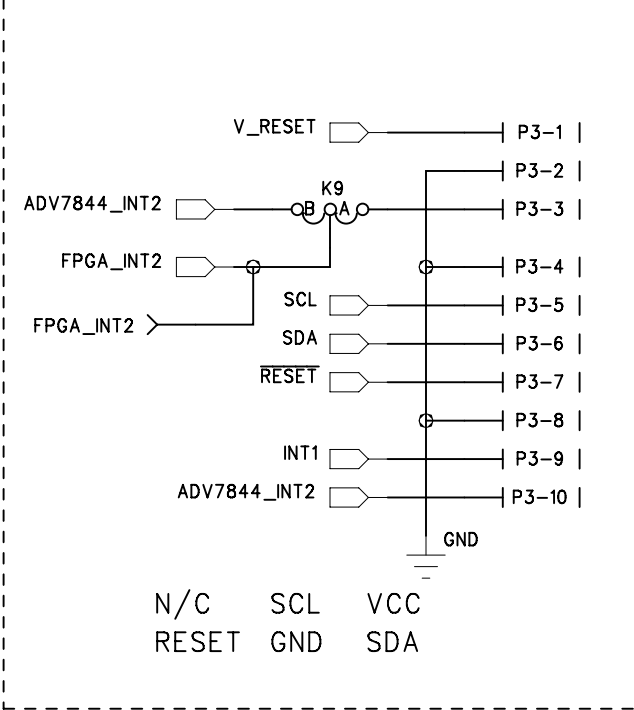
168-Pin Connector



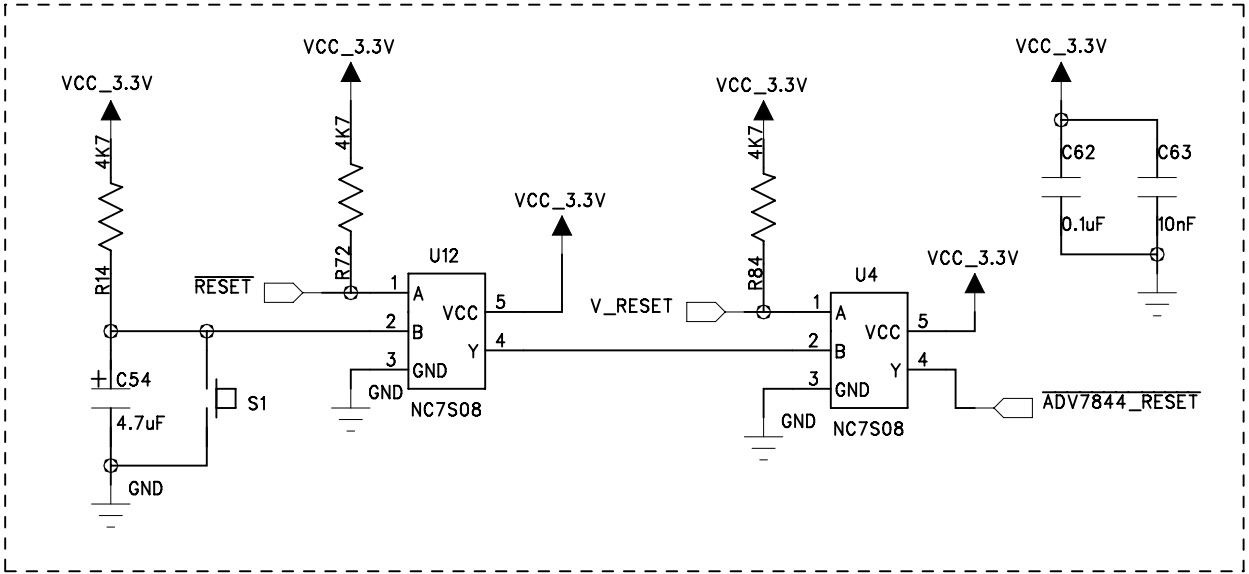
I2C Pull-Up Resistors



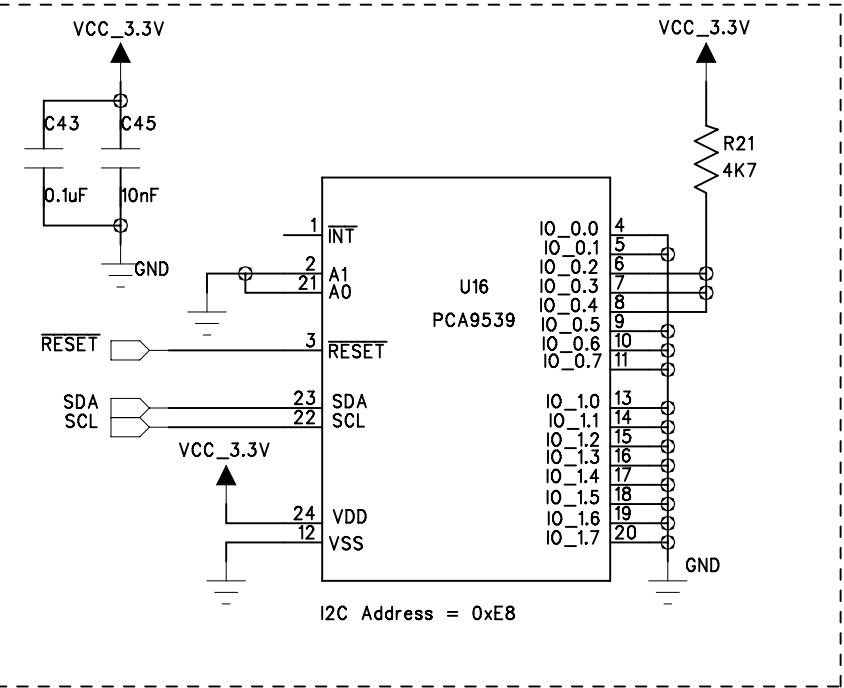
I2C Header



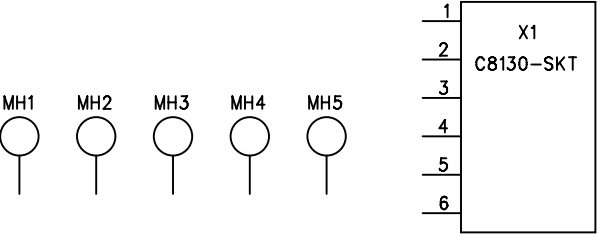
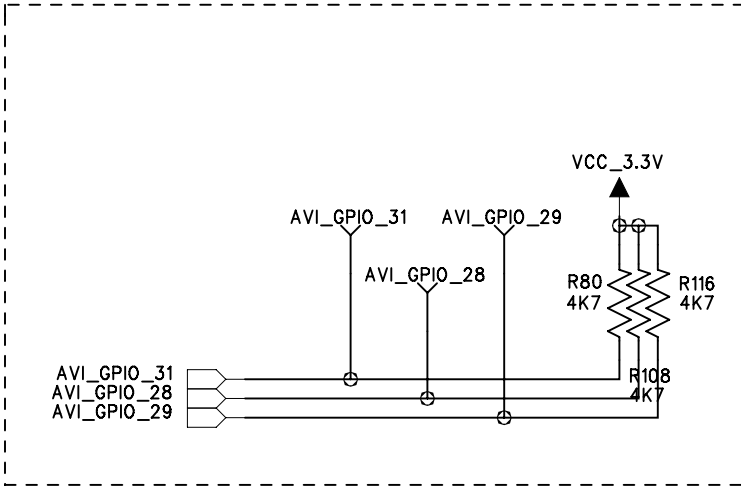
Reset Circuitry



Board Identification Bus Expander = 0x001C



GPIO



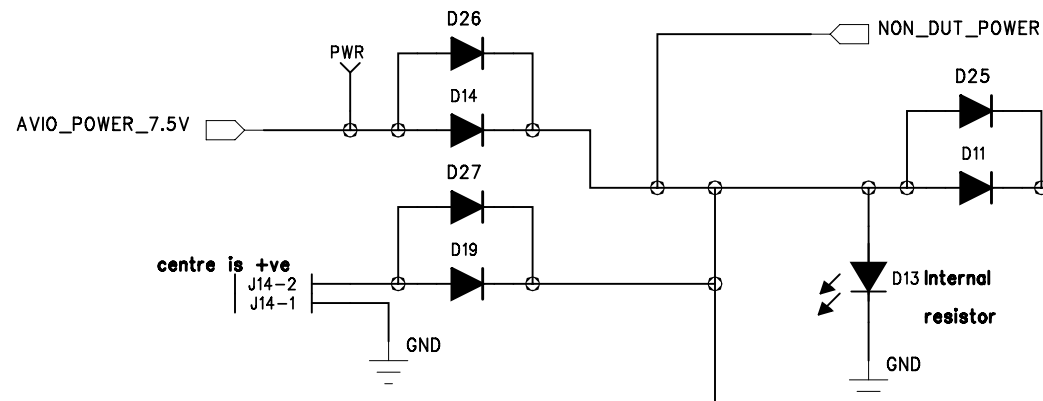
ADV7844 Input Module

Sheet: No. 9 – Connector

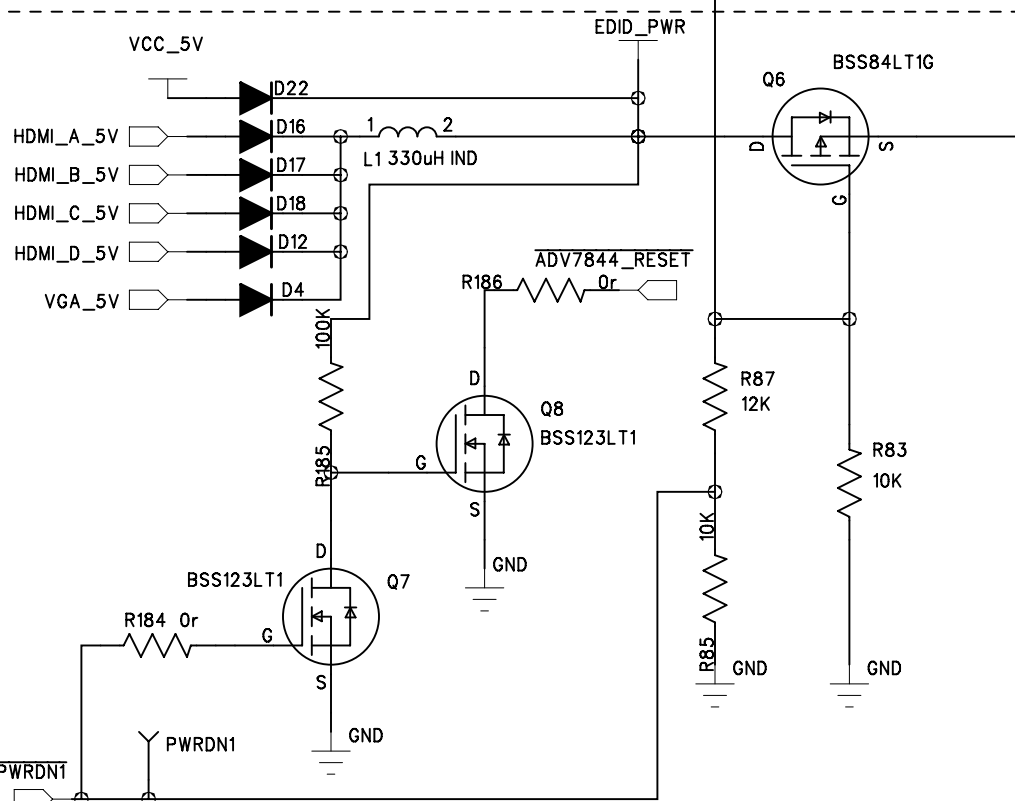
Video Applications

Date: July 2010 Revision: C

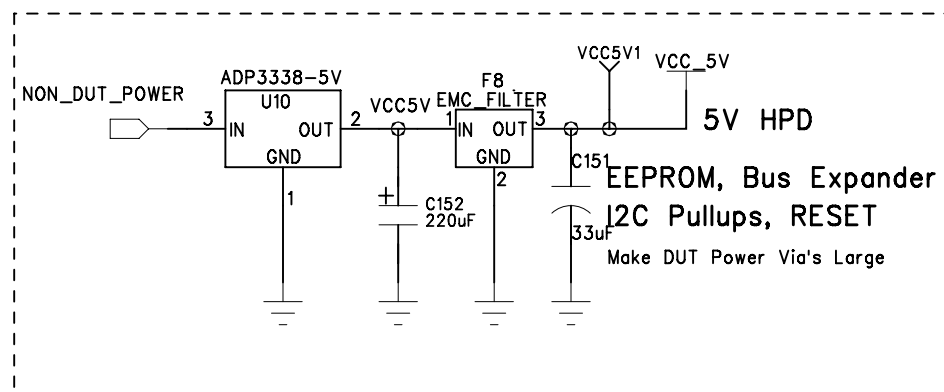
Large through Hole testpoints – keep PWR & GND together



EDID HDMI Power & Reset/Power Circuit

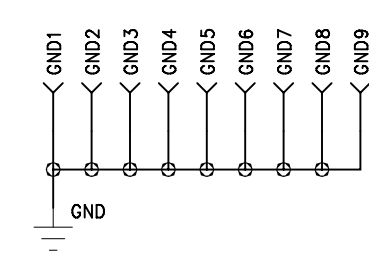


Misc. Regulators



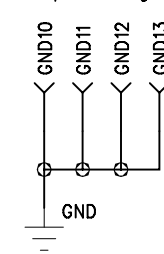
Ground Test Points 1

Distribute around the board

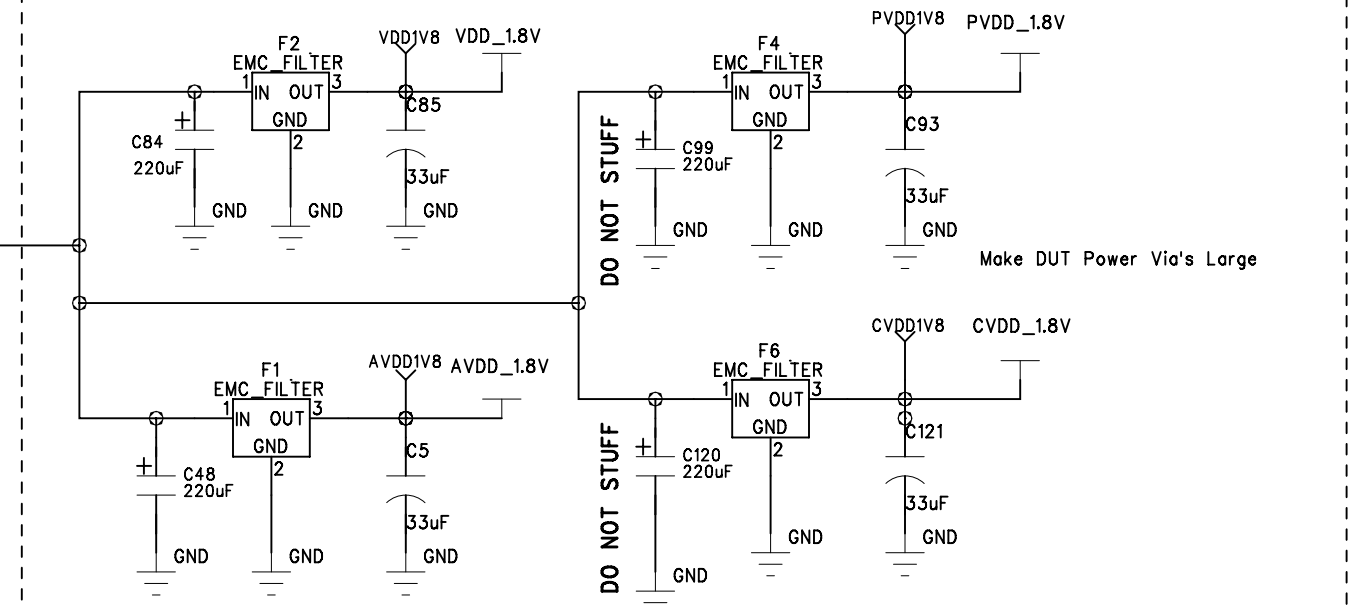
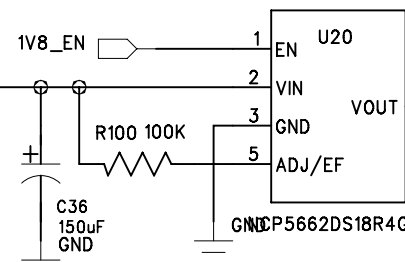


Ground Test Points 2

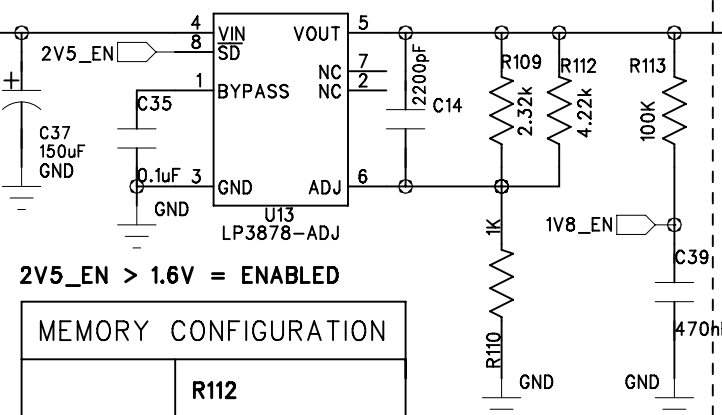
Locate close to power regulators



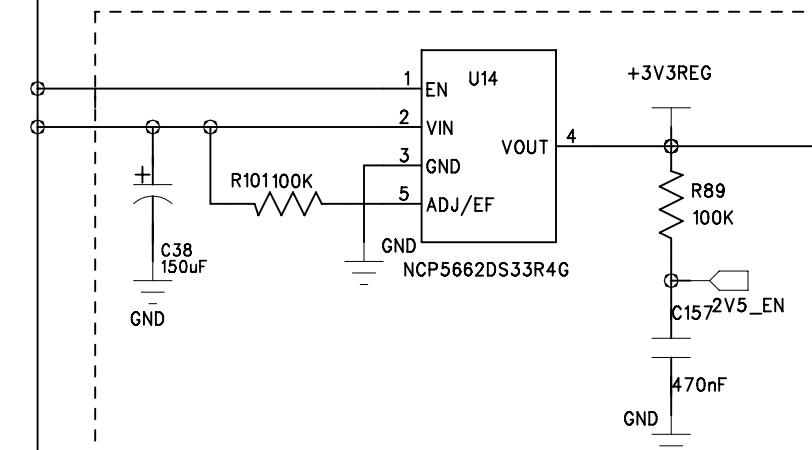
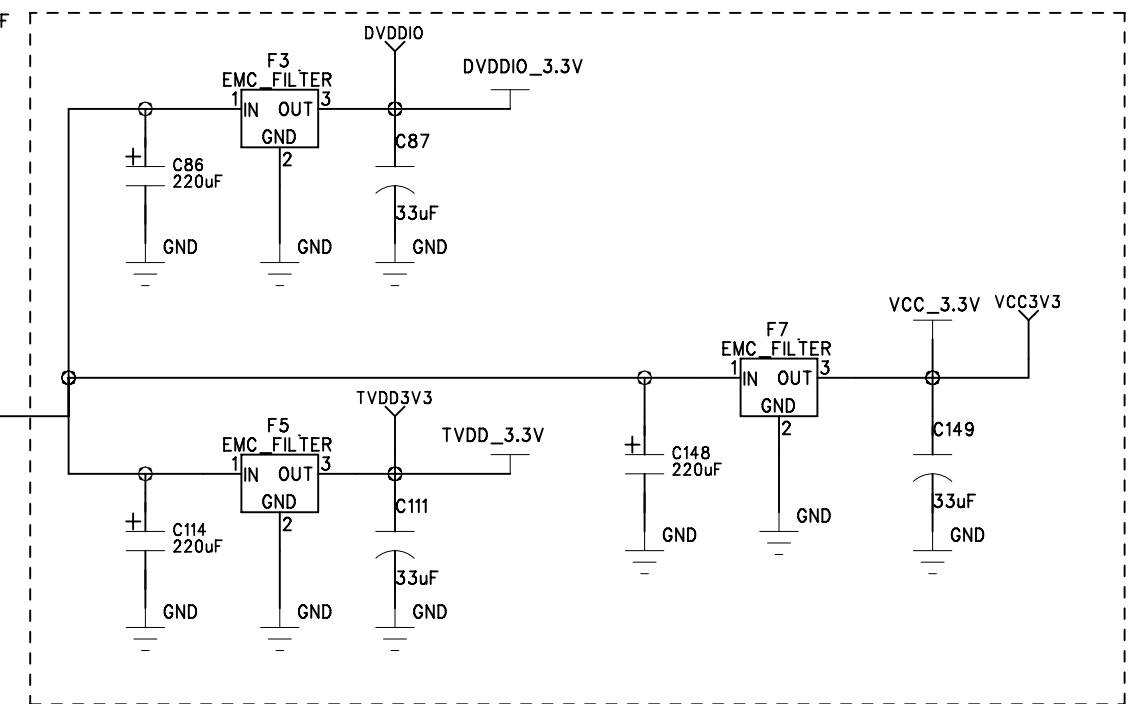
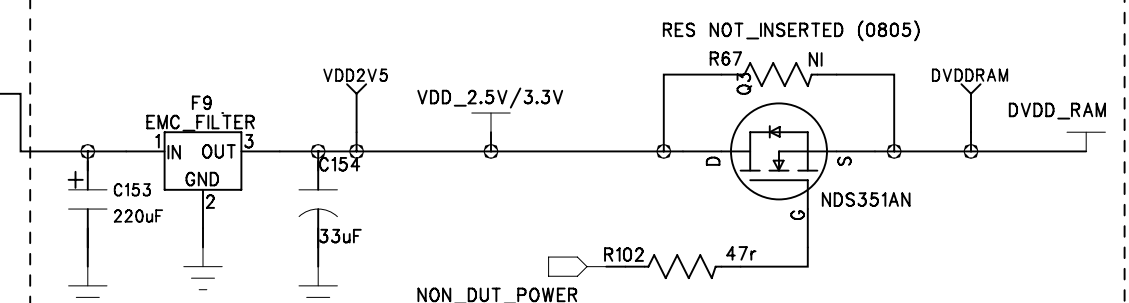
SHUTDOWN THRESHOLD:
1V8_EN > 1.3V = ENABLED
1V8_EN < 0.3V = DISABLED



PUT C117 CLOSE TO U27



MEMORY CONFIGURATION	
	R112
SDR (3V3)	DO NOT STUFF
DDR (2V5)	4.22k



ADV7844 Input Module

Sheet: No. 10 – Power Supply

Video Applications

Date: July 2010 Revision: C

