

Altera Arria V SoC Power Supply

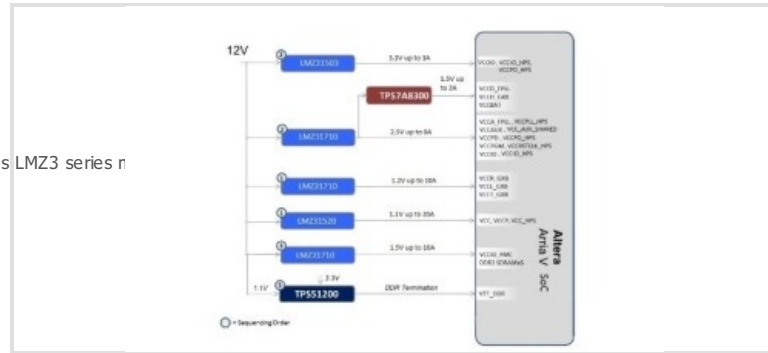
(ACTIVE) PMP9360.7

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Description

This reference design provides all the power supply rails necessary to power Altera's Arria V SoC FPGA. This design uses LMZ3 series n the FPGA.



Fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

Bill of Materials (BOM)

Find the complete list of components in this reference design.

[Download BOM](#)

TI Devices (1)

Order samples, get tools and find more information on the TI products in this reference design.

Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
TPS7A8300	2A, Low-Dropout Voltage Regulator for High Speed Communication Systems	Linear Regulator (LDO)	Sample & Buy	View Design Kits & Evaluation Modules

Technical Documents

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views
PMP9360 Test Results		PDF	2151	24 Feb 2014	291

Design Files (2)

Title	Abstract	Type	Size (KB)	Date	Views
PMP9360 BOM		PDF	104	24 Feb 2014	339
PMP9360 Schematic		PDF	1123	24 Feb 2014	386

Related Tools & Software

Software (1)

[PMP9360 Design File](#)
(ZIP, 274 KB) 118 views, 13 May 2014

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