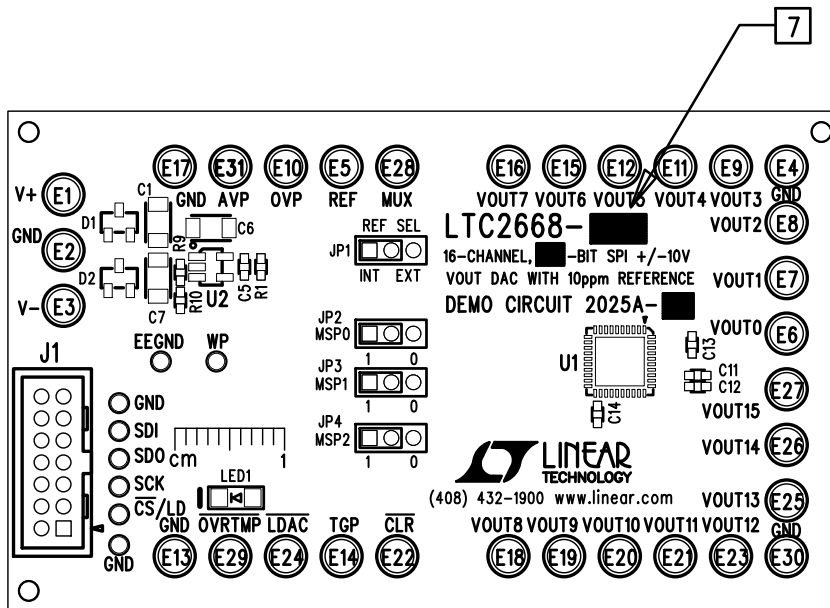



| REVISION HISTORY | | | | |
|------------------|-----|---------------|-----------|----------|
| ECO | REV | DESCRIPTION | APP. ENG. | DATE |
| - | 3 | 3RD PROTOTYPE | MARK T. | 01-09-14 |



NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. MARK EACH ASSEMBLY TYPE, SUFFIX, NUMBER OF -BIT IN THE WHITE BLOCK AREAS WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

| ASSY | U1 | SUFFIX | -BIT |
|------|------------|--------|------|
| A | LTC2668-16 | 16 | 16 |
| B | LTC2668-12 | 12 | 12 |

| APPROVALS | |  | |
|--------------|---------|---|---|
| PCB DES. | KIM T. | | |
| APP ENG. | MARK T. | TITLE: TOP ASSEMBLY DRAWING 16-CHANNEL, 16/12-BIT SPI +/-10V VOUT DAC WITH 10ppm REFERENCE | |
| | | SIZE N/A | IC NO. LTC2668CUJ-16/12 REV. 3 |
| SCALE = NONE | | FILENAME: DC2025A-3.PCB SHT 1 OF 2 | |

1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
www.linear.com
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY