


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: ISE-PLC-A0M-100	REV:	SUN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions			
PLOT NAME = TII_DAC_R10.GM2	GENERATED : 12/30/2013 2:33:09 PM	TEXAS INSTRUMENTS	

 TEXAS INSTRUMENTS	
PROJECT TITLE: ..PRJ_Title	
DESIGNED FOR: ..PRJ_Customer	
FILE NAME: ISE-PLC-A0M-100	
ENGINEER: ..PRJ_Engineer	LAYOUT BY: ..PCB_Layout
SCALE: 1.00	ALTIM DESIGNER VERSION: 10.0.0.27009

Symbol	Hit Count	Finished Hole Size	Plated	Hole Type
J	9	7.874mil (<0.2mm)	PTH	Round
H	75	12mil (<0.3048mm)	PTH	Round
I	4	12.992mil (<0.33mm)	PTH	Round
G	159	16mil (<0.4064mm)	PTH	Round
B	68	20mil (<0.508mm)	PTH	Round
E	5	23mil (<0.5882mm)	PTH	Round
C	6	40mil (<1.016mm)	PTH	Round
D	10	44mil (<1.1176mm)	PTH	Round
F	2	57.087mil (<1.45mm)	NPTH	Round
A	4	128mil (<3.2512mm)	NPTH	Round
33R Total				

Drill Table



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: ISE-PLC-A0M-100	REV:	SUN REV: Not In VersionControl	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.	ENGINEER:	LAYOUT BY:
LAYER NAME = Drill Drawing					.PRJ_Engineer	.PCB_Layout
PLOT NAME = TII DAC RI0.6DI	GENERATED : 12/30/2013 2:36:52 PM		TEXAS INSTRUMENTS		SCALE: 1.00	ALTUM DESIGNER VERSION: 10.0.0.27009