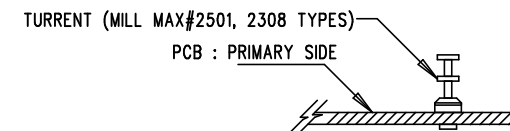


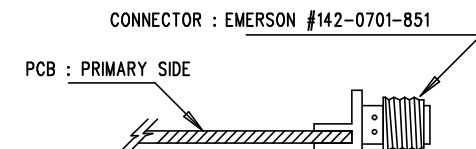
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	C. MAYOTT	01-30-14

NOTES: UNLESS OTHERWISE SPECIFIED

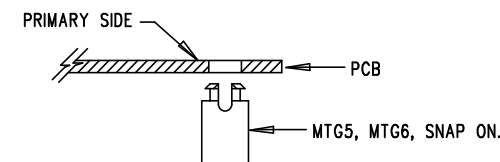
1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE REFLOW SOLDER TOP AND BOTTOM SIDES. MAXIMUM SOLDER TEMPERATURE SHALL NOT EXCEED 240 DEGREES CELSIUS.
3. OPTIONAL PARTS ARE SPECIFIED ON THE BILL OF MATERIALS. LAND PATTERNS FOR THESE PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL AT THESE LOCATIONS.
4. INSTALL SHUNTS ON JP1-JP2 AS DEPICTED.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON ALL BOARD EDGES.
6. DO NOT APPLY QA OR ASSEMBLY STAMPS TO BOARD.
7. INSTALL TURRETS E1-E12 AS SHOWN BELOW:



8. INSTALL J5, J6 SIDE-LAUNCH SMA CONNECTORS AS SHOWN BELOW:



9. INSTALL 2 STANDOFFS AT 2 LOCATIONS AS SHOWN BELOW:



10. J1-J4 AND J7-J9 SHALL BE INSTALLED ON PRIMARY SIDE.
11. J10, J11 INSTALLED ON SECONDARY SIDE.
12. MARK EACH LTC PART NUMBER AND ASSEMBLY TYPE WITH BLACK PERMANENT MARKER AS SHOWN IN TABLE BELOW:

ASSY	U1
-A	LTC2000IY-16
-B	LTC2000IY-14
-C	LTC2000IY-11

APPROVALS		<div> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY </div>		
PCB DES.	M.HAWKINS			
APP ENG.	C.MAYOTT	TITLE: ASSEMBLY DRAWING, PRIMARY SIDE		
		LTC2000 HIGH SPEED DAC		
		SIZE	IC NO. LTC2000-X FAMILY	REV.
		N/A	DEMO CIRCUIT 2085A	3
SCALE = NONE		FILENAME: DC2085A-3.PCB		SHT 1 OF 2