

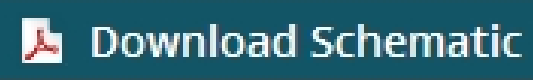
Key Document

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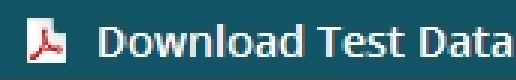
PMP9335 is designed for Xilinx Zynq FPGA applications utilizing the TPS84A20 and TPS84320. This design uses an external timer to synchronize the switching frequency to 300 kHz. It also employs a controlled power up and power down sequence.

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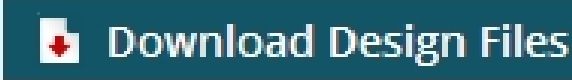
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	PMP9335.2
Vin (Min) (V)	12
Vin (Max) (V)	12
Vout (Nom) (V)	1.8
Iout (Max) (A)	3.7
Output Power (W)	6.66
Isolated/Non-Isolated	Non-Isolated
Input Type	DC
Topology	Buck- Synchronous
Designed for FPGA	Y

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Part #	Package Pins	CAD File (.bxl)	STEP Model (.stp)
LP2951	SOIC (D) 8	Download	Download
	VSSOP (DGK) 8	Download	-
LP38855	TO-220 (NDH) 5	Download	-
TPS84320	B1QFN (RUQ) 47	Download	-
TPS84A20	B3QFN (RVQ) 42	Download	-

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

Step 1: Download and install the Ultra Librarian software ZIP: 62MB

Step 2: Download the Symbol and Footprint .hxl file

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User guides (1)

Title	Abstract	Type	Size (KB)	Date	Views	Ti Recommends
 PMP9335 Test Results		PDF	1408	28 Sep 2015	15	✓

Design files (6)

Title	Abstract	Type	Size (KB)	Date	Views
PMP9335 Gerber		ZIP	299	28 Sep 2015	9
PMP9335 CAD Files		ZIP	557	28 Sep 2015	9
 PMP9335 PCB		PDF	1516	28 Sep 2015	10
 PMP9335 Assembly Drawing		PDF	168	28 Sep 2015	14
 PMP9335 BOM		PDF	121	28 Sep 2015	13
 PMP9335 Schematic		PDF	446	28 Sep 2015	35

Related Tools & Software

Reference Designs (2)

Name	Part Number	Tool Type
Multi-output Multi-buck 20W Power Supply for Xilinx Zynq FPGA Applications Reference Design	PMP9335	TI Designs
Multi-output Multi-buck 20W Power Supply for Xilinx Zynq FPGA Applications Reference Design	PMP9335.1	TI Designs

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