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The PMP10200 reference design uses the UCC28700 primary-side regulated quasi-resonant flyback controller to generate a 28V output from an DC input. The input voltage range is between 25VDC and 520VDC. It is a low cost solution because no optocoupler is needed to regulate the output voltage.

- Quasi-Resonant Flyback Topology
- Ultra-Wide Input Voltage Range
- High Efficiency
- No Need For an Optocoupler

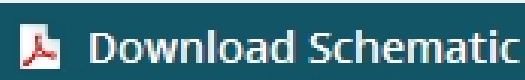
This reference design was built and tested and the test report is included

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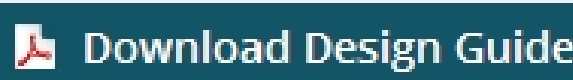


i Fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

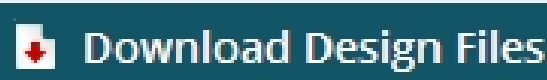
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Find the complete list of components in this reference design.

	PMP10200
Vin (Min) (V)	25
Vin (Max) (V)	520
Vout (Nom) (V)	28
Iout (Max) (A)	.715
Output Power (W)	20.02
Isolated/Non-Isolated	Isolated
Input Type	DC
Topology	Flyback- Quasi Resonant

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Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
UCC28700	Constant-Voltage, Constant-Current PWM With Primary-Side Regulation	Offline and Isolated DC/DC Controllers and Converters	Sample & Buy	View Design Kits & Evaluation Modules

Part #	Package Pins	CAD File (.bxl)	STEP Model (.stp)
UCC28700	SOT (DBV) 6	Download	Download

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

Step 1: Download and install the Ultra Librarian software [ZIP: 62MB](#)

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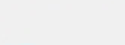
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User guides (1)

Title	Abstract	Type	Size (KB)	Date	Views	TI Recommends
PMP10200 Test Results		PDF	1195	23 Sep 2015	26	✓

Title	Abstract	Type	Size (KB)	Date	Views
PMP10200 CAD Files		ZIP	326	23 Sep 2015	10
PMP10200 BOM		PDF	133	23 Sep 2015	11
PMP10200 Schematic		PDF	139	23 Sep 2015	38

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