REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	1	REBUILD WITH CHANGE	DILIAN R.	05-28-15

U1 LTC4271

U2 LTC4270

T1 WURTH 7490100143

J1 2mm, 34 Pin Connector — SAMTEC, TMM134-01-T-S-RA

REQUIRED S1B DIODE PROTECTION SHOWN ON MOTHER BOARD. See NOTE 3

REQUIRED SURGE COMPONENTS (BULK 58V TVS and BULK CAPACITANCE) ON MOTHER BOARD.

NOTE: UNLESS OTHERWISE SPECIFIED

1, ALL SENSE RESISTORS TIE TO VEE AT ONE COMMON POINT.
2, VSSK CONNECTS TO THE COMMON SENSE RESISTOR VEE POINT THROUGH AN ISOLATED KELVIN SENSE TRACE.
3, ONE S1B PROTECTION DIODE FROM VEE TO OUTn AND ONE S1B PROTECTION DIODE FROM OUTn TO AGND SHOWN ON MOTHER BOARD.
4, ALL RESISTORS ARE IN 0603. ALL CAPACITORS ARE IN 0603.

CUSTOMER NOTICE
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	KIM T.
APP ENG.	DILIAN R.

SCALE = NONE

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LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

12-PORT PSE DAUGHTER CARD WITH DIGITAL ISOLATION

SIZE	IC NO.	REV.
N/A	LTC4270BIUKG, LTC4271IUF DEMO CIRCUIT 1682B	1

DATE: 05/28/2015, 11:48 AM SHEET 1 OF 1