18-60VDC Input Isolated Flyback Reference Design with a 54V/1.1A Output

(ACTIVE) PMP7361

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Key Document

- PMP7361 Test Results (PDF 838 KB)
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PMP7361 18-60VDC Input Isolated Flyback Reference Design with a 54V/1.1A Output Board Image

Fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

Description

PMP7361 is an isolated flyback converter with a 54V/1.1A output. The 18-60VDC input is suitable for 24VDC industrial and 48VDC telecom applications. The 54VDC output can be used to power PoE power sourcing equipment (PSE).

Features

- Efficiency of 93% with 48V input and maximum load
- Good line/load regulation with optocoupler feedback
- Wide input range
- 1500Vrms isolation for telecom or PoE
- Optional capacitors to provide hold up time

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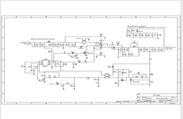
Schematic/Block Diagram

Quickly understand overall system functionality.



Download Schematic

Design Guide

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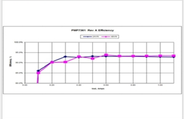

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Design Files

Download ready-to-use system files to speed your design process. Get Viewer.

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Bill of Materials (BOM)

Find the complete list of components in this reference design.

Download BOM

Parametrics

	PMP7361
Vin (Min) (V)	18
Vin (Max) (V)	60
Vout (Nom) (V)	54
Iout (Max) (A)	1.1
Output Power (W)	59.4
Isolated/Non-Isolated	Isolated
Input Type	DC
Topology	Flyback- Non Sync

TI Devices (2)

Order samples, get tools and find more information on the Ti products in this reference design.

Part Number	Name	Product Family	Sample & Buy	Design Kits & Evaluation Modules
TLV431A	Low Voltage Adjustable Precision Shunt Regulator	Voltage Reference	Sample & Buy	Not Available
UCC2809-2	Economy Primary Side Controller	Offline and Isolated DC/DC Controllers and Converters	Sample & Buy	View Design Kits & Evaluation Modules

CAD/CAE symbols

Part #	Package | Pins	CAD File (.bxl)	STEP Model (.stp)
TLV431A	SOT (DBZ) | 3	Download	Download
	TO-92 (LP) | 3	Download	Download
	SIP (PK) | 3	Download	-
	SOT (DBV) | 5	Download	Download
	SOT (DCK) | 6	Download	-
	SOIC (D) | 8	Download	Download
UCC2809-2	SOIC (D) | 8	Download	Download
	VSSOP (DGK) | 8	Download	Download
	TSSOP (PW) | 8	Download	Download

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

Step 1: Download and install the Ultra Librarian software ZIP: 62MB

Step 2: Download the Symbol and Footprint .bxl file.

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Technical Documents

User guides (1)

Title	Abstract	Type	Size (KB)	Date	Views	TI Recommends
PMP7361 Test Results		PDF	838	30 Nov 2015	42	✓

Design files (3)

Title	Abstract	Type	Size (KB)	Date	Views
PMP7361 Gerber		ZIP	270	30 Nov 2015	21
PMP7361 BOM		PDF	35	30 Nov 2015	41
PMP7361 Schematic		PDF	77	30 Nov 2015	101

Applications

- Power Over Ethernet (PoE)

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TI E2E™ community

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