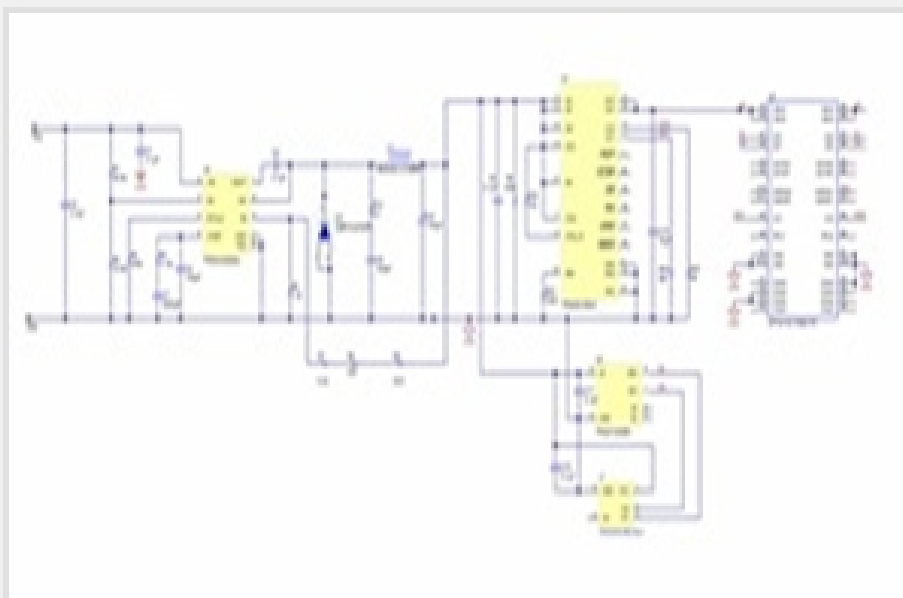


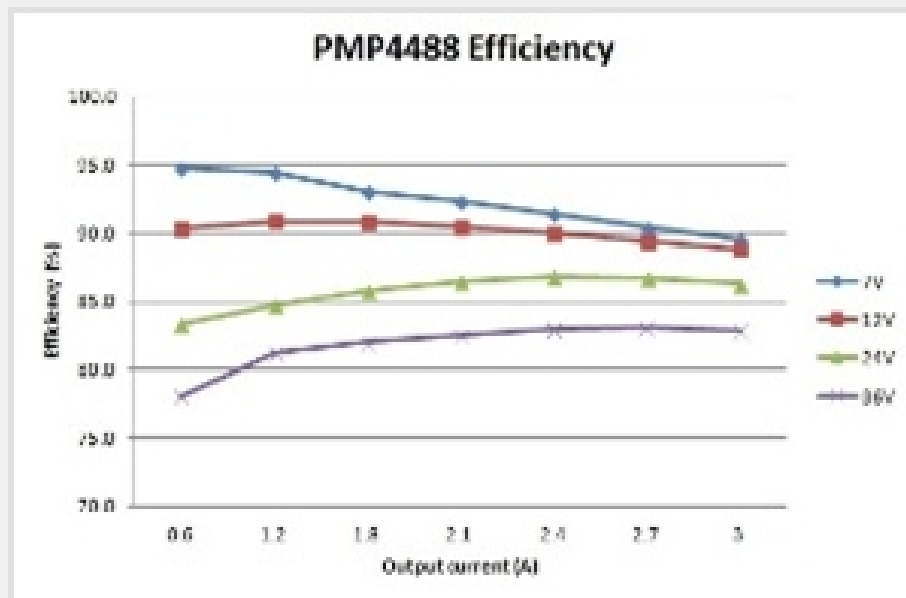
- 7~40Vin wide input range
- USB-C DFP only+BC1.2
- Non-SR solution with 2layer design reduces cost
- 89% @ 12Vin full load with good thermal performance
- Size: 43mm x 20mm x 5.6mm

 TI's Standard Terms and Conditions for Evaluation Modules apply.

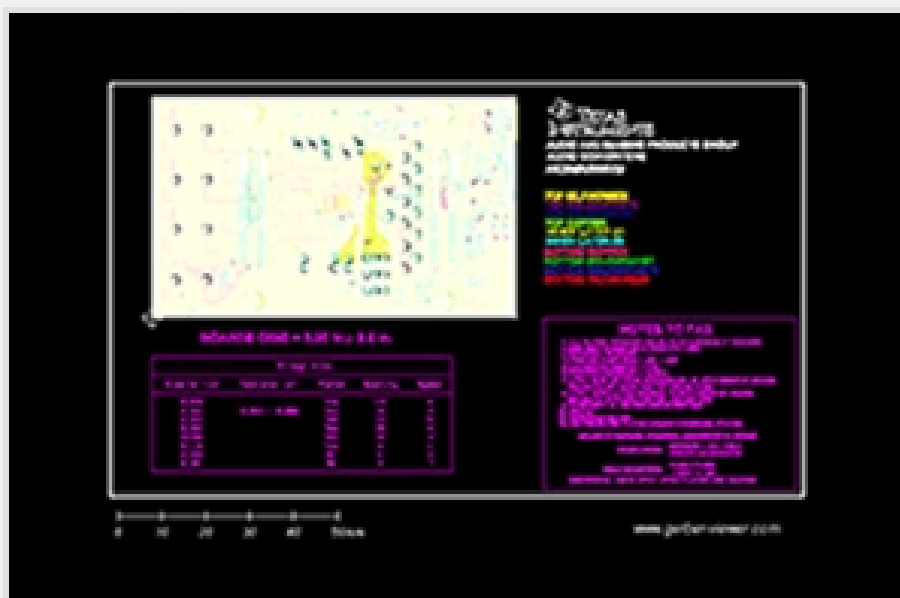
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Find the complete list of components in this reference design.



|                       | PMP4488        |
|-----------------------|----------------|
| Vin (Min) (V)         | 7              |
| Vin (Max) (V)         | 42             |
| Vout (Nom) (V)        | 5              |
| Iout (Max) (A)        | 3              |
| Output Power (W)      | 15             |
| Isolated/Non-Isolated | Non-Isolated   |
| Input Type            | DC             |
| Topology              | Buck- Non Sync |

Order samples, get tools and find more information on the TI products in this reference design.

| Part Number | Name                                                                                   | Product Family                          | Sample & Buy                     | Design Kits & Evaluation Modules                          |
|-------------|----------------------------------------------------------------------------------------|-----------------------------------------|----------------------------------|-----------------------------------------------------------|
| TPD2E001    | Low-Capacitance 2-Channel +/-15 kV ESD-Protection Array for High-Speed Data Interfaces | ESD Protection Diodes                   | <a href="#">Sample &amp; Buy</a> | <a href="#">View Design Kits &amp; Evaluation Modules</a> |
| TPS2514A    | USB Dedicated Charging Port Controller                                                 | USB Power and Charging Port Controllers | <a href="#">Sample &amp; Buy</a> | <a href="#">View Design Kits &amp; Evaluation Modules</a> |
| TPS25810    | Type C 3.0-A USB Power Supply                                                          | USB                                     | <a href="#">Sample &amp; Buy</a> | <a href="#">View Design Kits &amp; Evaluation Modules</a> |

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| Part #   | Package   Pins                                                                                    | CAD File (.bxi)          | STEP Model (.stp)        |
|----------|---------------------------------------------------------------------------------------------------|--------------------------|--------------------------|
| TPD2E001 |  SOP (DZD)   4  | <a href="#">Download</a> | -                        |
|          |  SOT (DRL)   5  | <a href="#">Download</a> | <a href="#">Download</a> |
|          |  WSON (DRS)   6 | <a href="#">Download</a> | -                        |
|          |  USON (DRY)   6 | <a href="#">Download</a> | <a href="#">Download</a> |

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

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Step 2: Download the Symbol and Footprint .bzl file.

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| Title                                                                                                                  | Abstract | Type | Size (KB) | Date        | Views | TI Recommends |
|------------------------------------------------------------------------------------------------------------------------|----------|------|-----------|-------------|-------|---------------|
|  <a href="#">PMP4488 Test Results</a> |          | PDF  | 442       | 02 Nov 2015 | 105   | ✓             |

| Title                                                                                               | Abstract | Type | Size (KB) | Date        | Views |
|-----------------------------------------------------------------------------------------------------|----------|------|-----------|-------------|-------|
| PMP4488 CAD Files                                                                                   |          | ZIP  | 318       | 02 Nov 2015 | 62    |
|  PMP4488 BOM       |          | PDF  | 197       | 02 Nov 2015 | 67    |
|  PMP4488 Schematic |          | PDF  | 272       | 02 Nov 2015 | 186   |



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