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Description

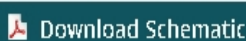
Features

- TI's [Standard Terms and Conditions for Evaluation Modules](#) apply



i Fully assembled board (shown above) developed for testing and performance validation only, not available for sale.

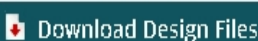
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Find the complete list of components in this reference design.

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	PMP10101
V _{in} (Min) [V]	12
V _{in} (Max) [V]	36
V _{out} (Nom) [V]	17
I _{out} (Max) [A]	.045
Output Power	.765
Isolated/Non-Isolated	Isolated
Input Type	DC
Topology	Flybuck

Order samples, get tools and find more information on the TI products in this reference design.

CAD/CAE symbols

Texas Instruments and Accelerated Designs Inc. have collaborated together to provide TI customers with schematic symbols and PCB layout footprints for TI products.

Step 1: Download and install the Ultra Librarian software [ZIP: 62MB](#)

Step 2: Download the Symbol and Footprint .bzl file.

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Technical Documents

User guides (1)

Design files (3)

Applications

- Analog Input Module

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