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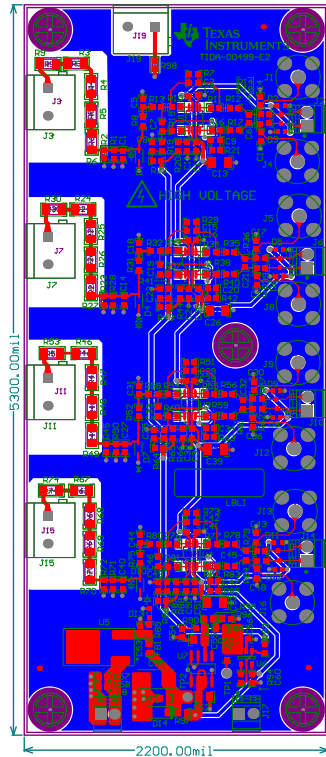
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- 221 ■ Install label in silkscreened box after final wash. Text shall be 8 pt font. Text shall be per the Label Table in the PDF schematic.
- 222 ■ These assemblies are ESD sensitive, ESD precautions shall be observed.
- 223 ■ These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- 224 ■ These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.  
ASSEMBLY VARIANTS (No Variations)

ESTABLISHED TYPICAL CLASSIC "400" COMMON 875000000  
(Symbol/Ref ID) (PARTS/UNIT)

ALTERNATE WORK SHEETED FROM TOP SIDE	BOARD #:	TIDA-00499	REV:	E2	SVN:	REV: Not In Version Control
LAYER NAME =	Top Layer	Bottom Layer	Top Layer	Bottom Layer	Top Layer	Bottom Layer
Plot Name =	Multi-Layer Composite Print	GENERATED:	9/22/2015	10:02:57 AM	TEXAS INSTRUMENTS	

Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Layer Stack Up Detail for: TIDA-00499			
Layer Name	Series	Copper Thickness	Dielectric Name/ID
Top Solder Mask	(.075)		Solder Resist
Top Layer	(.075)	1.4mil	FR4
Bottom Layer	(.075)	1.4mil	FR4
Bottom Solder Mask	(.075)		Solder Resist

DESIGN INFORMATION	
BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)	
2200MIL	X 5300MIL
Number of Layers: 2	
MIN. TRACK WIDTH: 8 MIL	
MIN. CLEARANCE: 7.87MIL	
MIN. VIA PAD SIZE: 32 MIL	
MINIMUM ANNULAR RING 0.186mm (7.345MIL) EXTERNAL	
PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- .5 MIL, HOLES +/- .3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408 <input type="checkbox"/> FR-4 High Tg <input checked="" type="checkbox"/> OTHER FR4	
THICKNESS: <input checked="" type="checkbox"/> 63 MIL (1.6mm) +/-10% <input type="checkbox"/> OTHER	
TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/-	
BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/-	
COPPER THICKNESS (FINISHED):	
OUTER: <input checked="" type="checkbox"/> 1.4MIL (1oz) <input type="checkbox"/> 2MIL (1.4oz) <input type="checkbox"/> 2.8MIL (2oz)	
INNER SIGNAL: <input type="checkbox"/> 1.4MIL (1oz) <input type="checkbox"/> 2.8MIL (2oz) <input type="checkbox"/> N/A	
DRILLING:	
REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC DRILL FILES	
PTH MIN COPPER THICKNESS: <input checked="" type="checkbox"/> 1MIL <input type="checkbox"/> OTHER	
BOARD FINISH:	
SILKSCREEN: <input checked="" type="checkbox"/> TOP <input type="checkbox"/> BOTTOM	
SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER	
SOLDER RESIST COLOR:	
<input checked="" type="checkbox"/> GREEN <input type="checkbox"/> BLUE <input type="checkbox"/> OTHER	
SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENERPIG	
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> OTHER	
ARRAY/PANEL:	
<input type="checkbox"/> CUT AND TRM PER MECH LAYER 1	
<input type="checkbox"/> N.C. ROUTE <input checked="" type="checkbox"/> V. SCORE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3	
<input checked="" type="checkbox"/> UL 94V-0 <input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER	
ADDITIONAL REQUIREMENTS: VIA TENTING: YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>	
MICROSECTION: <input type="checkbox"/> YES <input type="checkbox"/> IMPEDANCE CONTROL: YES <input type="checkbox"/> NO <input checked="" type="checkbox"/>	
BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER	
MANUFACTURER'S UL: <input type="checkbox"/> RAL <input type="checkbox"/> METAL <input checked="" type="checkbox"/> SILK	



PROJECT TITLE:	
AFE Transient Capture	
DESIGNED FOR:	
Public Release	
FILE NAME:	
TIDA-00499_E2.PcbDoc	
ENGINEER:	LAYOUT BY:
Sreenivas	Anur adha
ALTIM DESIGNER VERSION:	
10.0.0.22084	
SCALE: 1.00	

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