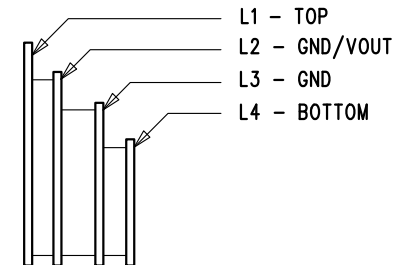


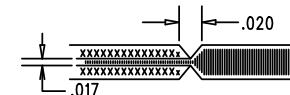
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	REBUILD WITH CHANGE	SY	08-24-15

## LAYER STRUCTURE

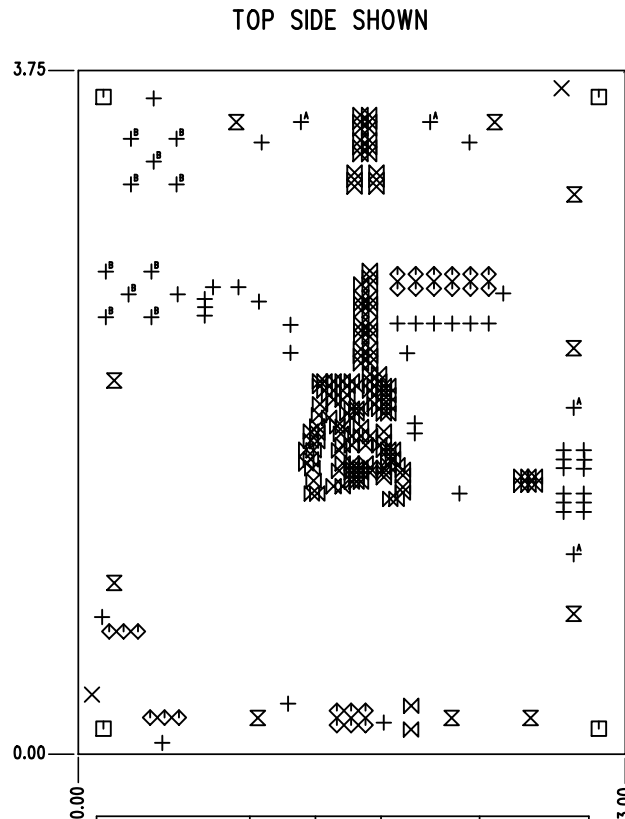


## NOTES: UNLESS OTHERWISE SPECIFIED


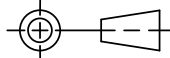
- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- DESIGN HAS SOLDER MASK DEFINED PADS ON U1 WITH 0.029" PAD, 0.025" SOLDER MASK, AND 0.024" SOLDER PASTE.  
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.



SIZE	QTY	SYM	PLATED	TOL
0.008	151	⊗	YES	+/-0.003
0.02	39	+	YES	+/-0.003
0.036	24	◇	YES	+/-0.003
0.056	10	+ <sup>B</sup>	YES	+/-0.003
0.072	2	⊗	NO	+/-0.003
0.095	10	⊗	YES	+/-0.003
0.187	4	□	NO	+/-0.005
0.205	4	+ <sup>A</sup>	YES	+/-0.005

UNLESS OTHERWISE SPECIFIED		APPROVALS			
DIMENSIONS ARE IN INCHES		PCB DES.	LT		
TOLERANCES:		APP ENG.	SY	TITLE: FABRICATION DRAWING	
0.XX = ± 0.01				HIGH EFFICIENCY, 10A STEP-DOWN $\mu$ MODULE	
0.XXX = ± 0.005				REGULATOR	
INTERPRET DIM AND TOL				SIZE	IC NO.
PER ASME Y14.5M-1994				N/A	LTM4648EY/LTM4649EY
THIRD ANGLE PROJECTION					DEMO CIRCUIT 1856B
		SCALE = NONE		FILENAME: DC1856B-1.PCB	REV 1
				SHT 1 OF 1	

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