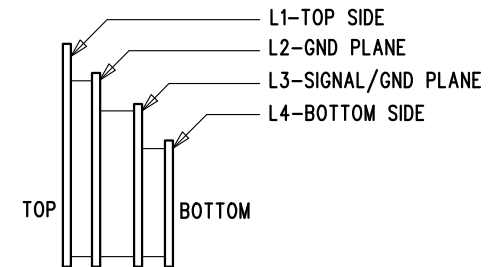


SIZE	QTY	SYM	PLATED	TOL
0.01	57		YES	+/-0.003
0.012	15		YES	+/-0.003
0.07	2		NO	+/-0.003
0.095	6		YES	+/-0.003
0.187	4		YES	+/-0.005
0.21	5		YES	+/-0.005

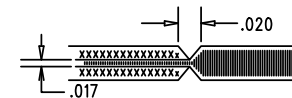
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	DB	09-28-15

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062+/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
O/O ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED		APPROVALS			
DIMENSIONS ARE IN INCHES		PCB DES.	LT		
TOLERANCES:		APP ENG.	DB	TITLE: FABRICATION DRAWING	
0.XX = ± 0.01				HIGH VOLTAGE SWITCHING SURGE STOPPER	
0.XXX = ± 0.005				SIZE IC NO. LTC7860EMSE	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				N/A DEMO BOARD 2392A	
THIRD ANGLE PROJECTION				REV 2	
		SCALE = NONE		FILENAME: DC2392A-2.PCB	
				SHT 1 OF 1	

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