NOTES (UNLESS OTHERWISE SPECIFIED):

1. THIS DRAWING SPECIFIES THE REQUIREMENTS FOR A PRINTED WIRING BOARD IN ACCORDANCE WITH SPECIFICATION IPC-A-600 CLASS 2 (LATEST REVISION).

2. THE PWB MUST BE LEAD FREE ASSEMBLY PROCESS COMPATIBLE AND MUST BE ABLE TO HANDLE A MINIMUM OF 5 CYCLES AT 260 DEGREES CELSIUS FOR 10 SECONDS.

3. BASE MATERIAL - LAMINATE AND PREPREG SHALL MEET IPC-4101B-26, 83 or 98
   Tg - MUST BE GREATER THAN OR EQUAL TO 150 DEGREES CELSIUS.
   Td - MUST BE GREATER THAN OR EQUAL TO 330 DEGREES CELSIUS.

4. COPPER FOIL WEIGHT - SEE STACKUP DETAIL 'A'

5. CHARACTERISTIC IMPEDANCE - REFER TO DETAIL B

6. MINIMUM CONDUCTIVE WIDTH/SPACING TO BE .005"/.006

7. PLATING FINISH - BOTH SIDES ENIG (ELECTROLESS NICKEL IMMERSION GOLD):
   .05080-.232 MICRON (2-8 MICROINCH) OF GOLD OVER
   2.540-6.350 MICRON (100-250 MICROINCH) OF NICKEL.

8. ALL THROUGH HOLE VIAS MAY BE PLATED SHUT.

9. SOLDERMASK - ORANGE COLOR LPI SOLDERMASK BOTH SIDES.
   MODIFICATION OF SOLDERMASK IS NOT ALLOWED WITHOUT WRITTEN PERMISSION FROM FREESCALE.

10. SILKSCREEN - WHITE EPOXY INK, BOTH SIDES. NO SILK ON PADS.

11. ELECTRICAL TEST - 100% IPCD356.

12. PRINTED WIRING BOARD IS TO BE INDIVIDUALLY BAGGED.

13. DRC'S MUST BE RUN ON THE GERBER BEFORE BUILDING BOARDS, UNLESS PRIOR APPROVAL IS GIVEN IN WRITING BY FREESCALE.

14. TEARDROPS MAYBE ADDED AT THE FAB HOUSE TO ALL SIGNAL LAYERS.

15. 2 SOLDER SAMPLES TO BE PROVIDED.

16. BASIC GRID INCREMENT AT 1:1 IS .0001.

17. SUPPLIER MARKINGS - ON SOLDER SIDE ONLY, WHERE SHOWN.
   - MUST BE UL RECOGNIZED AND MUST HAVE AN ID THAT CONFORMS TO UL94V-0

18. THE PWB WILL BE MARKED AS LEAD FREE BY USE OF AN INK STAMP (pb)

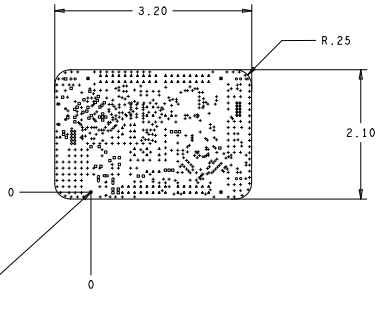
19. THE PWB WILL BE MARKED AS LEAD FREE PROCESS COMPATIBLE BY USE OF AN INK STAMP (260°C)

20. ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP. ALL HOLE LOCATION TOLERANCES ARE TO BE +/-.002 IN REFERENCE TO THE PRIMARY DATUM.

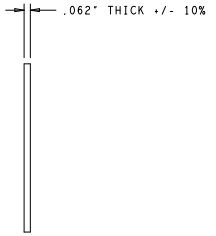
21. FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURERS REQUIREMENTS. THE ADDITION OF RAILS AND .125" NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER.PANELIZATION MUST BE APPROVED BY CONTRACT MANUFACTURER.

22. .
   INTENTIONAL SHORT IN SH1 BETWEEN NETS P3V3 AND V_TGTMCU.
   INTENTIONAL SHORT IN J5 BETWEEN NETS SDA_RST_TGTMCU_B AND SDA_RST_TGTMCU_J_B
   INTENTIONAL SHORT IN J8 BETWEEN NETS SWD_CLK_TGTMCU_BUF AND SWD_CLK_TGTMCU.
   INTENTIONAL SHORT IN J11 BETWEEN NETS SWD_CLK_TGTMCUU AND SWD_CLK_KV10.
   INTENTIONAL SHORT IN J12 NETS SWD_DIO_TGTMCU_BUF AND SWD_DIO_TGTMCU.
   INTENTIONAL SHORT IN J19 BETWEEN P3V3 AND P3V3_REG.
   INTENTIONAL SHORT IN J20 BETWEEN P3V3 AND P3V3_KV10.

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	A	ORIGINAL RELEASE	09/04/15	P. SUSTEK
	B			
	C			
	D			
	E			

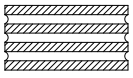


PRIMARY DATUM
GRID ORIGIN



DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
▪	10.0	+2.0/-10.0	PLATED	516
▫	12.0	+2.0/-12.0	PLATED	52
▲	31.0	+2.0/-2.0	PLATED	20
▴	40.0	+2.0/-2.0	PLATED	85
●	28.0	+2.0/-2.0	NON-PLATED	6
◆	63.0	+2.0/-2.0	NON-PLATED	2
■	125.0	+3.0/-3.0	NON-PLATED	4
▪	34.0x30.0	+2.0/-2.0	PLATED	2
▬	59.0x33.0	+2.0/-2.0	PLATED	2
▭	91.0x32.0	+3.0/-3.0	PLATED	1
▯	111.0x32.0	+3.0/-3.0	PLATED	2

layers	Single ended		Differential(USB all nets)		
	Trace spac (mils)	Impedance (Ohms)	Trace Width (mils)	Trace spacing (mils)	Impedance (Ohms)
TOP	6	52 +/-10	6.00	7.00 mils	90 +/-10
Int_1	6	54 +/-10	5.20	7.00 mils	90 +/-10



SCALE: NONE

DETAIL A LAYER STACKUP
Theoretically Thickness 63mils

	FINISHED Cu WEIGHT
LAYER 1 COMPONENT SIDE	1.50 oz
Prepreg 4.20 mils	
LAYER 2 GROUND PLANE	1 oz.
Core 6.00 mils	
LAYER 3 POWER PLANE	1 oz.
Prepreg 45.0 mils	
LAYER 4 SOLDER SIDE	1.50 oz.

___ PUBI (PUBLIC INFORMATION)
X FIUO (FREESCALE INTERNAL USE ONLY)
___ FCP (FREESCALE CONFIDENTIAL PROPRIETARY)

THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO FREESCALE AND SHALL NOT BE USED FOR ENGINEERING DESIGN PROCUREMENT OR MANUFACTURE IN WHOLE OR IN PART WITHOUT THE CONSENT OF FREESCALE.

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ARE:
DECIMALS ANGLES
XX .01 ±1-30'
XXX .005
✓ RMS ALL MACHINED SURFACES
BREAK ALL SHARP EDGES AND CORNERS. REMOVE BURRS.
UNDERLINED DIM. NOT TO SCALE.
THIRD ANGLE ORTHOGRAPHIC PROJECTION IS USED.

APPROVALS	DATE
DRAWN AVID	09/04/15
CHECKED P. SUSTEK	09/04/15
DESIGN ENGINEER P. SUSTEK	09/04/15

PART NO. 170-28718

FREESCALE
6501 WILLIAM CANNON DRIVE WEST AUSTIN, TEXAS 78735 USA

TITLE: PRINTED WIRING BOARD
FRDM-KV10Z

SIZE	CAD FILE NAME	DWG. NO.	REV
D	LAY-28718	FAB-28718	A

SCALE 1/1 DO NOT SCALE DRAWING SHEET 1 OF 1